

Mandarinfish

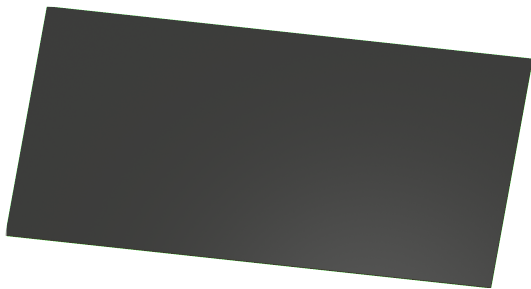
Variant: DRAFT

2025-12-07

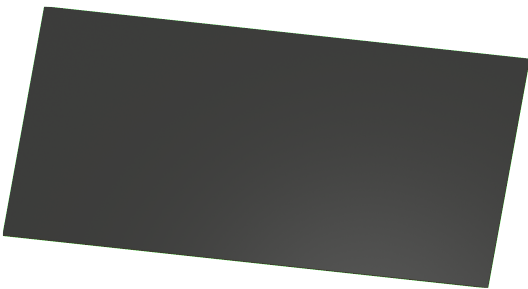
Rev + (Unreleased)

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TOP VIEW



BOTTOM VIEW



NOTES

Comment

Not fitted components are marked as **X**

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 07-Dec-2025

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for
informational design
notes.

DESIGN NOTE:
Example text for
debug notes.

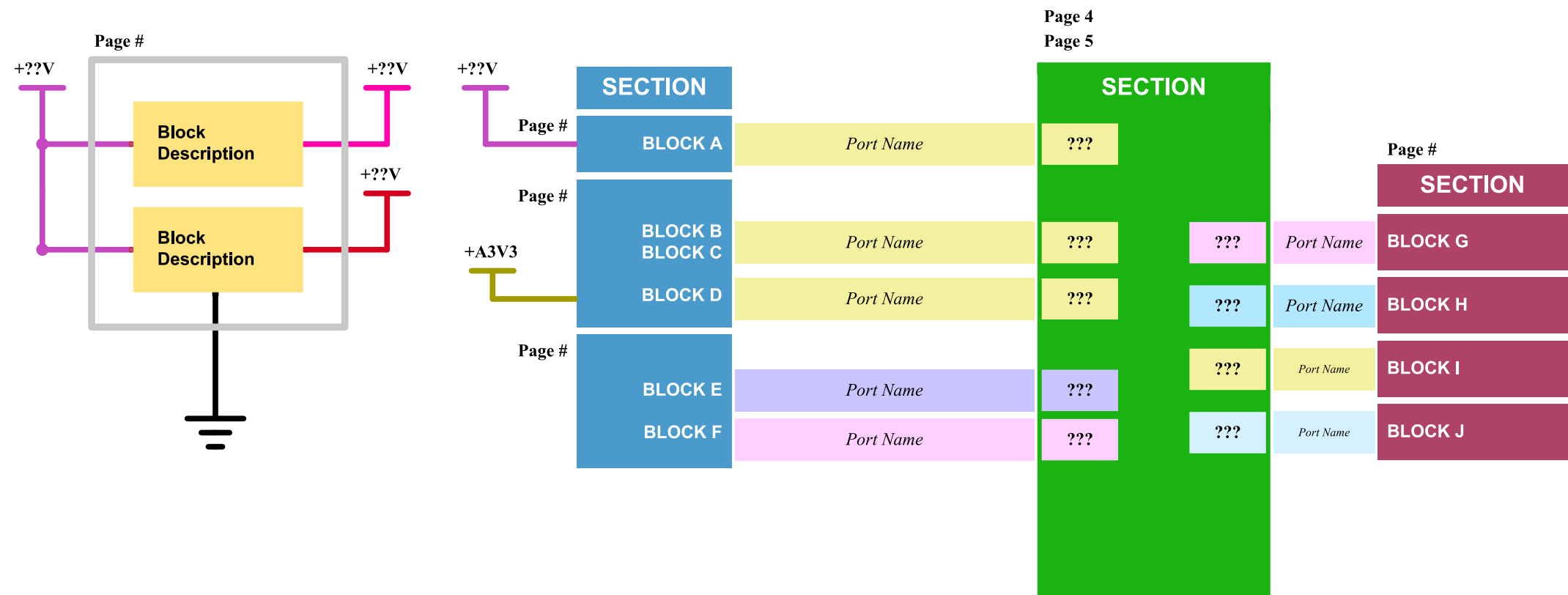
DESIGN NOTE:
Example text for
cautionary design
notes.

DESIGN NOTE:
Example text for
critical design
notes.

LAYOUT NOTE:
Example text for
critical layout
guidelines.

	Comments:	Company: Company Name	Variant: DRAFT	Git Hash: b33dce6
	Sheet Title:	Board Name: Mandarinfish	Project Name: OpenTamagochi	
	Sheet Path: /	File Name: OpenTamagochiHardware.kicad_sch	Designer: Enrique Ramirez	Date: 2025-01-12
			Revision: + (Unreleased)	
			Size: A3	Sheet: 1 of 7

[2] Block Diagram



Target specifications:	
Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

	Comments:	Company: Company Name		Variant: DRAFT	Git Hash: b33dce6
		Board Name: Mandarinfish		Project Name: OpenTamagochi	
	Sheet Title: Block Diagram	File Name: Block Diagram.kicad_sch	Designer: Enrique Ramirez	Date: 2025-01-12	Revision: + (Unreleased)
Sheet Path: /Block Diagram/			Reviewer:	Size: A3	Sheet: 2 of 7

[3] Project Architecture

Description

Section A - Title A

Page 4

File: Section A - Title A.kicad_sch

Section B - Title B

Page 5

File: Section B - Title B.kicad_sch

Description

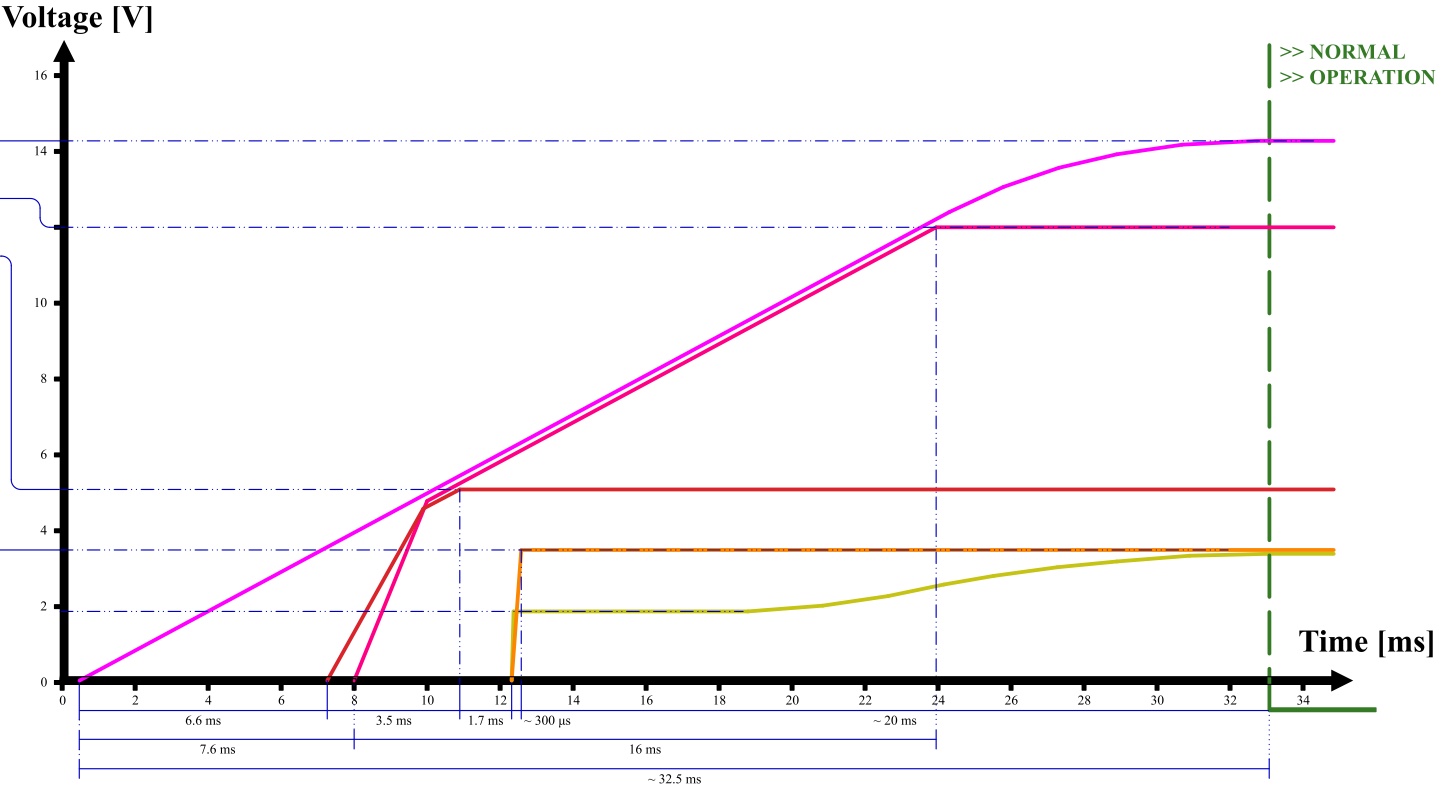
	Comments:	Company: Company Name		Variant: DRAFT	Git Hash: b33dce6
		Board Name: Mandarinfish		Project Name: OpenTamagochi	
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: Enrique Ramirez	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Project Architecture/		Reviewer:	Size: A3	Sheet: 3 of 7

[4] Sheet Title A					
<div><div>LAYOUT NOTE: blablabla</div><div>DESIGN NOTE: blablabla</div><div>Block Description</div></div>					
		Comments: AN5346 STM32G474 Datasheet p.81 J. Pieper ADC investigation		Company: Company Name	
		Sheet Title: Sheet Title A		Variant: DRAFT	
		Sheet Path: /Project Architecture/Section A - Title A/		Git Hash: b33dce6	
				Project Name: OpenTamagochi	
		File Name: Section A - Title A.kicad_sch		Board Name: Mandarinfish	
		Designer: Enrique Ramirez		Date: 2025-01-12	
		Reviewer:		Revision: + (Unreleased)	
		Size: A4		Sheet: 4 of 7	

[illegible]

[6] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



	Comments:	Company:		Variant:		Git Hash:	
		Company Name		DRAFT		b33dce6	
		Board Name:			Project Name:		
		Mandarinfish			OpenTamagochi		
	Sheet Title:	File Name:	Designer:	Date:	Revision:		
Power - Sequencing	Power - Sequencing.kicad_sch	Enrique Ramirez	2025-01-12	+ (Unreleased)			
Sheet Path:		Reviewer:	Size:	Sheet:			
/Power - Sequencing/			A4	6 of 7			

[7] Revision History

	Comments:	Company: Company Name		Variant: DRAFT	Git Hash: b33dce6
		Board Name: Mandarinfish		Project Name: OpenTamagochi	
	Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: Enrique Ramirez	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Revision History/		Reviewer:	Size: A4	Sheet: 7 of 7