

Board Name

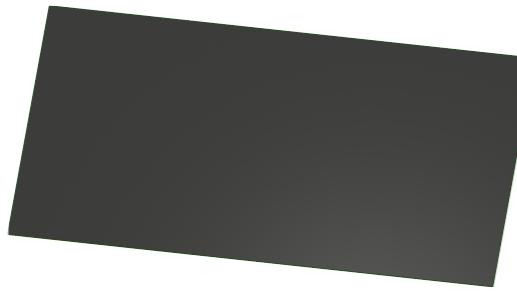
2025-12-03

Variant: DRAFT

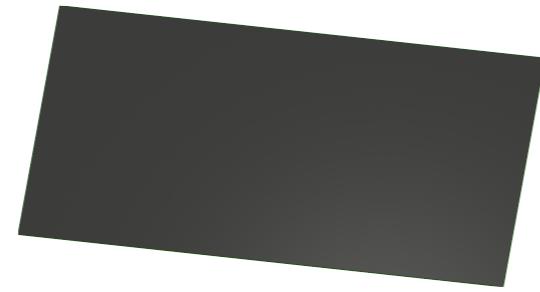
Rev + (Unreleased)

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TOP VIEW



BOTTOM VIEW



NOTES

Comment

Not fitted components are marked as

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 03-Dec-2025

DESIGN CONSIDERATIONS

DESIGN NOTE:  
Example text for informational design notes.

DESIGN NOTE:  
Example text for debug notes.

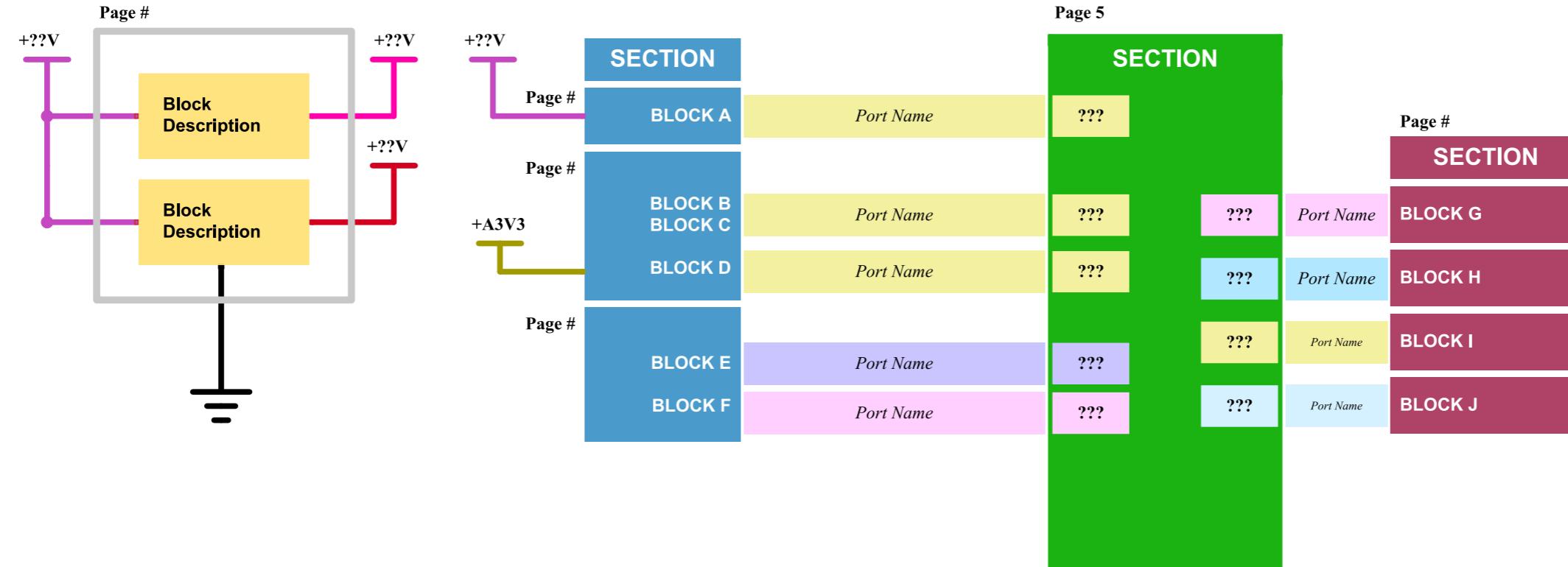
DESIGN NOTE:  
Example text for cautionary design notes.

DESIGN NOTE:  
Example text for critical design notes.

LAYOUT NOTE:  
Example text for critical layout guidelines.

	Comments:	Company: Company Name	Variant: DRAFT	Git Hash: e0f712d
	Board Name: <b>Board Name</b>	Project Name: <b>Project Name</b>		
	Sheet Title: File Name: KDT_Hierarchical_KiBot.kicad_sch	Designer: Author	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /	Reviewer:	Size: <b>A3</b>	Sheet: <b>1 of 7</b>

# [2] Block Diagram

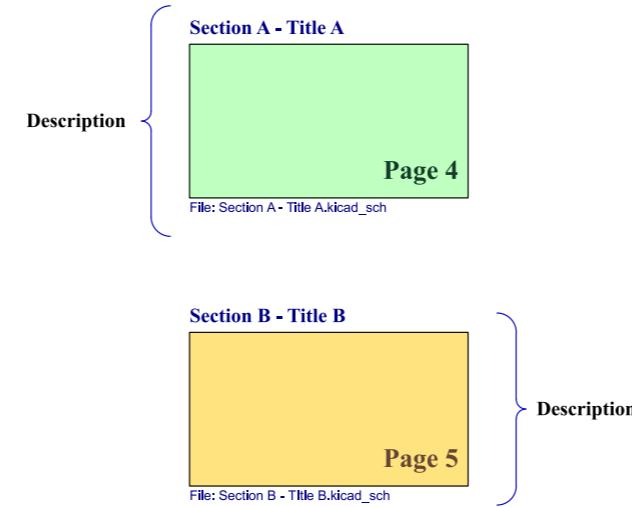


## Target specifications:

Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

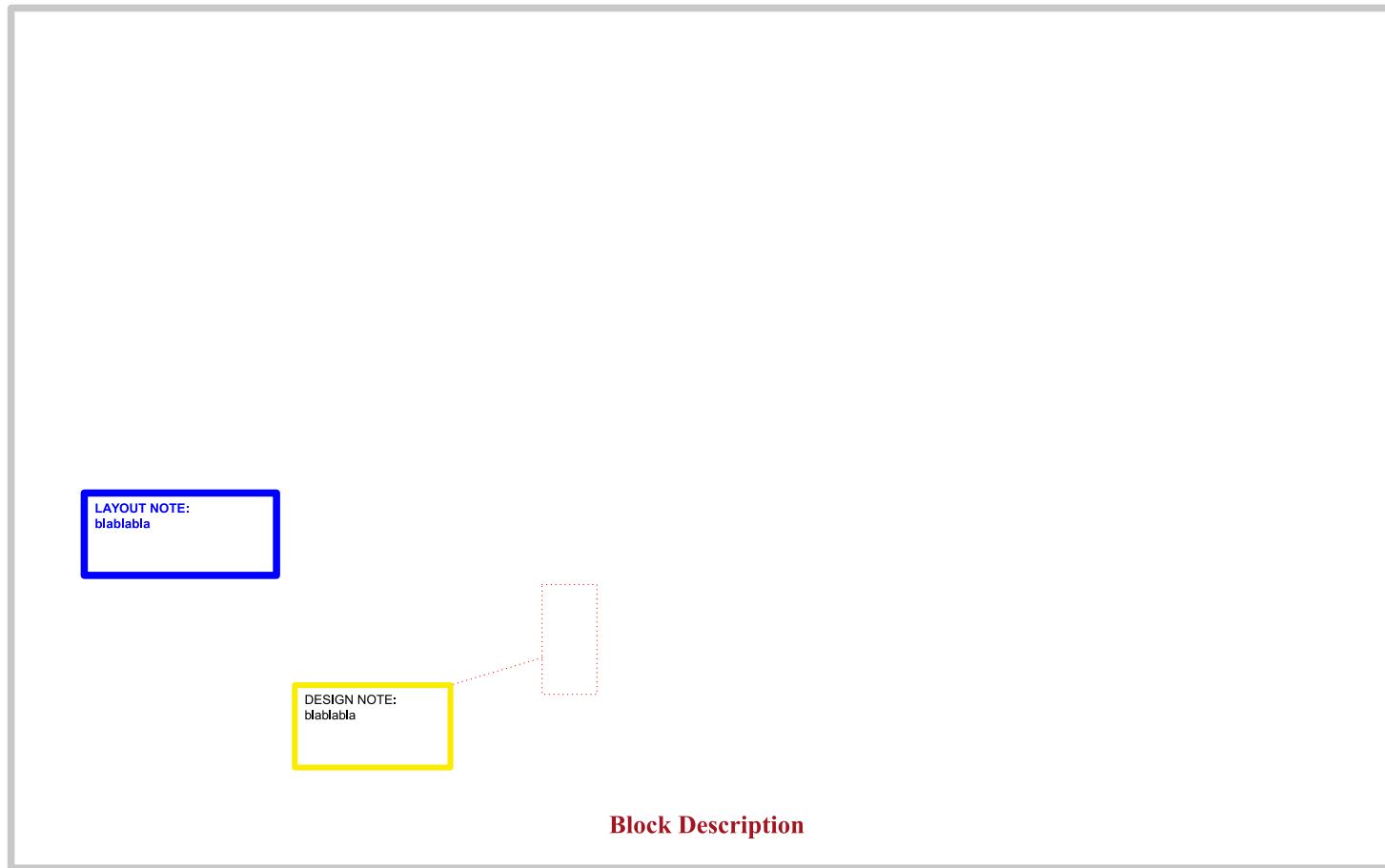
	Comments:	Company: Company Name	Variant: DRAFT	Git Hash: e0f712d
	Board Name: <b>Board Name</b>	Project Name: <b>Project Name</b>		
	Sheet Title: Block Diagram	File Name: Block Diagram.kicad_sch	Designer: Author	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Block Diagram/	Reviewer:	Size: <b>A3</b>	Sheet: <b>2 of 7</b>

# [3] Project Architecture



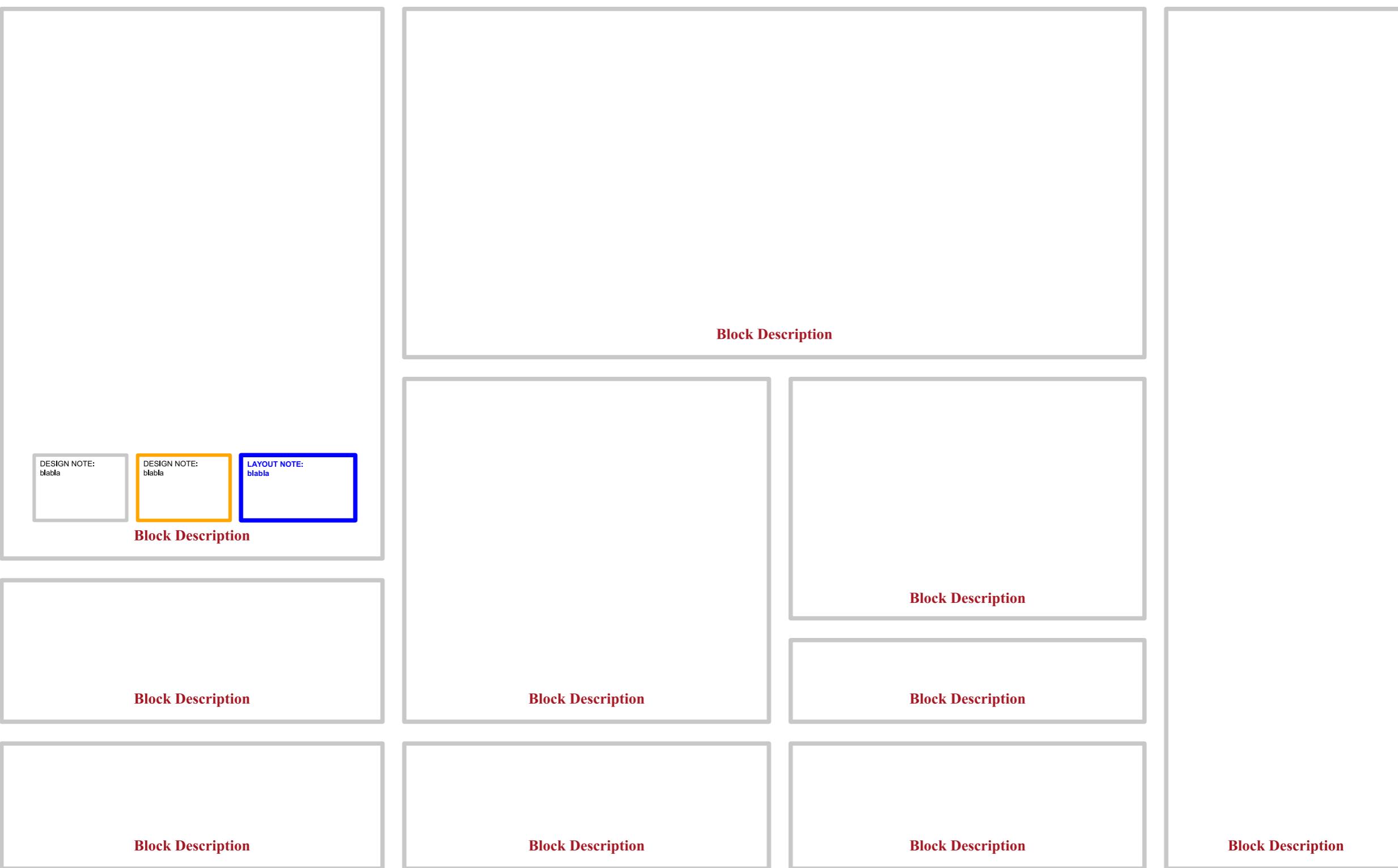
	Comments:	Company: Company Name	Variant: DRAFT	Git Hash: e0f712d
		Board Name: <b>Board Name</b>	Project Name: <b>Project Name</b>	
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: Author	Date: 2025-01-12 Revision: + (Unreleased)
	Sheet Path: /Project Architecture/	Reviewer:	Size: <b>A3</b>	Sheet: <b>3 of 7</b>

# [4] Sheet Title A



		Comments: AN5346 STM32G474 Datasheet p.81 J. Pieper ADC investigation	Company: Company Name	Variant: DRAFT	Git Hash: e0f712d
		Sheet Title: Sheet Title A	Board Name: <b>Board Name</b>		Project Name: <b>Project Name</b>
		File Name: Section A - Title A.kicad_sch	Designer: Author	Date: 2025-01-12	Revision: + (Unreleased)
		Sheet Path: /Project Architecture/Section A - Title A/		Reviewer: 	Size: <b>A4</b> Sheet: <b>4</b> of <b>7</b>

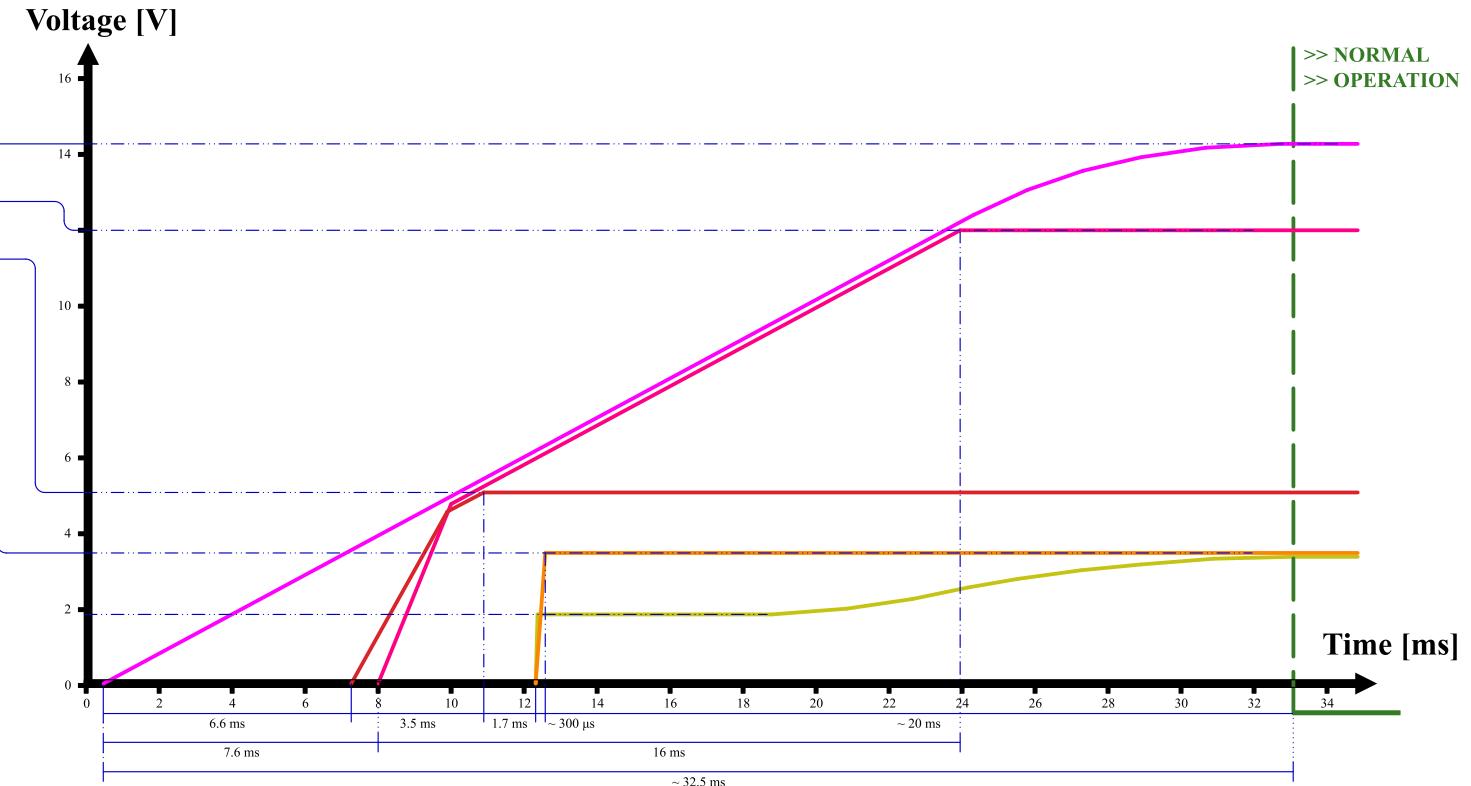
# [5] Sheet Title B



	Comments: References: Flexible I/O worked examples Flexible I/O source configuration	Company: Company Name	Variant: DRAFT	Git Hash: e0f712d
	Board Name: <b>Board Name</b>		Project Name: <b>Project Name</b>	
	Sheet Title: Sheet Title B	File Name: Section B - Title B.kicad_sch	Designer: Author	Date: 2025-01-12 Revision: + (Unreleased)
	Sheet Path: /Project Architecture/Section B - Title B/		Reviewer:	Size: <b>A3</b> Sheet: <b>5 of 7</b>

# [6] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



		Comments:  Company: Company Name  Board Name: <b>Board Name</b>	Variant: DRAFT		Git Hash: e0f712d
			Project Name: <b>Project Name</b>		
			Sheet Title: Power - Sequencing	File Name: Power - Sequencing.kicad_sch	Designer: Author
			Date: 2025-01-12		Revision: + (Unreleased)
Sheet Path:  /Power - Sequencing/			Reviewer:	Size: <b>A4</b>	Sheet: <b>6</b> of 7

## [7] Revision History

	Comments:	Company: <b>Company Name</b>	Variant: DRAFT	Git Hash: e0f712d
		Board Name: <b>Board Name</b>	Project Name: <b>Project Name</b>	
	<b>Sheet Title:</b> Revision History	<b>File Name:</b> Revision History.kicad_sch	<b>Designer:</b> Author	<b>Date:</b> 2025-01-12 <b>Revision:</b> + (Unreleased)
	<b>Sheet Path:</b> /Revision History/		<b>Reviewer:</b>	<b>Size:</b> <b>A4</b> <b>Sheet:</b> <b>7</b> of <b>7</b>