

Mandarinfish

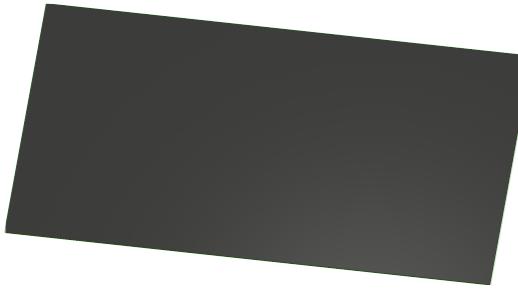
Variant: DRAFT

2025-12-14

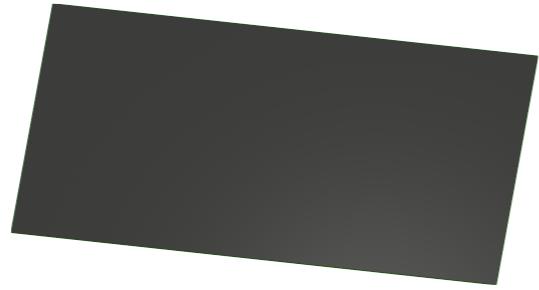
Rev + (Unreleased)

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TOP VIEW



BOTTOM VIEW



NOTES

Comment

Not fitted components are marked as

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 14-Dec-2025

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational design notes.

DESIGN NOTE:
Example text for debug notes.

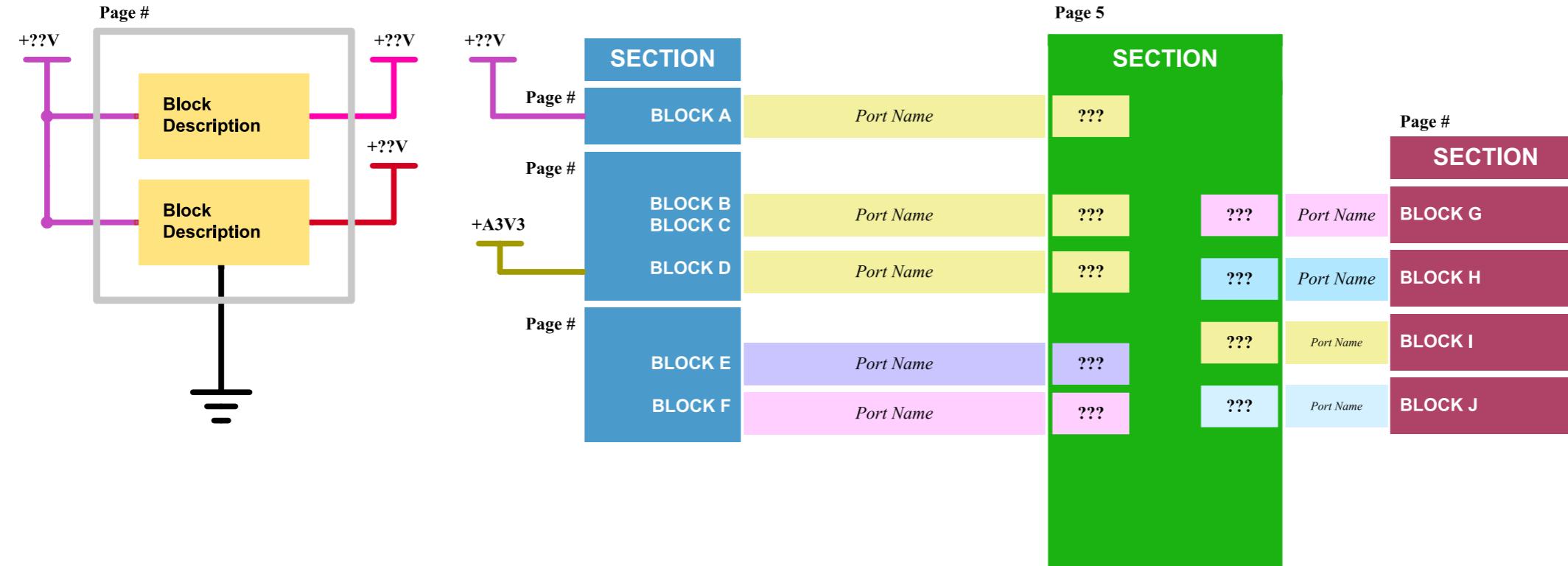
DESIGN NOTE:
Example text for cautionary design notes.

DESIGN NOTE:
Example text for critical design notes.

LAYOUT NOTE:
Example text for critical layout guidelines.

	Comments:	Company: Company Name	Variant: DRAFT	Git Hash: 231f62d
		Board Name: Mandarinfish	Project Name: OpenTamagochi	
	Sheet Title:	File Name: OpenTamagochiHardware.kicad_sch	Designer: Enrique Ramirez	Date: 2025-01-12
	Sheet Path:	/	Reviewer:	Revision: + (Unreleased)
				Size: A3
				Sheet: 1 of 9

[2] Block Diagram

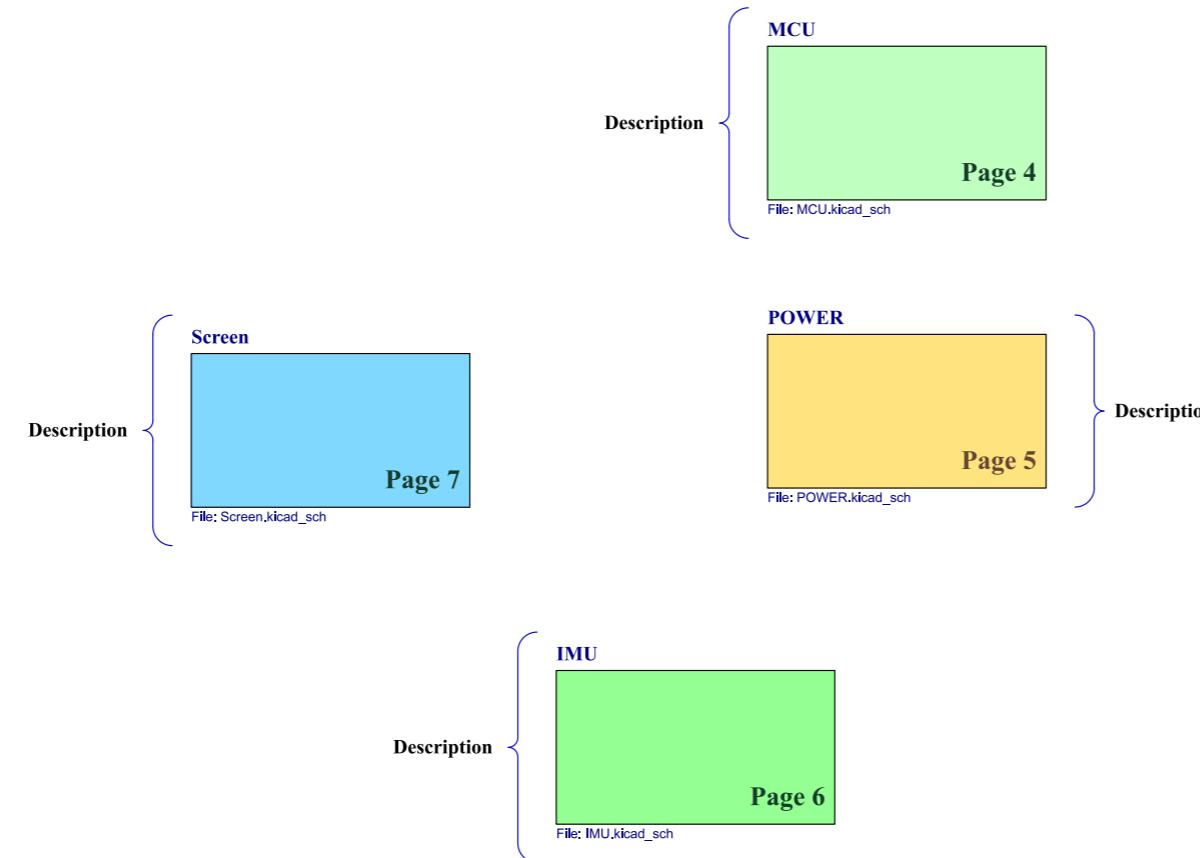


Target specifications:

Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

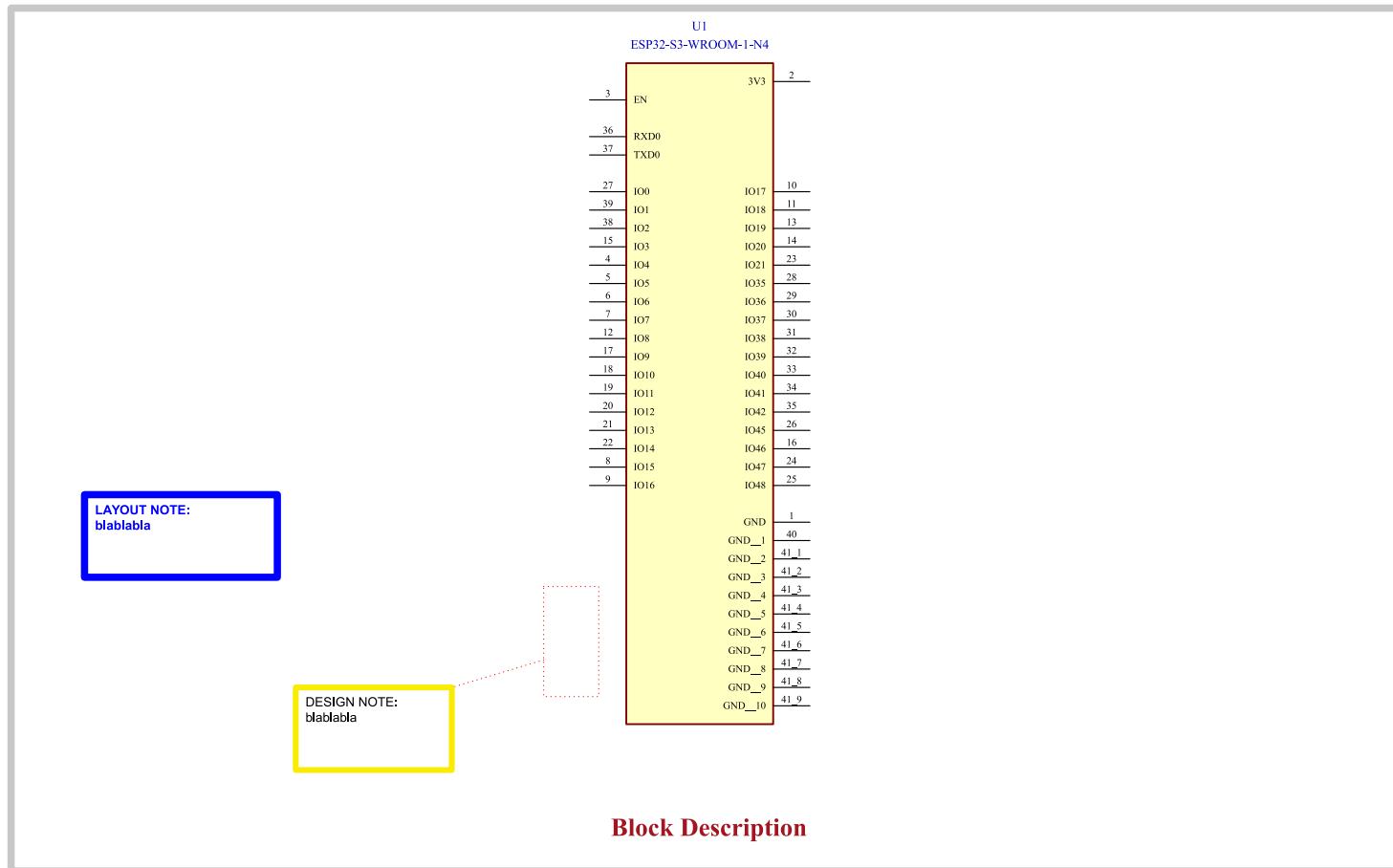
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	Board Name: Mandarinfish	Project Name: OpenTamagochi		
	Sheet Title: Block Diagram	File Name: Block Diagram.kicad_sch	Designer: Enrique Ramirez	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Block Diagram/	Reviewer:	Size: A3	Sheet: 2 of 9

[3] Project Architecture



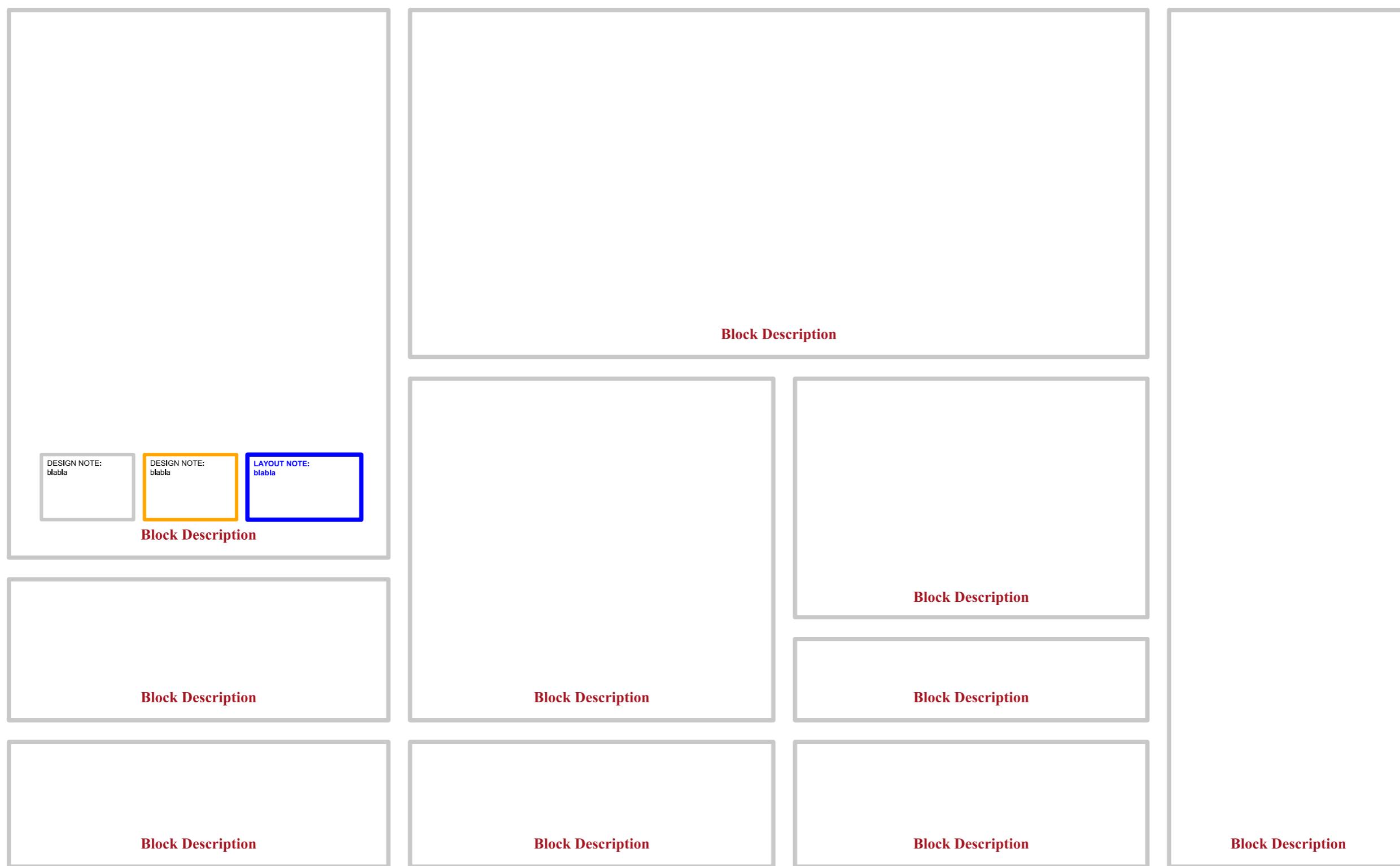
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	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: Enrique Ramirez	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Project Architecture/		Reviewer:	Size: A3 Sheet: 3 of 9

[4] MCU



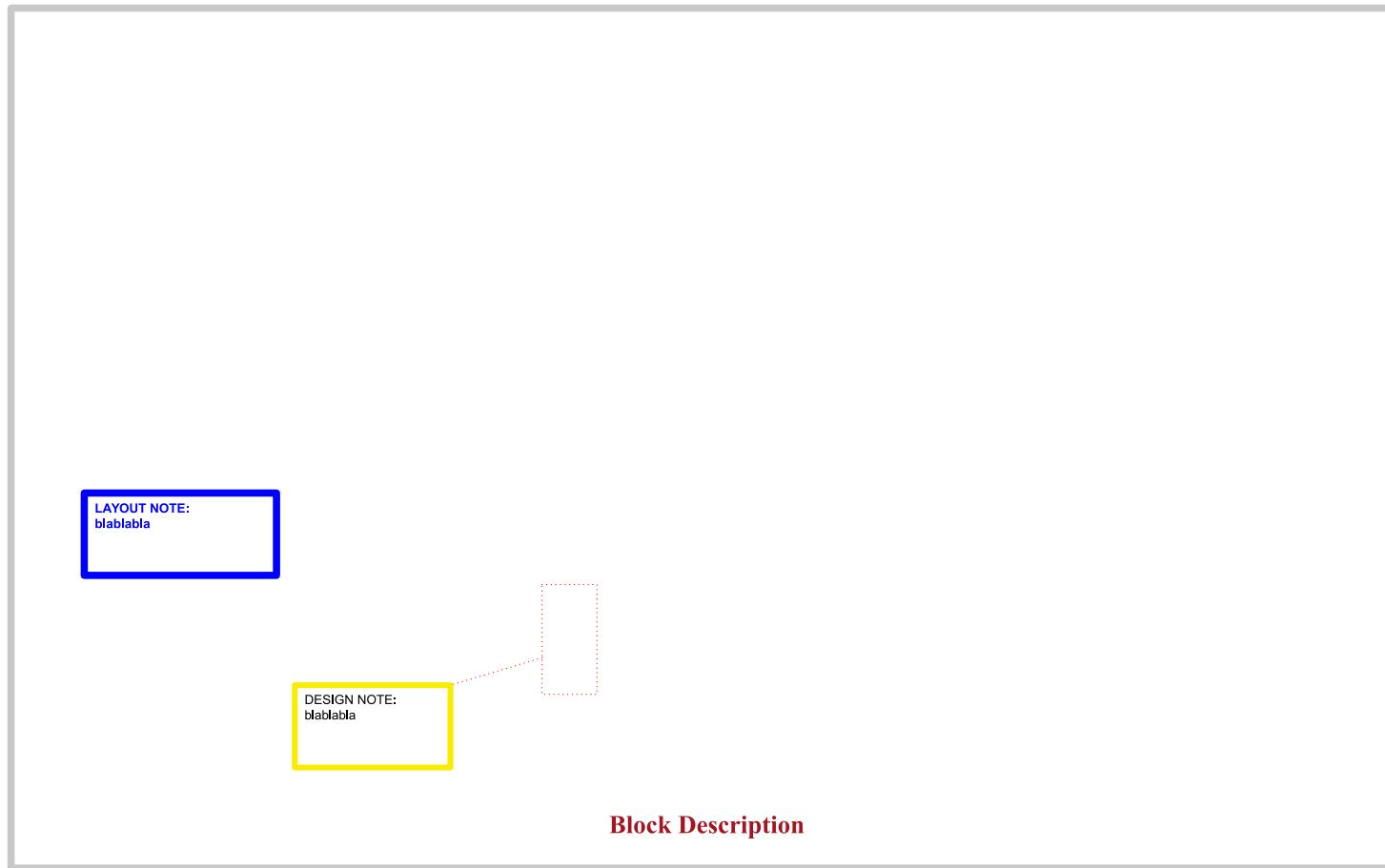
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	Board Name: Mandarinfish			Project Name: OpenTamagochi
	Sheet Title: MCU	File Name: MCU.kicad_sch	Designer: Enrique Ramirez	Date: 2025-01-12 Revision: + (Unreleased)
	Sheet Path: /Project Architecture/MCU/		Reviewer:	Size: A4 Sheet: 4 of 9

[5] Power



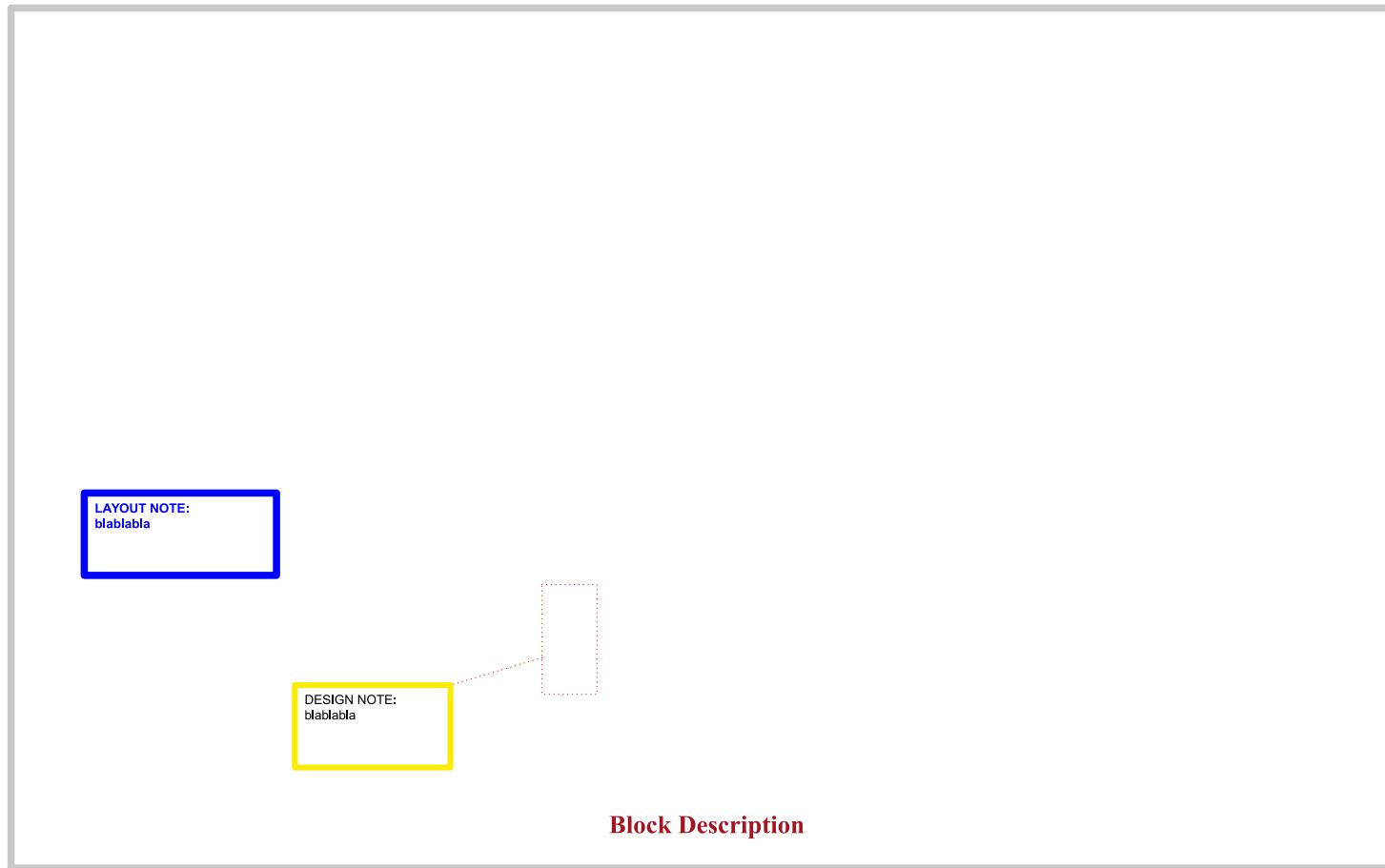
	Comments: References: Flexible I/O worked examples Flexible I/O source configuration	Company: Company Name	Variant: DRAFT	Git Hash: 231f62d
	Board Name: Mandarinfish	Project Name: OpenTamagochi		
	Sheet Title: Power	File Name: POWER.kicad_sch	Designer: Enrique Ramirez	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Project Architecture/POWER/	Reviewer:	Size: A3	Sheet: 5 of 9

[6] screen



		Comments: AN5346 STM32G474 Datasheet p.81 J. Pieper ADC investigation	Company: Company Name	Variant: DRAFT	Git Hash: 231ff62d	
		Board Name: Mandarinfish	Project Name: OpenTamagochi			
		Sheet Title: screen	File Name: IMU.kicad_sch	Designer: Enrique Ramirez	Date: 2025-01-12	
		Revision: + (Unreleased)			Sheet: 6 of 9	
D		Sheet Path: /Project Architecture/IMU/	Reviewer:	Size: A4		

[7] screen



		Comments: AN5346 STM32G474 Datasheet p.81 J. Pieper ADC investigation	Company: Company Name	Variant: DRAFT	Git Hash: 231ff62d	
		Board Name: Mandarinfish	Project Name: OpenTamagochi			
		Sheet Title: screen	File Name: Screen.kicad_sch	Designer: Enrique Ramirez	Date: 2025-01-12	Revision: + (Unreleased)
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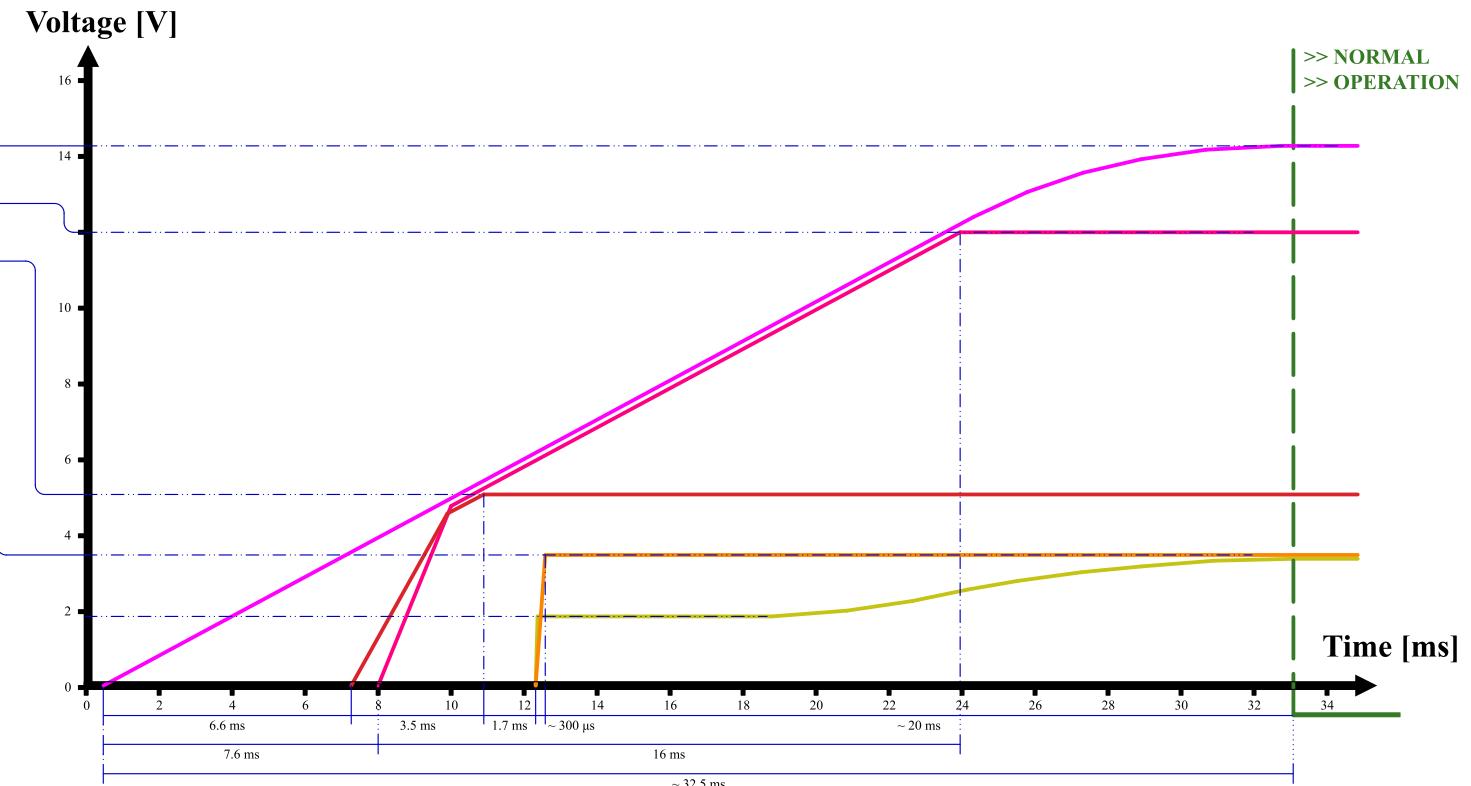
[8] Revision History

A					A
B					B
C					C
D					D

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			Board Name: Mandarinfish	Project Name: OpenTamagochi	
		Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: Enrique Ramirez	Date: 2025-01-12 Revision: + (Unreleased)
		Sheet Path: /Revision History/			Reviewer: A4 Size: 8 of 9 Sheet:

[9] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



		Comments: Company: Company Name Board Name: Mandarinfish	Variant: DRAFT		Git Hash: 231ff62d
			Project Name: OpenTamagochi		
			Sheet Title: Power - Sequencing	File Name: Power - Sequencing.kicad_sch	Designer: Enrique Ramirez
			Date: 2025-01-12		Revision: + (Unreleased)
Sheet Path: /Power - Sequencing/			Reviewer:	Size: A4	Sheet: 9 of 9