Enrique J. González Méndez

11211 NE 20TH Street, Apt 55, Vancouver, WA
enrique.j.gonzalez.mendez.pr@gmail.com
1+ (787)-426-5743
Github | LinkedIn | Portfolio

JOBS

Physical Design Clocking Engineer at Intel – Hillsboro, OR — January 2021 – present

- Leading clock design efforts for next-gen processor, overseeing back-end clock delivery for various sub-systems.
- Leading global clock EDA tool enhancements, customization, integration and testing with outside vendors and multi-disciplinary cross-functional internal teams.
- Responsible for STA for high frequency clocks for various sub-systems, including identifying slope violations, setup and hold violations and possible synchronization problems, this includes simulating at different corners.
- Delivered high frequency Die to Die clocks for synchronization between SoC and compute die.
- Knowledgeable in clock domain crossing, low-power physical design and static timing analysis techniques.
- Clock designer for Intel 4, Intel 3, and Intel 18A client products, including the new Intel Ultra Core products.
- Integrated clock quality indicator trackers into FCL workflow and Splunk using Python, improving project-wide clock quality visibility for 32 clock quality checkers.
- Designed tools for global clock verification and balancing using TCL, enhancing team efficiency.
- Code requested RTL logic design changes for correct clock gating and delivery for new clock be flows.
- Experienced in Caliber DRC/LVS, Static Timing Analysis, Place and Route, clock balancing and FEV.
- Knowledge of Synopsys/Cadence Tools - Fusion Compiler/ICC2, Innovus and PrimeTime.

Software Engineer at CIQA – Hormigueros, PR — January – May 2021

- Directed the development of a web app for validating ISO Standard 14644 compliance for 32+ companies.
- Used React, Express, MySQL, AWS Amplify, AWS Cognito and AWS Lambda for development.
- Accomplished an 80% decrease in report delivery time and a 75% cost reduction.

Software Engineer at Biosecurity Office of PRDH – San Juan, PR — June – October 2020

- Developed and maintained the Bioportal web app that reports all the Covid-19 test results from all of Puerto Rico's hospitals, airports, public and private laboratories.
- Prepared dashboard queries for different reports regarding Covid-19 cases for municipalities.

R&D Engineer Intern at ERDC (DoD) **–** Vicksburg, MS — June – August 2019

- Executed performance analysis on four generative adversarial networks across various datasets, selecting one for further research and development.
- Anticipated an 88% reduction in simulation time with future models, while maintaining a projected accuracy of 94% compared to the current solution.
- Did ETL processes for 6 different datasets and sources for effective model training
- Evaluated model accuracy utilizing the differential results from ABAQUS fluid simulations

IT Engineer at ABB – Arecibo, PR — January – May 2019

- Designed and developed a web application and database for asset tracking with ASP.Net and MS SQL Server, granting visibility to $90k asset movements per year through the manufacturing plant.

EDUCATION

John Hopkins University – Whiting School of Engineering — Currently Attending

- Currently pursuing **M.S.** in **Artificial Intelligence (Part-Time)**

University of Puerto Rico - Mayaguez Campus

- **B.S.** in **Computer Engineering , GPA: 3.24/4.00** — Graduated: **May 2021**
- Relevant Graduate Level Courses: Big Data Analytics, Computer Vision, Digital Design, Microprocessor Systems

SOFTWARE SKILLS

Programming Languages: C, C++, C#, Java, Python, Javascript, HTML, CSS, Swift/SwiftUI, TCL, SQL
Big Data Technologies: Hadoop, Apache Spark, Apache Hive, Dataiku, Splunk
Python Specific Libraries: Pandas, Scikit-Learn, Keras, Tensorflow
Technologies/Frameworks: React, Angular, ASP.net Core with Entity Framework, Maven, Django, Express
Cloud Service Technologies: AWS Lamda, AWS Cognito, AWS Amplify, AWS Lamda