



# SOFTWARE TRACEABILITY

Banking System

Group3

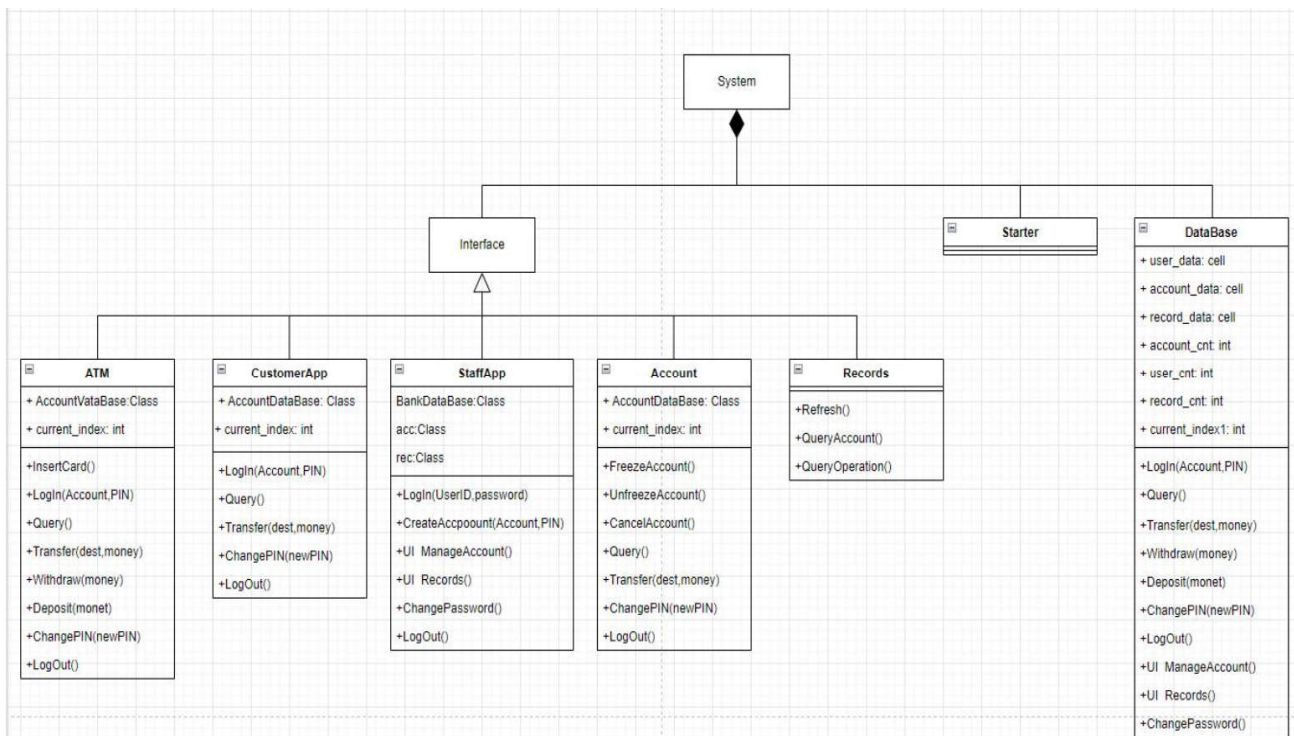
Author(D) : XXXX

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System Architecture

Software Traceability

## System Architecture



## Software Traceability

	A	B	C
1	Requirement	is implemented by	is validated by
2	R1	S2.1, S2.2, S2.4, S1.1	T1-T8
3	R1.1	S2.1, S1.1	T1.1, T1.2
4	R1.2	S2.2, S1.1	T2.1, T2.2, T2.3, T2.4, T2.5
5	R1.3	S2.4.1, S1.1, S1.6	T3.1, T3.2
6	R1.4	S2.4.2, S1.1, S1.7	T6.1, T6.2, T6.3, T6.4, T6.5, T6.6, T6.7
7	R1.5	S2.4.4, S1.1, S1.8	T4.1, T4.2, T4.3, T4.4, T4.5, T4.6, T4.7, T4.8
8	R1.6	S2.4.3, S1.1, S1.9	T5.1, T5.2, T5.3, T5.4, T5.5, T5.6
9	R1.7	S2.4.5, S1.1, S1.11	T7.1, T7.2, T7.3, T7.4, T7.5, T7.6, T7.7, T7.8, T7.9
10	R1.8	S2.4.6, S1.1	T8.1
11	R2	S3.1, S3.3, S1.1	T2, T3, T5, T6, T8
12	R2.1	S3.1, S1.1	T2.1, T2.2, T2.3, T2.4, T2.5
13	R2.2	S3.3.1, S1.1, S1.6	T3.1, T3.2
14	R2.3	S3.3.2, S1.1, S1.7	T6.1, T6.2, T6.3, T6.4, T6.5, T6.6, T6.7
15	R2.4	S3.3.3, S1.1, S1.11	T5.1, T5.2, T5.3, T5.4, T5.5, T5.6
16	R2.5	S3.3.4, S1.1	T8.1
17	R3	S4.1, S4.4, S4.5, S4.6, S4.8, S1.1	Ta1, Ta2, Ta3, Ta4, Ta6
18	R3.1	S4.1, S1.1	Ta1.1, Ta1.2, Ta1.3
19	R3.2	S4.4, S1.1, S1.2, S1.3	Ta2.1, Ta2.2, Ta2.3, Ta2.4, Ta2.5
20	R3.3	S4.5, S1.1	Ta3.1, Ta3.2, Ta3.3, Ta3.4
21	R3.4	S4.6, S1.1	Ta4.1, Ta4.2, Ta4.3
22	R3.5	S4.8, S1.1	Ta6.1
23	R4	S4.6, S1.1, S1.12	Ta4.1, Ta4.2, Ta4.3
24	R4.1	S4.6, S1.1, S1.12	Ta4.1, Ta4.2, Ta4.3
25	R4.2	S4.6, S1.1, S1.12	Ta4.1, Ta4.2, Ta4.3
26	R4.3	S4.6, S1.1, S1.12	Ta4.1, Ta4.2, Ta4.3
27	R5	S4.5, S1.1	Ta3.1, Ta3.2, Ta3.3, Ta3.4
28	R5.1	S4.5.1, S1.1, S1.14	Ta3.1, Ta3.2, Ta3.3, Ta3.4
29	R5.2	S4.5.1, S1.1, S1.15, S1.16	Ta3.1, Ta3.2, Ta3.3, Ta3.4
30	R5.3	S4.5.2, S1.1, S1.13, S1.16	Ta3.1, Ta3.2, Ta3.3, Ta3.4
31	R5.4	S4.5.3, S1.1, S1.6, S1.16	Ta3.1, Ta3.2, Ta3.3, Ta3.4
32	R5.5	S4.5.4, S1.1, S1.7	Ta3.1, Ta3.2, Ta3.3, Ta3.4
33	R5.6	S4.5.5, S1.1	Ta3.1, Ta3.2, Ta3.3, Ta3.4
34	R5.7	S4.5.6, S1.1	Ta3.1, Ta3.2, Ta3.3, Ta3.4