Eric Gustafson

Software Architect | Technology Leader | Cloud Native | DevOps | Monitoring | Networking

Status: Architect and Technical Leader at HPE
Fields: Software Architecture and Development, Cloud, Monitoring, Networking, DevOps
Technologies: Go, Python, Java, C/C++, Ansible, Chef, Nagios/Icinga, ELK, Hadoop, Kafka, RabbitMQ, ZeroMQ
Cloud: Microservices, Kubernetes, Rancher, CoreOS, OpenStack, AWS, Google

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Summary

Agile, detail oriented, hands-on technology leader with excellent communication skills. Extensive experience in Cloud native development and operation, DevOps, System Management, Monitoring, Networking and Messaging.

Professional Experience

Architect - Software Defined & Cloud Group, Hewlett Packard Enterprise (HPE) 2016 - 2017

· Design and development of HPE's project 'New Stack' – a continuation of the R&D effort done under the ATG.
· Kubernetes connector – software networking technology that enables communication between K8s clusters.
· Assess and set technical direction for components in 'New Stack', including API Gateway, Monitoring, and Analytics.

Principal Engineer - Advanced Technology Group (ATG) for Cloud and Open Source, HPE 2014 - 2016

· Incubate and influence new technology adoption across HPE with an emphasis on Cloud focused business units.
· Kubernetes, CoreOS, nesting Container & VM env's, System Mgmt & Mon, Ansible, ELK, Load Balancing, IPv6.

Engineering Manager - Monitoring, HP Cloud, Hewlett-Packard 2011 - 2014

· Lead the design and operation of HP's Public Cloud (internal) monitoring and customer Monitoring-aaS.
· Cloud Native & DevOps, OpenStack(Nova, Swift, Glance, pre-Neutron), Chef, collectd, RabbitMQ, Icinga, HAProxy.

Principal Engineer - Melodeo Inc. & HP Music, Hewlett-Packard 2005 - 2011

· Responsible for multiple, back-end components in the Melodeo service: music ingestion, transcoding, and delivery.
· Amazon-WS(EC2, S3, SQS, EMR, RDS), Java, Python, Ruby, elastic transcoding, music CODEC's, Hadoop/HIVE.

Principal Engineer - Continuous Computing 2003 - 2005

· Developed ARM & x86 firmware/BIOS for multiple lines of blade servers. Chassis managment module dev.
· ARM & x86 assembly, PXE, embedded Linux & kernel development, remote GDB, BusyBox, Python, GCC x-compile.

Software Engineer - *various employeers* 1995 - 2003

· Voice over IP, CPLD Partition, Place, & Route (CAD/EDA), Criminal Link Analysis, Remote Medical Coding.
· C++, Java, CORBA, distributed & network programming, heuristic algorithms, Data Center scale networking.

Education

Bachelor of Science, Computer Engineering - Valparaiso University 1989 - 1993

· Ada language research enabling cross-host communication using language primitives - drove changes in ADA95.
· Focus on embedded system design (68k), networking, and VLSI design.

Masters of Science, Electrical Engineering *(unconferred)* - Universities of Colorado & Houston 1993-1995

· Research: "Multi-threaded Processor Cache Coherency", "CPLD Design for Test", developed Embedded Sys. course.
· Computer Architecture, Digital Design, Digital Signal Processing (DSP), CMOS/VLSI Design, Neural Networks.