Eric Qian

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Summary

- ❏ Enthusiastic tinkerer who's passionate to contribute out of box thinking to disruptive projects, and applying laboratory education into real-world applications.

Education

California Polytechnic State University - San Luis Obispo

Expected Graduation: June 2023   San Luis Obispo, CA

- ❏ Bachelor of Science in Computer Engineering
- ❏ Cumulative GPA: 3.63 | Major GPA: 3.94
- ❏ Relevant Coursework:

Electric Circuit Analysis	Circuit Manufacturing	FPGA Digital Design
RISC-V Assembly and Design	Technical Writing	Python Data Structures
C++	JavaScript	Bash Prog

Work Experiences And Projects

IoT Full Stack Developer - [Trackversal]

Project Description: Portable Multi-Technology Device For Global Asset Tracking
Nov. 2019 to Present   Cupertino, CA

- ❏ IoT ARM Firmware development with Nordic Semiconductor nRF chips via SEGGER Embedded Studio.
- ❏ Implemented serial communication between subsystems.
- ❏ Developed backend API system written in NodeJS with Express and MongoDB.
- ❏ Developed frontend stack for mobile devices by utilizing React Native.
- ❏ Completed hardware integration by sourcing proper components and optimized IoT power consumption by more than 75%.
- ❏ Evaluated technical feasibility and market demand.

Electrical Engineer - [Cal Poly CubeSat Laboratory]

Project Description: Battery Management Systems For Spacecrafts
September. 2020 to Present   San Luis Obispo, CA

- ❏ Launched trade study investigation for power management IC selection.
- ❏ Performed battery subsystem risk analysis to maximize safety for spacecraft applications.

On-Site AV Student Assistant - [Cal Poly Information Technology Services]

Nov. 2019 to June 2020   San Luis Obispo, CA

- ❏ Rapidly troubleshoot and repair critical classroom technologies, and assisted numerous professors in diagnosing problems on-demand. Devices include Windows-based computers, projectors, speakers, monitors, and networking.
- ❏ Accurately troubleshot and repaired tickets with rapid turnaround.
- ❏ Maintained positive work morale and co-worker environment.

Instructional Student Assistant - [Cal Poly, Electrical Engineering & Computer Science]

Apr. 2020 to Present   San Luis Obispo, CA

- ❏ Assisted students in learning by overcoming dynamic technical challenges during laboratory experiments, as well as providing debugging assistance. This includes the use of oscilloscopes, DMMs, and other laboratory equipment, in addition to verifying circuit designs.
- ❏ Rapid on-demand troubleshooting of algorithms and code for Python-based programs.
- ❏ Provided students with technical support for adjustment to a digital-only learning environment.

Experiences

Hardware Related
- ❏ Eagle, Altium Designer
- ❏ LTSpice
- ❏ Circuit Design
- ❏ Verilog
- ❏ Cura & Slic3r
- ❏ Circuit Verification, Assembly, and Debugging
- ❏ SEGGER ARM Development

Web Related Technologies
- ❏ JavaScript
- ❏ HTML & CSS
- ❏ PHP
- ❏ Gatsby & TypeScript
- ❏ Firebase
- ❏ JQuery

Standalone Technologies
- ❏ Bash, Apt, Pacman
- ❏ Python
- ❏ Java
- ❏ C++
- ❏ .NET (C#)
- ❏ Linux/Embedded Linux

Workflow Related Technologies
- ❏ Microsoft Word/Excel
- ❏ Git & GitHub
- ❏ Travis CI & Unit testing
- ❏ VSCode Live Share
- ❏ ~110 WPM

Organizations
- ❏ BananiumLabs Incorporated
- ❏ Cal Poly CubeSat Laboratory
- ❏ IEEE-HKN Honor Society

Credentials
- ❏ Amateur Radio - General Class License

Languages
- ❏ English (Fluent/Native Speaker)
- ❏ Mandarin (Professional Proficiency)

Misc.
- ❏ Numerous Hackathon Submissions (Devpost.com/EnumC) 2017-2020