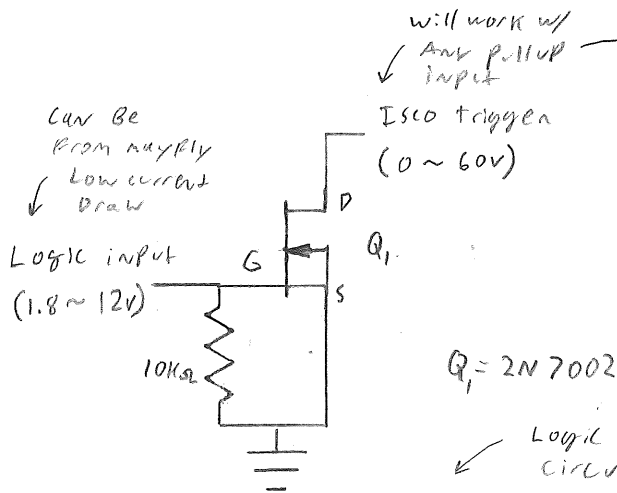
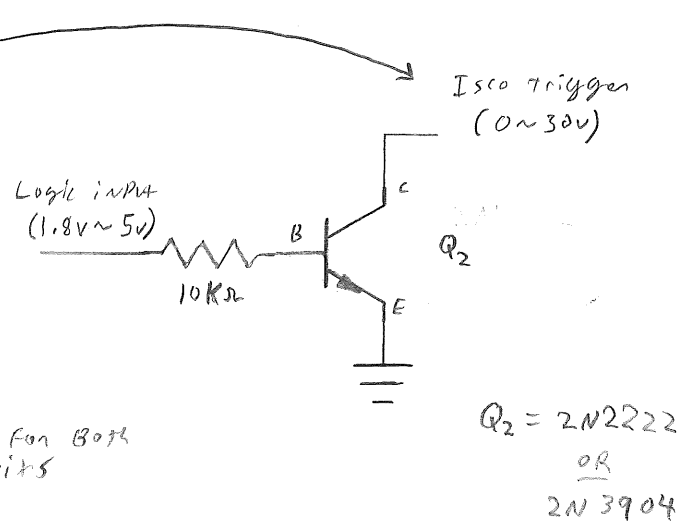


N-FET implementation



NPN implementation



| Logic input | ISCO trigger |
|-------------|--------------|
| High | Low |
| open | Float |
| Low | Float |