

Design Review Notes

Board Layout

- No Zero ohm resistors shown for external connection test points
- stitching vias look great
- Silk stream looks good
- Components seem clustered even though there is unused board space.
- Power traces seem a bit small, but I don't know the potential max draw of the system
- ON/OFF switch?
- Labeling for header connection pins missing

Schematic

- Accelerometer Interface labeling correct but could use addition of alternate mode pinout assignments to avoid confusion (SDA/MOSI for example) had to hit the data sheet.
- RGB LED pixel data pin label slightly confusing, maybe addition of LED_DATA or similar
- Pin 22 not grounded
- No test points indicated
- ON/OFF switch?