1

```
     Stage_1   Stage_2                               Forward
 ■─────┤ ├──────┤/├──────────────────────────■────(S)─
                                                    F2
                                                ────(S)─
```

2

```
     Stage_2   Stage_3                                F3
 ■─────┤ ├──────┤/├──────────────────────────■────(S)─
                                                    F2
                                                ────(R)─
```

3

```
     Stage_3   Stage_4                                F1
 ■─────┤ ├──────┤/├──────────────────────────■────(S)─
                                                    F3
                                                ────(R)─
```

4

```
     Stage_4   Stage_5                              Forward
 ■─────┤ ├──────┤/├──────────────────────────■────(R)─
                                                    F1
                                                ────(R)─
```

5

```
                        Timer_Out
     Stage_5   Stage_6    TON                        Back
 ■─────┤ ├──────┤/├─────IN    Q─────────────■────(S)─
                 T#20s──PT   ET─                     F1
                                            ■────(S)─
                                                    F2
                                                ────(S)─
```

6

```
     Stage_6   Stage_7                                F1
 ■─────┤ ├──────┤/├──────────────────────────■────(R)─
                                                    F2
                                                ────(S)─
```

7

```
     Stage_7   Stage_1                               Back
 ■─────┤ ├──────┤/├──────────────────────────■────(R)─
                                                    F2
                                                ────(R)─
```

1	Start ——| |——————————————————————————(S)— Run

2

```
Stage_7
——| |——          Seq_Ct
           ┌────CTU────┐              Run
           │CU       Q │             —( R )—
           │           │              Stage_1
           │RESET   CV │             —( R )—
Start      │           │              Stage_2
——| |——  5│PV         │             —( R )—
           └───────────┘              Stage_3
                                     —( R )—
                                      Stage_4
    Reset                            —( R )—
——| |——                               Stage_5
                                     —( R )—
                                      Stage_6
                                     —( R )—
                                      Stage_7
                                     —( R )—
```

3	Sens_1 Run Stage_7 Stage_1
	——| |——| |——|/|————————————————————————(S)—

4

```
Stage_7  Sens_1  Run      Timer_In
——| |——| |——| |——   ┌───TON───┐       Stage_1
                     │IN      Q│      —( S )—
              T#5s──│PT     ET│
                     └─────────┘       Stage_7
                                      —( R )—
```

5	Sens_2 Run Stage_1 Stage_2
	——| |——| |——| |————————————————————————(S)—
	Stage_1
	—(R)—

6	Sens_3 Run Stage_2 Stage_3
	——| |——| |——| |————————————————————————(S)—
	Stage_2
	—(R)—

7	Sens_4 Run Stage_3 Stage_4
	——| |——| |——| |————————————————————————(S)—
	Stage_3
	—(R)—

8	Sens_4 Run Stage_4 Stage_5
	——| |——| |——| |————————————————————————(S)—
	Stage_4
	—(R)—

9	Sens_2 Run Stage_5 Stage_6
	——| |——| |——| |————————————————————————(S)—
	Stage_5
	—(R)—

10	Sens_1 Run Stage_6 Stage_7
	——| |——| |——| |————————————————————————(S)—
	Stage_6
	—(R)—

	Class	Label Name	Data Type	Constant	Device	Address	Comment	Remark	Relation with System Label	System Label Name	Attribute
1	VAR_GLOBAL	Sens_1	Bit		X002	%IX2					
2	VAR_GLOBAL	Sens_2	Bit		X003	%IX3					
3	VAR_GLOBAL	Sens_3	Bit		X004	%IX4					
4	VAR_GLOBAL	Sens_4	Bit		X005	%IX5					
5	VAR_GLOBAL	Start	Bit		X000	%IX0					
6	VAR_GLOBAL	Reset	Bit		X001	%IX1					
7	VAR_GLOBAL	Stage_1	Bit		M0	%MX0.0					
8	VAR_GLOBAL	Stage_2	Bit		M1	%MX0.1					
9	VAR_GLOBAL	Stage_3	Bit		M2	%MX0.2					
10	VAR_GLOBAL	Stage_4	Bit		M3	%MX0.3					
11	VAR_GLOBAL	Stage_5	Bit		M4	%MX0.4					
12	VAR_GLOBAL	Stage_6	Bit		M5	%MX0.5					
13	VAR_GLOBAL	Stage_7	Bit		M6	%MX0.6					
14	VAR_GLOBAL	Forward	Bit		Y000	%QX0					
15	VAR_GLOBAL	Back	Bit		Y001	%QX1					
16	VAR_GLOBAL	F1	Bit		Y003	%QX3	Hastighet 1				
17	VAR_GLOBAL	F2	Bit		Y002	%QX2	Hastighet 2				
18	VAR_GLOBAL	F3	Bit		Y004	%QX4	Hastighet 3				

Data Name : Activation

Local Label Setting

	Class	Label Name	Data Type	Constant	Device	Address	Comment
1	VAR	Timer_Out	TON				

	Class	Label Name	Data Type	Constant	Device	Address	Comment
1	VAR	Run	Bit				
2	VAR	Seq_Ct	CTU				
3	VAR	Timer_In	TON				