

## CD4512BM/CD4512BC 8-Channel Buffered Data Selector

### General Description

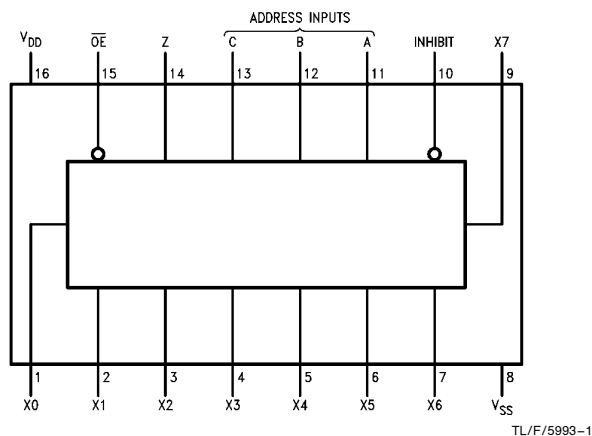
The CD4512BM/CD4512BC buffered 8-channel data selector is a complementary MOS (CMOS) circuit constructed with N- and P-channel enhancement mode transistors. This data selector is primarily used as a digital signal multiplexer selecting 1 of 8 inputs and routing the signal to a TRI-STATE® output. A high level at the Inhibit input forces a low level at the output. A high level at the  $\overline{\text{OE}}$  input forces the output into the TRI-STATE condition. Low levels at both the Inhibit and ( $\overline{\text{OE}}$ ) inputs allow normal operation.

### Features

- Wide supply voltage range 3.0V to 15V
- High noise immunity 0.45  $V_{DD}$  (typ.)
- TRI-STATE output
- Low quiescent power dissipation 0.25  $\mu\text{W}/\text{package}$  (typ.) @  $V_{CC} = 5.0\text{V}$
- Plug-in replacement for Motorola MC14512

### Connection Diagram and Truth Table

Dual-In-Line Package



Order Number CD4512B

Top View

Address Inputs			Control Inputs		Output
C	B	A	Inhibit	$\overline{\text{OE}}$	Z
0	0	0	0	0	X0
0	0	1	0	0	X1
0	1	0	0	0	X2
0	1	1	0	0	X3
1	0	0	0	0	X4
1	0	1	0	0	X5
1	1	0	0	0	X6
1	1	1	0	0	X7
⓪	⓪	⓪	1	0	0
⓪	⓪	⓪	⓪	1	Hi-Z

⓪ = Don't care

Hi-Z = TRI-STATE condition

Xn = Data at input n

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