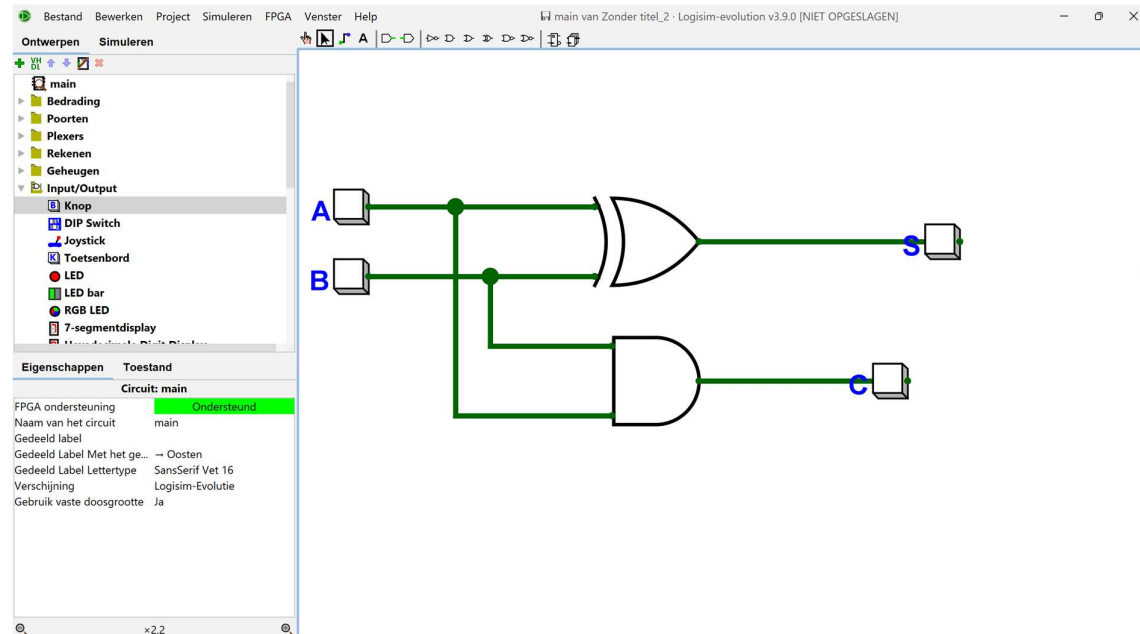


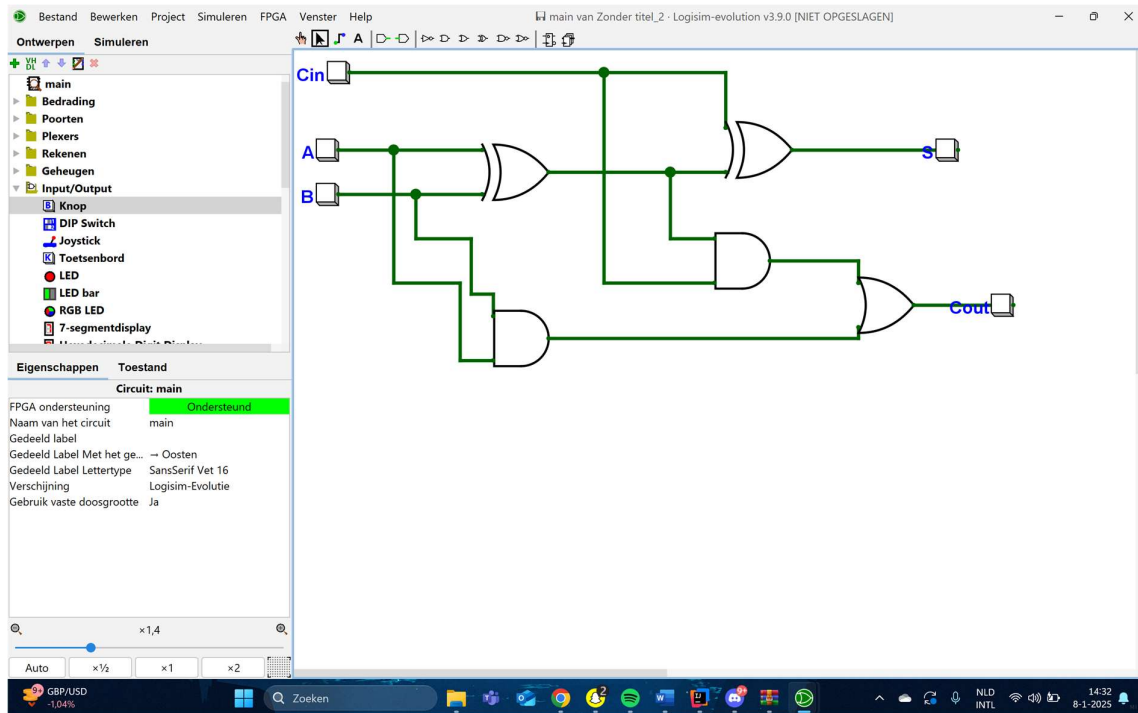
Template Week 3 – Hardware

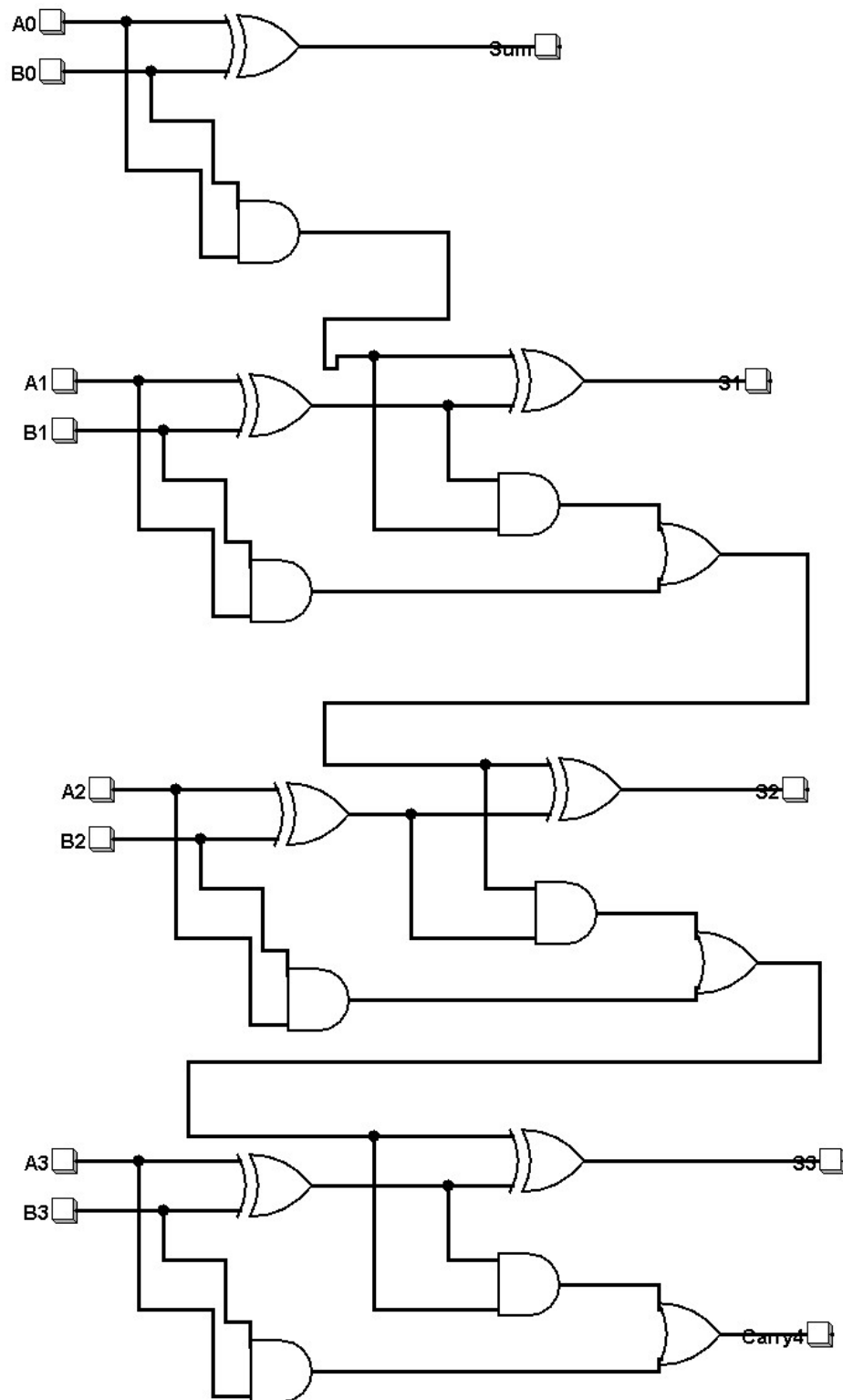
Bonus point assignment – week 3

Complete the **half adder**, **full adder** and **4-bit adder** assignment as described in the PowerPoint slides of week 3 in Logisim. Save the chip design and also export three PNG pictures of the separate finished designs. See the PowerPoint slides of week 3.

Paste the three exported PNG pictures in here.







Ready? Save this file and export it as a pdf file with the name: [week3.pdf](#)