Timing diagram with axis marked in nanoseconds from 0ns to 1000ns (labeled at 25ns intervals). Signal stages shown:

- analog front end delay
- Cables delay
- Digitizing analog signal
- Deserealization
- Fast Trigger Signal in Pre-trigger
- Fast Trigger Signal in Pre-trigger
- Cable delay
- Fast Trigger Signal in Distr.board (Puls or Trigger protocol (Header))
- Fast Trigger Signal EROS/ROESTI
- Trigger signal in Pre-trigger (Max.Ampl. and Number are Found)
- Transport delay from Pre-trigger board to EROS/ROESTI thought FCT-FC7-FCT
- Hedder searching in EROS/ROESTI