Contact

8, Choice Palace, Rajendra Nagar, Indore 452009 9755491130 (Mobile) ervijayraghuwanshi@gmail.co m

www.linkedin.com/in/ ervijayraghuwanshi (LinkedIn)

Top Skills

Reasoning Skills

Mathematics

Emerging Technologies

Certifications

Operating Systems and You:
Becoming a Power User
Industrial Automation Training
Introduction to the Internet of Things
and Embedded Systems
AutoCAD Electrical: Implementing
PLCs

AutoCAD Professional 2D & 3D Training

Honors-Awards

Swadeshi Microprocessor Challenge 2020

Vijay Raghuwanshi

SDE @ClearTrail | AgenticRAG & LLM Integration | Backend & DevOps | Cloud & Automation

Indore, Madhya Pradesh, India

Summary

As a software engineer at ClearTrail Technologies, I leverage my skills in software design and infrastructure to streamline infrastructure management and deployment. I have experience with Vagrant, Docker, Shell scripts, PyInstaller, Docker containers, RPM builds, ELK Stack, and CI/CD pipelines. I also prioritize code quality by performing regular static code analysis and adhering to standards like PEP8.

In addition to my current role, I have developed OSINT tools that improved data accuracy by 80% and reduced collection time by 50%. I have also redesigned web scraping systems for higher throughput and handling large concurrent requests. Furthermore, I have enhanced cybersecurity with PCAP analysis and intrusion detection systems for threat intelligence. I am passionate about creating scalable, fault-tolerant systems and solving complex problems in the field of software engineering.

Experience

ClearTrail Technologies 2 years 11 months

Software Engineer
July 2024 - Present (9 months)

Indore, Madhya Pradesh, India

In my current role, I streamline infrastructure management and deployment using Vagrant, Docker, and Shell scripts. I release software using Pylnstaller, Docker containers, and RPM builds, ensuring reliable production environments. I maintain service logs through the ELK Stack and implement CI/CD pipelines to automate deployment processes. I also prioritize code quality by performing regular static code analysis and adhering to standards like PEP8.

Associated Software Engineer

May 2022 - July 2024 (2 years 3 months)

Indore, Madhya Pradesh, India

Responsibilities:

Developed OSINT tools, optimized HTTP sessions and proxy rotation to improve data accuracy and efficiency.

Led the development of OSINT tools, improving data accuracy by 80% and reducing collection time by 50%.

Redesigned web scraping systems for higher throughput and handling large concurrent requests.

Enhanced cybersecurity with PCAP analysis and intrusion detection systems for threat intelligence.

Created a scalable, fault-tolerant web scraping system, handling large concurrent requests with ease.

Designed a CIDR-based PostgreSQL IP geo-database for efficient offline data retrieval.

Implemented caching for multiple REST services, significantly improving server performance.

Key Achievements:

- ✓ March 2024: Awarded for implementing a complex Controller Service in the LightSabre project.
- ✓ Oct Dec 2022: Recognized for contributions to WebTrail onboarding and feature development.
- ✓ Sep 2022: Acknowledged for supporting Snapchat and Flight Radar integrations.

NATIONAL INSTITUTE OF ELECTRONICS & INFORMATION TECHNOLOGY (NIELIT)

IEP on FPGA Based Embedded Systems-Covering Swadeshi Microprocessors

October 2020 - October 2020 (1 month)

India

#During this intensive training program at NIELIT Calicut, I gained hands-on experience in FPGA-based embedded systems with a focus on Swadeshi microprocessors, under the C2SD (SMDP-III) Project. This program deepened my understanding of Field-Programmable Gate Arrays (FPGAs) and their critical role in embedded system design, prototyping, and application

development. I also worked on several core aspects of system design, including:

- ✓ RISC-V architecture and its integration in embedded systems.
- ✓ Verilog programming for FPGA implementations.
- ✓ PCB design and hardware prototyping.
- ✓ Linux system administration for IoT and embedded applications.

This program provided a comprehensive exploration of Field-Programmable Gate Arrays (FPGAs) and their critical role in embedded system design, fostering domestic technological expertise.

Shree Balaji Enterprise indore Electrical Engineer - Robotics June 2017 - August 2018 (1 year 3 months) India

Designed and maintained automated industrial systems leveraging robotics, PLCs, HMIs, and VFDs to enhance operational efficiency.

Key Achievements:

- # Optimized Production Efficiency: Implemented automation solutions that significantly improved production workflows.
- # System Reliability: Utilized troubleshooting expertise in electrical and control systems to ensure seamless operation.
- # SCADA Integration: Proficiently integrated SCADA for centralized monitoring and control, enabling real-time insights into system performance.

My experience in this role honed my skills in industrial automation and reinforced my passion for engineering solutions that drive innovation and efficiency.

Education

Rajiv Gandhi Prodyogiki Vishwavidyalaya
Bachelor of Technology, Electrical and Electronics Engineering · (August 2018 - June 2021)

Rajiv Gandhi Prodyogiki Vishwavidyalaya

Diploma In Electrical Engineering , Electrical Engineering · (2013 - 2016)

Page 3 of 4