LQFP64 PinNr.	Pin Name	Type	Main Function (after Reset)	Alternate Function	Remap
14	PA0-WKUP	I/O	PA0	WKUP/USART2_CTS/ ADC12_IN0/ TIM2_CH1_ETR/TIM5_CH1/ ETH_MII_CRS_WKUP	-
15	PA1	I/O	PA1	USART2_RTS/ ADC12_IN1/ TIM5_CH2/TIM2_CH2/ ETH_MII_RX_CLK/ ETH_RMII_REF_CLK	-
16	PA2	I/O	PA2	USART2_TX/ TIM5_CH3/TIM2_CH3/ ADC12_IN2/ ETH_MII_MDIO/ETH_RMII_MDIO	-
17	PA3	I/O	PA3	USART2_RX TIM5_CH4/TIM2_CH4/ ADC12_IN3/ ETH_MII_COL	-
20	PA4	I/O	PA4	SPI1_NSS/ DAC_OUT1/ USART2_CK/ ADC12_IN4	SPI3_NSS/
21	PA5	I/O	PA5	SPI1_SCK/ DAC_OUT2/ ADC12_IN5	-
22	PA6	I/O	PA6	SPI1_MISO/ ADC12_IN6/ TIM3_CH1	TIM1_BKIN
23	PA7	I/O	PA7	SPI1_MOSI_/ ADC12_IN7/ TIM3_CH2/ ETH_MII_RX_DV/ ETH_RMII_CRS_DV	TIM1_CH1N
41	PA8	I/O	PA8	USART1_CK/ OTG_FS_SOF/ TIM1_CH1/MCO	-
42	PA9	I/O	PA9	USART1_TX/ TIM1_CH2/ OTG_FS_VBUS	-
43	PA10	I/O	PA10	USART1_RX/ TIM1_CH3/ OTG_FS_ID	-
44	PA11	I/O	PA11	USART1_CTS/ CAN1_RX/ TIM1_CH4/ OTG_FS_DM	-