

$$Vbuffer_{in} \ = \ V_{sine} \ x \ \frac{R4}{R1 + R2 + R3 + R4} \ = \ \frac{5.36K}{100k + 100k + 100k + 5.36k} \ x \ 220 \ = \ 3.86V$$

Figure-8: Voltage Reading Circuit from the Mains

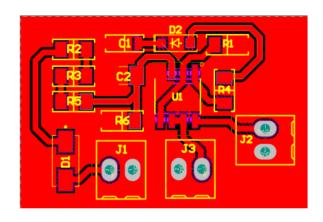


Figure-10: Board Design

