

Parameters:

<Logarithmic> float
 Increment: 0.01
 *RST: 1

Example:

SWE:POW:STEP 10dB
 sets the step width for logarithmic sweep spacing to 10 dB of the previous level in each instance (for a level sweep).

Manual operation: See "Step - Level Sweep" on page 192

[:SOURce<hw>]:SWEep:RESet[:ALL]

Resets all active sweeps to the starting point.

Example: SWE:RES

resets all active sweeps to the starting point.

Usage: Event

Manual operation: See "Reset Sweep - Frequency Sweep" on page 183

6.14 STATus subsystem

This system contains the commands for the status reporting system. See also Chapter 5.5, "Status reporting system", on page 273 for detailed information.

*RST on page 286 has no effect on the status registers.

Value ranges

- Queries return the current value of the respective register, which permits a check of the device status.
 Return values: A decimal value in the range 0 to 32767 (= $2^{15}-1$)
- The configuration commands set the respective register thus determining which status changes of the R&S SMB cause the status registers to be changed.
 Setting values: A decimal value in the range 0 to 32767 (= $2^{15}-1$)

:STATus:OPERation:CONDITION.....	434
:STATus:OPERation:ENABLE.....	434
:STATus:OPERation[:EVENT].....	434
:STATus:OPERation:NTRansition.....	434
:STATus:OPERation:PTRansition.....	435
:STATus:PRESet.....	435
:STATus:QUESTIONable:CONDITION.....	435
:STATus:QUESTIONable:ENABLE.....	435
:STATus:QUESTIONable[:EVENT].....	436
:STATus:QUESTIONable:NTRansition.....	436
:STATus:QUESTIONable:PTRansition.....	436
:STATus:QUEue[:NEXT]?.....	436

:STATus:OPERation:CONDition?

Quieres the content of the CONDition part of the STATus:OPERation register.

This part contains information on the action currently being performed in the instrument. The content is not deleted after being read out because it indicates the current hardware status.

Return values:

<Condition> string

Example: :STATus:OPERation:CONDition?

Usage: Query only

:STATus:OPERation:ENABLE <Enable>

Sets the bits of the ENABLE part of the STATus:OPERation register. This setting determines which events of the Status-Event part are forwarded to the sum bit in the status byte. These events can be used for a service request.

Parameters:

<Enable> string

Example: :STAT:OPER:ENAB 32767

all events are forwarded to the sum bit of the status byte.

:STATus:OPERation[:EVENT] <Event>

Quieres the content of the EVENT part of the STATus:OPERation register. This part contains information on the actions performed in the instrument since the last readout. The content of the EVENT part is deleted after being read out.

Parameters:

<Event> string

Example: :STAT:OPER:EVEN?

queries the STATus:OPERation:EVENT register.

:STATus:OPERation:NTRansition <Ntransition>

Sets the bits of the NTRansition part of the STATus:OPERation register. If a bit is set, a transition from 1 to 0 in the condition part causes an entry to be made in the EVENT part of the register. The disappearance of an event in the hardware is thus registered, for example the end of an adjustment.

Parameters:

<Ntransition> string

Example: :STAT:OPER:NTR 0

a transition from 1 to 0 in the condition part of the Status:Operation register does not cause an entry to be made in the EVENT part.

:STATus:OPERation:PTRansition <Ptransition>

Sets the bits of the PTRansition part of the STATus:OPERation register. If a bit is set, a transition from 0 to 1 in the condition part causes an entry to be made in the EVENT part of the register. A new event in the hardware is thus registered, for example the start of an adjustment.

Parameters:

<Ptransition> string

Example:

:STAT:OPER:PTR 32767

all transitions from 0 to 1 in the condition part of the Status:OPERation register cause an entry to be made in the EVENT part.

:STATus:PRESet <Preset>

Resets the status registers. All PTRansition parts are set to FFFFh (32767), i.e. all transitions from 0 to 1 are detected. All NTRansition parts are set to 0, i.e. a transition from 1 to 0 in a CONDition bit is not detected. The ENABLE parts of STATus:OPERation and STATus:QUESTIONable are set to 0, i.e. all events in these registers are not passed on.

Parameters:

<Preset> string

Example:

STAT:PRES

resets the status registers.

:STATus:QUESTIONable:CONDition <Condition>

Queries the content of the CONDition part of the STATus:QUESTIONable register. This part contains information on the action currently being performed in the instrument. The content is not deleted after being read out since it indicates the current hardware status.

Parameters:

<Condition> string

Example:

:STATus:QUESTIONable:CONDition?

queries the Status:Questionable:Condition register.

:STATus:QUESTIONable:ENABLE <Enable>

Sets the bits of the ENABLE part of the STATus:QUESTIONable register. The enable part determines which events of the STATus:EVENT part are enabled for the summary bit in the status byte. These events can be used for a service request.

If a bit in the ENABLE part is 1, and the corresponding EVENT bit is true, a positive transition occurs in the summary bit. This transition is reported to the next higher level.

Parameters:

<Enable> string

Example: STAT:QUES:ENAB 1
Problems when performing an adjustment cause an entry to be made in the sum bit.

:STATus:QUEstionable[:EVENT] <Event>

Queries the content of the EVENT part of the STATus:QUEstionable register. This part contains information on the actions performed in the instrument since the last readout. The content of the EVENT part is deleted after being read out.

Parameters:

<Event> string

Example: STAT:QUES:EVEN?
queries the Status:Questionable:Event register.

:STATus:QUEstionable:NTRansition <Ntransition>

Sets the bits of the NTRansition part of the STATus:QUEstionable register. If a bit is set, a transition from 1 to 0 in the condition part causes an entry to be made in the EVENT part of the register.

Parameters:

<Ntransition> string

Example: STAT:QUES:NTR 0
a transition from 1 to 0 in the condition part of the STATus:QUEstionable register does not cause an entry to be made in the EVENT part

:STATus:QUEstionable:PTRansition <PTRansition>

Sets the bits of the PTRansition part of the STATus:QUEstionable register. If a bit is set, a transition from 1 to 0 in the condition part causes an entry to be made in the EVENT part of the register.

Parameters:

<PTRansition> string

Example: STAT:QUES:PTR 32767
all transitions from 0 to 1 in the condition part of the STATus:QUEstionable register cause an entry to be made in the EVENT part

:STATus:QUEue[:NEXT]?

Queries the oldest entry in the error queue and then deletes it. Positive error numbers denote device-specific errors, and negative error numbers denote error messages defined by SCPI. If the error queue is empty, 0 ("No error") is returned.

The command is identical to :SYSTem:ERRor [:NEXT]? on page 440.