

# Annex

## A Hardware Interfaces

This section covers hardware related topics, like pin assignment of the GPIB bus interface.

The remote control interfaces are described in details in [Chapter 5, "Remote Control Basics"](#), on page 240.

All other interfaces are described in sections "Legend of Front Panel" and "Legend of Rear Panel" in the Quick Start Guide.

For specifications refer to the data sheet.

### A.1 GPIB Bus Interface

#### Pin assignment

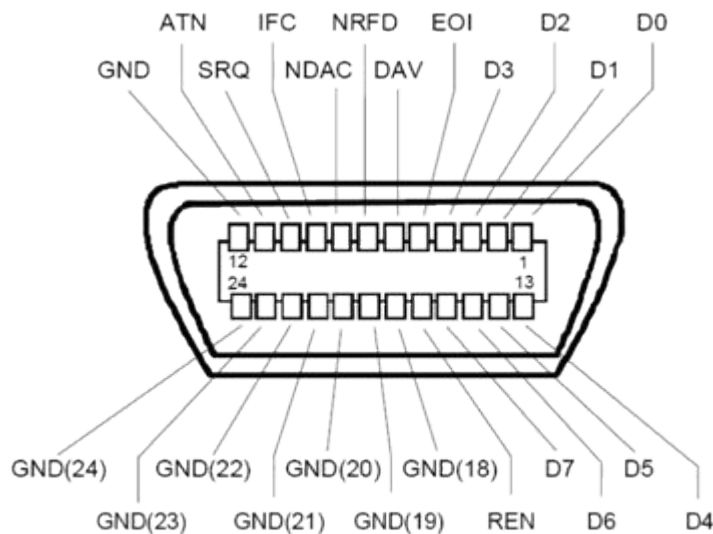


Figure A-1: Pin assignment of GPIB bus interface

#### Bus lines

- Data bus with 8 lines D0 to D7:  
The transmission is bit-parallel and byte-serial in the ASCII/ISO code. D0 is the least significant bit, D7 the most significant bit.
- Control bus with five lines: