Eric S. Crosson

11010 Domain Dr Apt 11310 Austin, TX 78758

(360) 820 - 8196 · esc@ericcrosson.com · github.com/ericcrosson

Objective Leverage technical skills to increase revenue and collaborate with a knowledgeable team

Relevant Skills

- · Product ownership over entire lifecycle, from design to deployment and iteration in production
- · Emphasis on verifiability through pure functions, type theory, property/system testing, and ruthless automation
- · Hard- & soft- real-time event-driven distributed and concurrent systems
- \cdot Entrepreneurial experience leading a diverse team of contractors and junior programmers
- · Strong sense of ownership and determination to see project completion

Qualifications

- · 17 years experience programming, 11 professionally including 2 start-ups, 1 profitable
- · Expertise with TypeScript on Node.js, Docker, C/C++, Python, Ansible, Lisp, Ruby, Unix, Bash, git, LATEX
- · Familiar with AWS, Terraform, NoSQL, Haskell, Clojure, React, MVC, Java, golang, CMake, ACL2, Promela
- · Looking forward to deeper mastery of Scala, Rust, Nix, GraphQL, algebra, and statistics

Recent Work Experience

- · Strong Roots Capital Chief Technology Officer (2018 Today)
 - Architected, implemented platform for research and autonomous execution of quantitative trading strategies
 - Purely functional, 100% TypeScript microservice architecture on AWS from ETL to order execution
 - o Entirely immutable cloud with infrastructure-as-code and serverless functions
- · ShoreTel Software Engineer (2015 2018)
 - o Architected next-gen embedded real-time phone firmware
 - o Created custom Linux distribution for in-house hardware with yocto
 - o Maintained on-premise GitLab service and CI cluster with infrastructure-as-code
 - Developed custom tooling to automate engineer workflows
 - o Refactored legacy codebase to decrease coupling and increase testability
- · IBM Cloud Infrastructure Engineer (2015)
 - o DevOps management of public OpenStack cloud offering
 - Collaboration with patent teams
- · Intel Pre and Post Silicon Validation (2014 2015)
 - $\circ\,$ Created analysis engine for $3^{\rm rd}$ party DHCP RTL signals
- · Centaur Technology Design Verification (2013 2014)
 - Invariant-based verification of multi-core PSE-36/PAE access and caching
 - o Formal verification with ACL2

Educational History

· University of Texas Bachelor of Science in Computer Architecture and Embedded Systems, May 2016