

**Eric S. Crosson**  
11010 Domain Dr Apt 11310 Austin, TX 78758  
(360) 820 - 8196 · [esc@ericcrosson.com](mailto:esc@ericcrosson.com) · [github.com/ericcrosson](https://github.com/ericcrosson)

**Objective** Leverage technical skills to increase revenue and collaborate with a knowledgeable team

---

## Relevant Skills

- Iterative deployments of well-defined functional software
  - Emphasis on verifiability through pure functions, type theory, and property testing
  - Real-time event-driven distributed programming
  - Experience leading a diverse team of contractors and junior programmers
  - Strong sense of ownership and determination to see project completion
- 

## Qualifications

- 17 years experience programming, 11 professionally including 3 start-ups
  - Expertise with TypeScript, Node.js, C/C++, Python, Docker, Ansible, Lisp, Ruby, Unix, Bash, git, L<sup>A</sup>T<sub>E</sub>X
  - Familiar with AWS, Terraform, NoSQL, Haskell, Clojure, Java, go, CMake, Octave, ACL2, Promela, asm
  - Looking forward to learning more Scala, Rust, Nix, GraphQL, algebra, and statistics
- 

## Recent Work Experience

- **Strong Roots Capital – Chief Technology Officer** (2018 - Today)
    - Architected, implemented platform for research and autonomous execution of quantitative trading strategies
    - Purely functional, 100% TypeScript microservice architecture on AWS from ETL to order execution
    - Entirely immutable cloud with infrastructure-as-code and serverless functions
  - **ShoreTel – Software Engineer** (2015 - 2018)
    - Architected next-gen embedded real-time phone firmware
    - Created custom Linux distribution for in-house hardware with yocto
    - Maintained on-premise GitLab service and CI cluster with infrastructure-as-code
    - Developed custom tooling to automate engineer workflows
    - Refactored legacy codebase to decrease coupling and increase testability
  - **IBM – Cloud Infrastructure Engineer** (2015)
    - DevOps management of public OpenStack cloud offering
    - Collaboration with patent teams
  - **Intel – Pre and Post Silicon Validation** (2014 - 2015)
    - Created analysis engine for 3<sup>rd</sup> party DHCP RTL signals
  - **Centaur Technology – Design Verification** (2013 - 2014)
    - Invariant-based verification of multi-core PSE-36/PAE access and caching
    - Formal verification with ACL2
- 

## Educational History

- **University of Texas** Bachelor of Science in Computer Architecture and Embedded Systems, May 2016