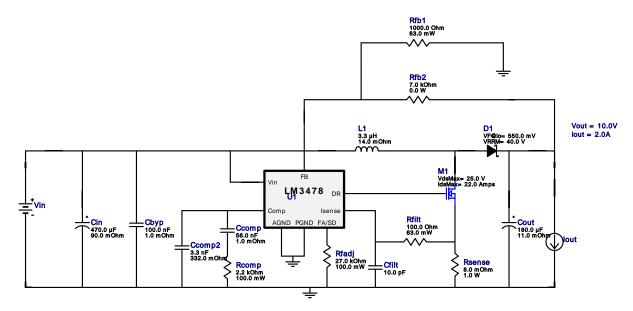
VinMin = 3.5V VinMax = 4.2V Vout = 10.0V lout = 2.0A

Device = LM3478MM/NOPB Topology = Boost Created = 2023-01-24 12:49:05.416 BOM Cost = NA BOM Count = 16 Total Pd = 2.67W

WEBENCH® Design Report

Design: 3 LM3478MM/NOPB LM3478MM/NOPB 3.5V-4.2V to 10.00V @ 2A

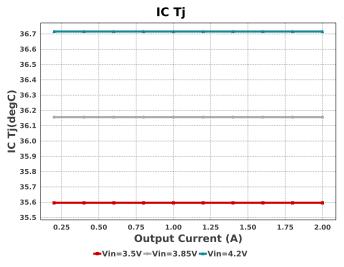


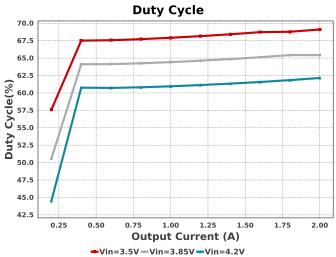
1. With the low turn of voltage of the LM34x8 your power supply may current limit before you reach your working input voltage. If this happens, or to preempt this from happening, you can include a low pass RC filter from input voltage to Vin on the IC. Make sure the rise time on the RC network is slower than your supply's rise time.

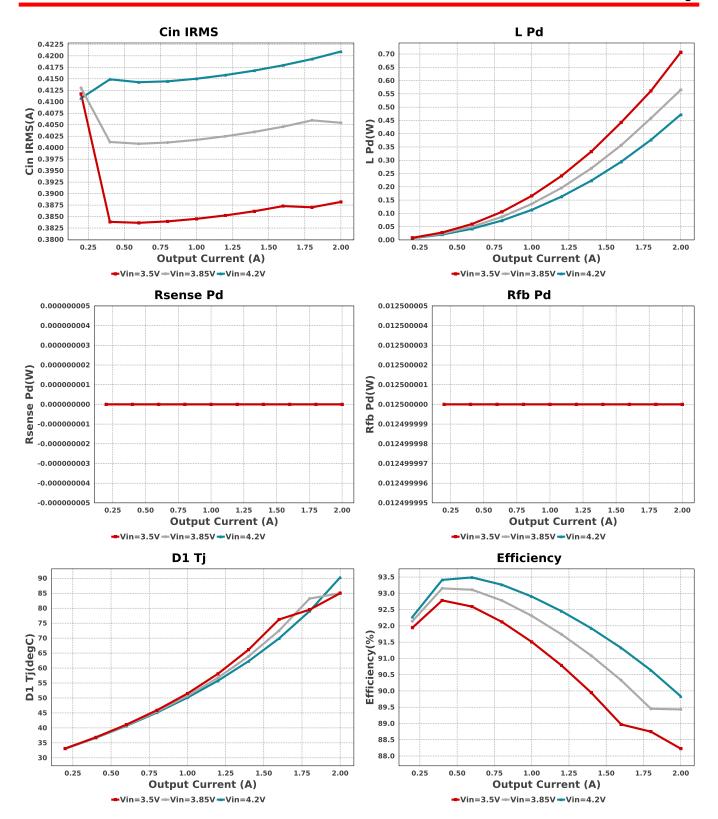
Electrical BOM

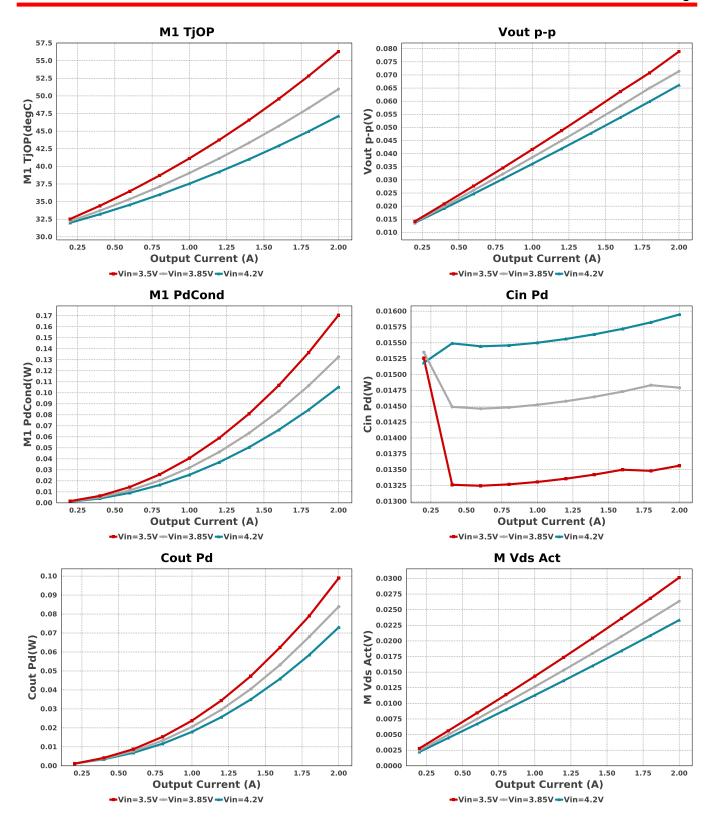
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cbyp	MuRata	GRM155R70J104KA01D Series= X7R	Cap= 100.0 nF ESR= 1.0 mOhm VDC= 6.3 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Ccomp	MuRata	GRM155R71C563KA88D Series= X7R	Cap= 56.0 nF ESR= 1.0 mOhm VDC= 16.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Ccomp2	Kemet	C0805C332K5RACTU Series= X7R	Cap= 3.3 nF ESR= 332.0 mOhm VDC= 50.0 V IRMS= 319.0 mA	1	\$0.01	0805 7 mm ²
Cfilt	Yageo	CC0805JRNPO9BN100 Series= C0G/NP0	Cap= 10.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0805 7 mm ²
Cin	Nichicon	UUD1E471MNL1GS Series= uD	Cap= 470.0 uF ESR= 90.0 mOhm VDC= 25.0 V IRMS= 670.0 mA	1	\$0.23	SM_RADIAL_10BMM 160 mm²
Cout	Panasonic	16SVPE180M Series= SVPE	Cap= 180.0 uF ESR= 11.0 mOhm VDC= 16.0 V IRMS= 4.46 A	1	\$0.73	CAPSMT_62_C10 74 mm²

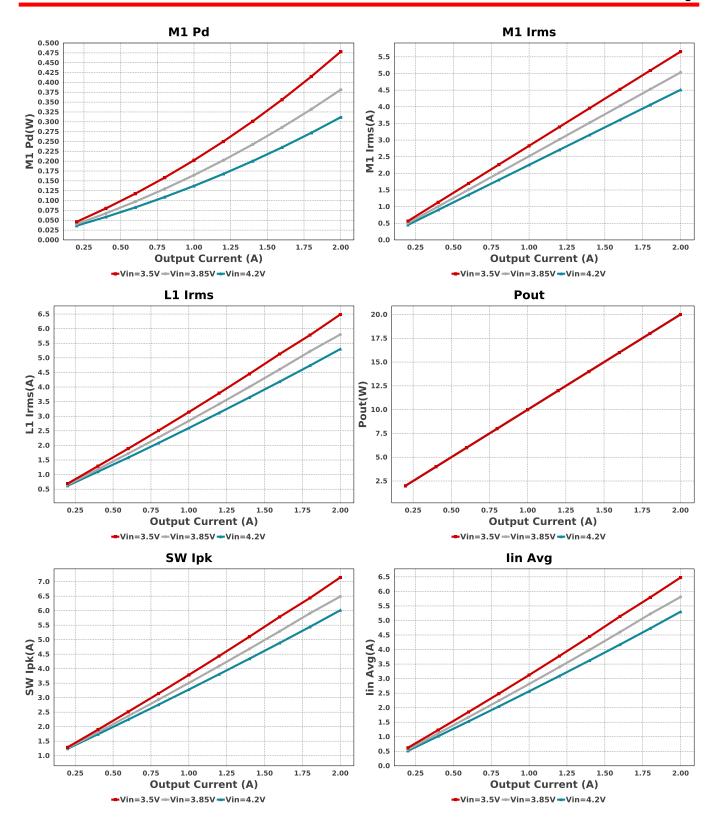
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
D1	Diodes Inc.	B540C-13-F	VF@Io= 550.0 mV VRRM= 40.0 V	1	\$0.19	SMC 83 mm ²
L1	NIC Components	NPI52P3R3MTRF	L= 3.3 μH 14.0 mOhm	1	\$0.36	IND_NPI52P 445 mm²
M1	Texas Instruments	CSD16327Q3	VdsMax= 25.0 V IdsMax= 22.0 Amps	1	\$0.34	DQG0008A 18 mm ²
Rcomp	Yageo	RC0603FR-072K2L Series=?	Res= 2.2 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
Rfadj	Yageo	RC0603FR-0727KL Series= ?	Res= 27.0 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
Rfb1	Vishay-Dale	CRCW04021K00FKED Series= CRCWe3	Res= 1000.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rfb2	CUSTOM	CUSTOM Series= ?	Res= 7.0 kOhm Power= 0.0 W Tolerance= 0.0%	1	NA	CUSTOM 0 mm ²
Rfilt	Vishay-Dale	CRCW0402100RFKED Series= CRCWe3	Res= 100.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rsense	Susumu Co Ltd	PRL1632-R008-F-T1 Series= PRL1632	Res= 8.0 mOhm Power= 1.0 W Tolerance= 1.0%	1	\$0.20	0612 11 mm ²
U1	Texas Instruments	LM3478MM/NOPB	Switcher	1	\$1.05	MUA08A 24 mm ²

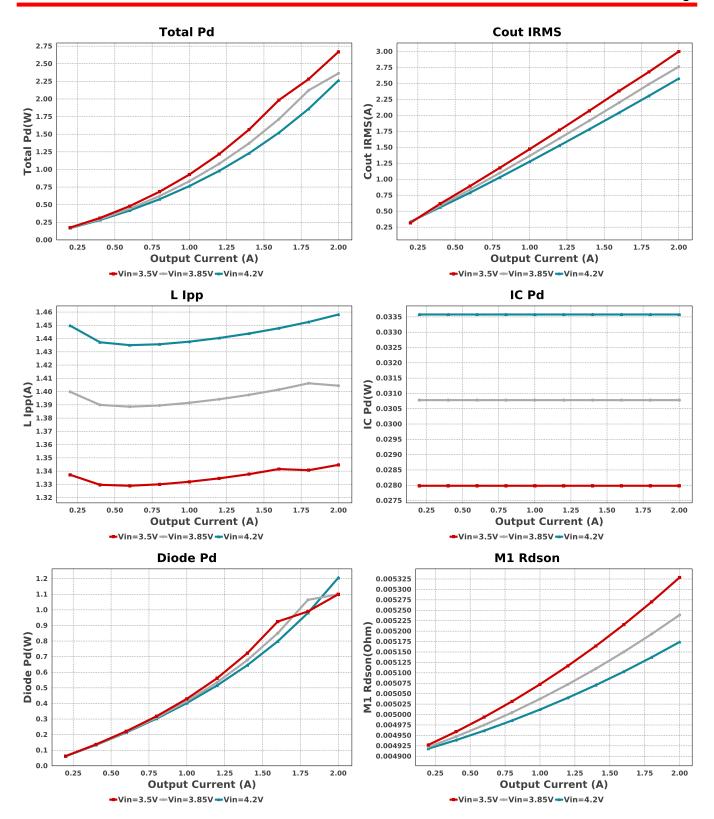


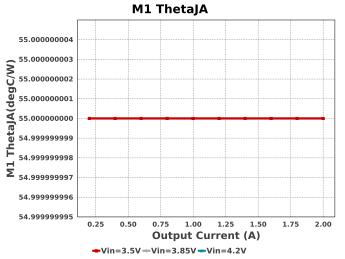


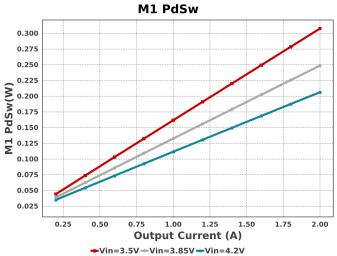


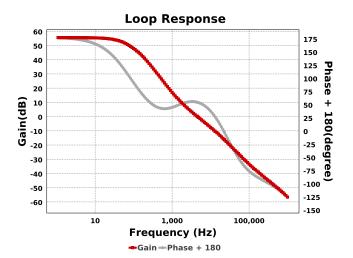












Operating Values

999	raming rando			
#	Name	Value	Category	Description
1.	BOM Count	16		Total Design BOM count
2.	Total BOM	NA		Total BOM Cost
3.	Cin IRMS	388.187 mA	Capacitor	Input capacitor RMS ripple current
4.	Cin Pd	13.562 mW	Capacitor	Input capacitor power dissipation
5.	Cout IRMS	2.999 A	Capacitor	Output capacitor RMS ripple current
6.	Cout Pd	98.926 mW	Capacitor	Output capacitor power dissipation
7.	D1 Tj	85.0 degC	Diode	D1 junction temperature
8.	Diode Pd	1.1 W	Diode	Diode power dissipation
9.	IC Pd	27.983 mW	IC	IC power dissipation
10.	IC Tj	35.597 degC	IC	IC junction temperature
11.	IC Tolerance	24.3 mV	IC	IC Feedback Tolerance
12.	ICThetaJA	200.0 degC/W	IC	IC junction-to-ambient thermal resistance
13.	lin Avg	6.477 A	IC	Average input current
14.	L lpp	1.345 A	Inductor	Peak-to-peak inductor ripple current
15.	L Pd	706.52 mW	Inductor	Inductor power dissipation
16.	L1 Irms	6.485 A	Inductor	Inductor ripple current
17.	M Vds Act	30.138 mV	Mosfet	M Vds
18.	M1 Irms	5.656 A	Mosfet	M1 MOSFET Irms
19.	M1 Pd	478.08 mW	Mosfet	M1 MOSFET total power dissipation
20.	M1 PdCond	170.45 mW	Mosfet	M1 MOSFET conduction losses
21.	M1 PdSw	307.63 mW	Mosfet	M1 MOSFET switching losses
22.	M1 Rdson	5.329 mOhm	Mosfet	Drain-Source On-resistance
23.	M1 ThetaJA	55.0 degC/W	Mosfet	MOSFET junction-to-ambient thermal resistance
24.	M1 TjOP	56.294 degC	Mosfet	M1 MOSFET junction temperature
25.	Cin Pd	13.562 mW	Power	Input capacitor power dissipation
26.	Cout Pd	98.926 mW	Power	Output capacitor power dissipation
27.	Diode Pd	1.1 W	Power	Diode power dissipation
28.	IC Pd	27.983 mW	Power	IC power dissipation
29.	L Pd	706.52 mW	Power	Inductor power dissipation
30.	M1 Pd	478.08 mW	Power	M1 MOSFET total power dissipation
31.	M1 PdCond	170.45 mW	Power	M1 MOSFET conduction losses
32.	M1 PdSw	307.63 mW	Power	M1 MOSFET switching losses
				S

#	Name	Value	Category	Description
33.	Rfb Pd	12.5 mW	Power	Rfb Power Dissipation
34.	Rsense Pd	0.0 W	Power	LED Current Rsns Power Dissipation
35.	Total Pd	2.669 W	Power	Total Power Dissipation
36.	Rfb Pd	12.5 mW	Resistor	Rfb Power Dissipation
37.	Rsense Pd	0.0 W	Resistor	LED Current Rsns Power Dissipation
38.	Cross Freq	3.819 kHz	System	Bode plot crossover frequency
	•		Information	
39.	Duty Cycle	69.104 %	System	Duty cycle
	• •		Information	• •
40.	Efficiency	88.225 %	System	Steady state efficiency
	•		Information	•
41.	FootPrint	855.0 mm ²	System	Total Foot Print Area of BOM components
			Information	'
42.	Frequency	536.973 kHz	System	Switching frequency
	- 17		Information	2 3 (1.1.1)
43.	Gain Marg	-14.774 dB	System	Bode Plot Gain Margin
			Information	
44.	lout	2.0 A	System	lout operating point
			Information	31
45.	Low Freq Gain	53.742 dB	System	Gain at 1Hz
	'		Information	
46.	Mode	CCM	System	Conduction Mode
			Information	
47.	Phase Marg	54.086 deg	System	Bode Plot Phase Margin
		3	Information	9
48.	Pout	20.0 W	System	Total output power
			Information	
49.	SW lpk	7.146 A	System	Peak switch current
	- '		Information	
50.	Vin	3.5 V	System	Vin operating point
			Information	sheraming herm
51.	Vout	10.0 V	System	Operational Output Voltage
			Information	-p
52.	Vout Actual	10.08 V	System	Vout Actual calculated based on selected voltage divider resistors
		- v - ·	Information	
53.	Vout Tolerance	2.829 %	System	Vout Tolerance based on IC Tolerance (no load) and voltage divider
		/-	Information	resistors if applicable
54.	Vout p-p	78.922 mV	System	Peak-to-peak output ripple voltage
	eman in	*:=== :::•	Information	· · · · · · · · · · · · · · · · · · ·

Design Inputs

Name	Value	Description	
lout	2.0	Maximum Output Current	
VinMax	4.2	Maximum input voltage	
VinMin	3.5	Minimum input voltage	
VinTyp	3.8	Typical input voltage	
Vout	10.0	Output Voltage	
base_pn	LM3478	Base Product Number	
source	DC	Input Source Type	
Та	30.0	Ambient temperature	
UserFsw	542.0 k	Customer Selected Frequency	

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 3.5V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum lout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.

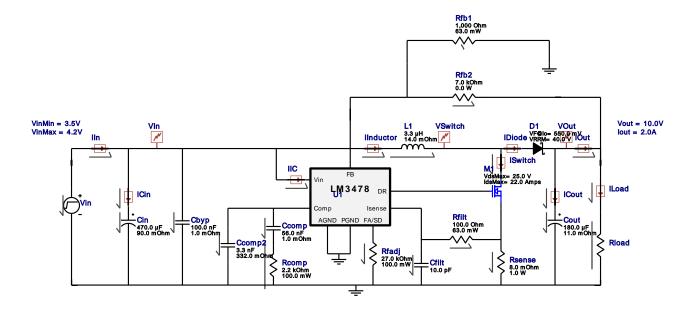


WEBENCH[®] Electrical Simulation Report

Design Id = 3

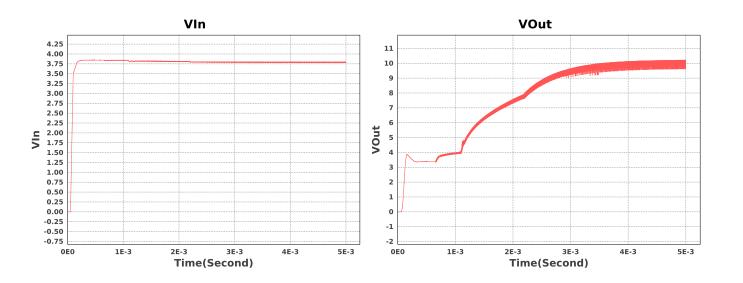
sim_id = 13

Simulation Type = Startup



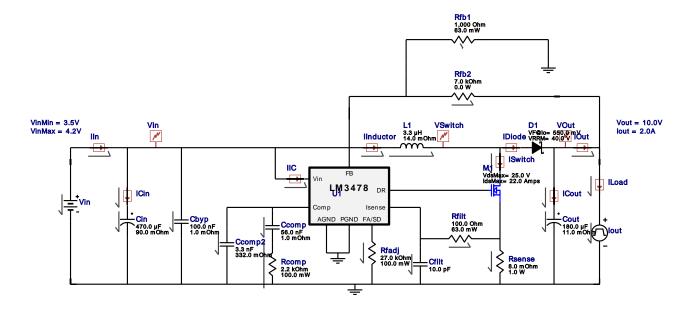
Simulation Parameters

# Name	Parameter Name	Description	Values
1. Rload		Load Resistance	5.0 Ohm



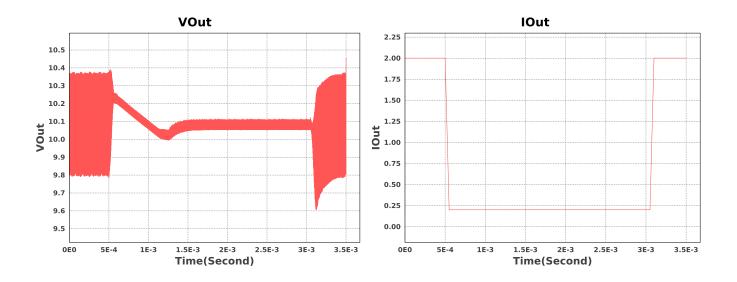
Design Id = 3 sim_id = 14

Simulation Type = Load Transient



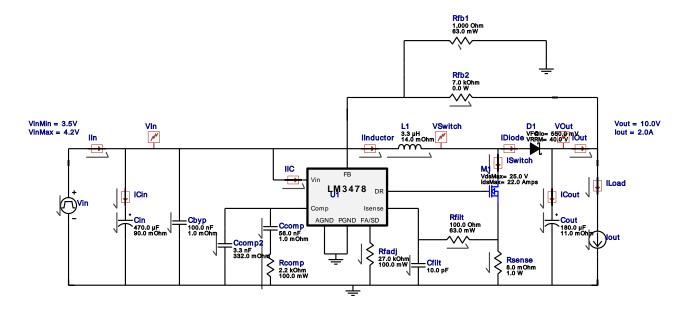
Simulation Parameters

# 1	Name	Parameter Name	Description	<u>Values</u>
1. le	lout	signal_type	Signal Type	PULSE
		l1	Initial Current	2.0 A
		I2	Peak Current	0.2 A
		Td	Initial Delay Time	0.5m Sec
		Tr	Rise Time	50u Sec
		Tf	Fall Time	50u Sec
		Pw	Pulse Width	2.5m Sec



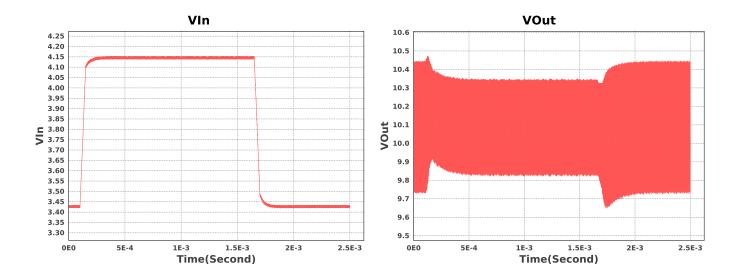
Design Id = 3 sim_id = 15

Simulation Type = Input Transient



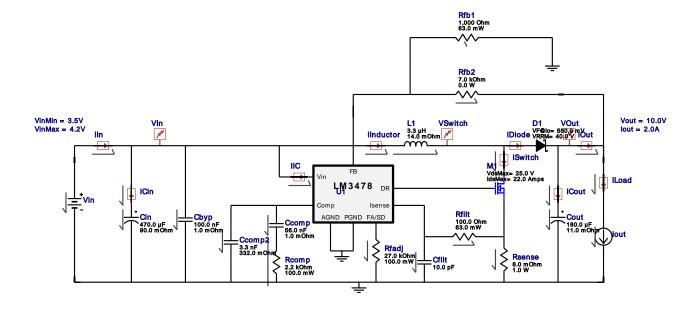
Simulation Parameters

#	Name	Parameter Name	Description	Values
1.	lout	1	Load Current	2.0 A



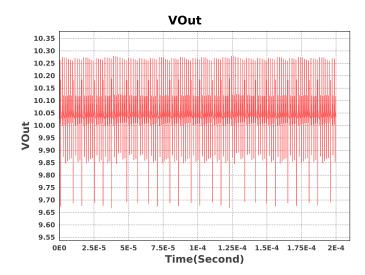
Design Id = 3 sim_id = 16

Simulation Type = Steady State



Simulation Parameters

#	Name	Parameter Name	Description	Values
1.	lout	1	Load Current	2.0 A



Design Assistance

- 1. Master key: 98CBF142CCCD1FF284E86A30ABA5F8D7[v1]
- 2. LM3478 Product Folder: http://www.ti.com/product/LM3478: contains the data sheet and other resources.

Important Notice and Disclaimer

TI provides technical and reliability data (including datasheets), design resources (including reference designs), application or other design advice, web tools, safety information, and other resources AS IS and with all faults, and disclaims all warranties. These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

Providing these resources does not expand or otherwise alter TI's applicable Terms of Sale or other applicable terms available either on ti.com or provided in conjunction with TI products.