

## Digital System Design Project 3 – State Minimization

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### 1. My KISS file

a) Input

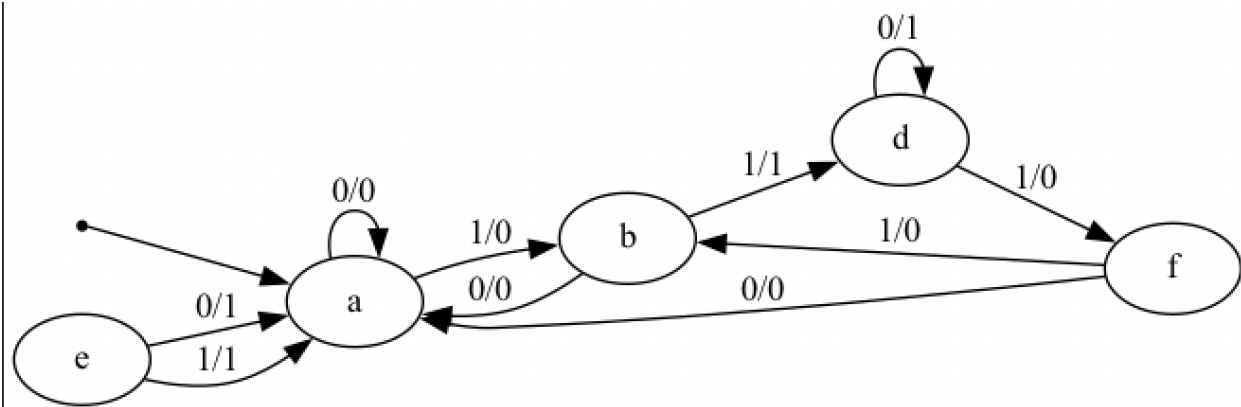
```
.start_kiss  
.i 1  
.o 1  
.p 10  
.s 5  
.r a  
0 a a 0  
1 a b 0  
0 b a 0  
1 b d 1  
0 d d 1  
1 d f 0  
0 e a 1  
1 e a 1  
0 f a 0  
1 f b 0  
.end_kiss
```

b) output

```
.start_kiss  
.i 1  
.o 1  
.p 8  
.s 4  
.r a  
0 a a 0  
1 a b 0  
0 b a 0  
1 b d 1  
0 d d 1  
1 d a 0  
0 e a 1  
1 e a 1  
.end_kiss
```

## 2. STG

a) Before: (Also work if state “c” doesn’t exist)



b) After: (Wonder how to reach state “e”)

