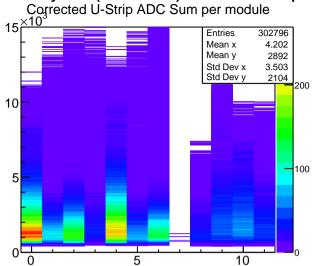
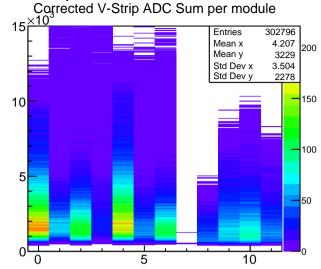
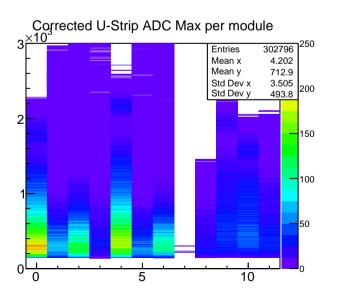
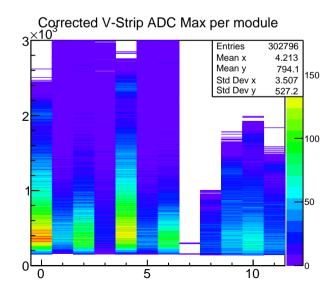


Summary Plots(Run #11994) 3: Corrected Strip ADC Sums and Max per module









Summary Plots(Run #11994) 4: ADC Asymmetry by module Module 0 Module 1 Module 2 Module 3 Module 4 Module 5 ×10³ ×10³ 150 400 200 150 300 100 150 100 0.5 200 0.5 100 50 100 -1 -0.5 0 0.5 -1 -0.5 0 0.5 -1 -0.5 0 0.5 -1 -0.5 0 0.5 -1 -0.5 0 0.5 -1 -0.5 0 0.5 Module 6 Module 7 Module 8 Module 9 Module 10 Module 11 300 150 400 400 300 200 100 0.5 200 200 200 100 50 100 -1 -0.5 0 0.5 1 -1 -0.5 0 0.5 1 -1 -0.5 0 0.5 1 -1 -0.5 0 0.5 -1 -0.5 0 0.5 1 -1 -0.5 0 0.5

