ADC Asymmetry per module

Entries	231708
Mean x	3.913
Mean y	0.008518
Std Dev x	2.637
Std Dev y	0.3193

ADC Average per module

Entries	231708
Mean x	3.897
Mean y	7198
Std Dev x	2.635
Std Dev y	5584
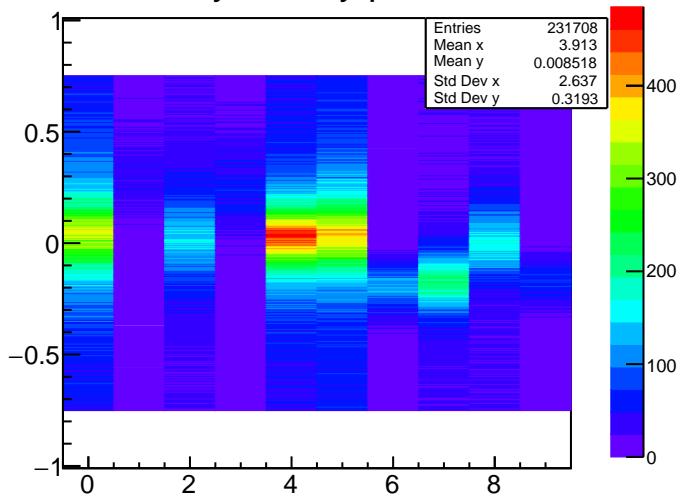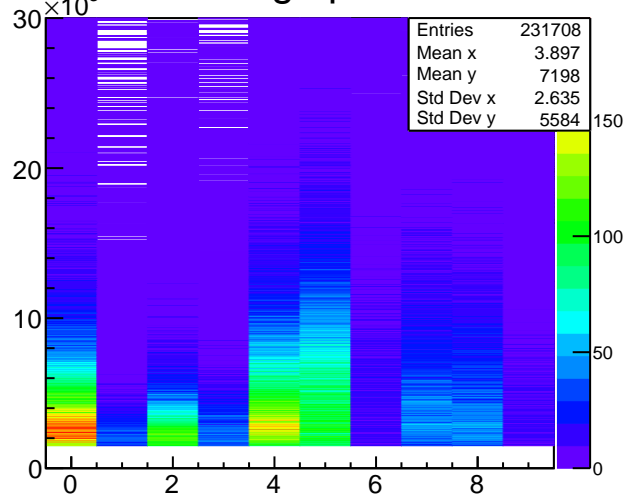
Number X-strips per module

Entries	231708
Mean x	3.913
Mean y	3.68
Std Dev x	2.637
Std Dev y	1.615
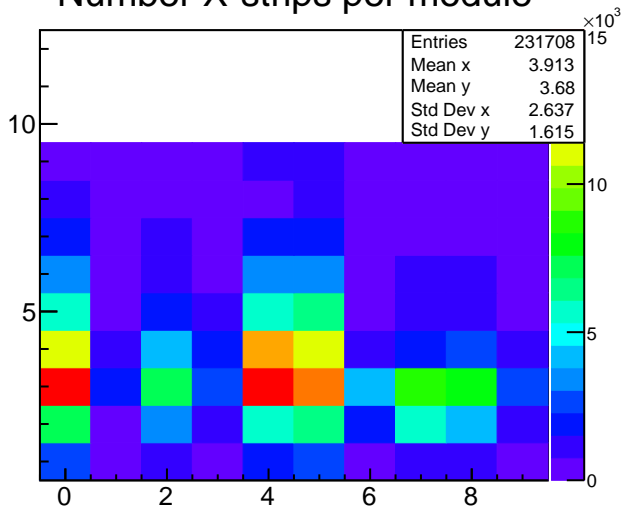
Number Y-strips per module

Entries	231708
Mean x	3.913
Mean y	3.368
Std Dev x	2.637
Std Dev y	1.578

Corrected ADC Average per module

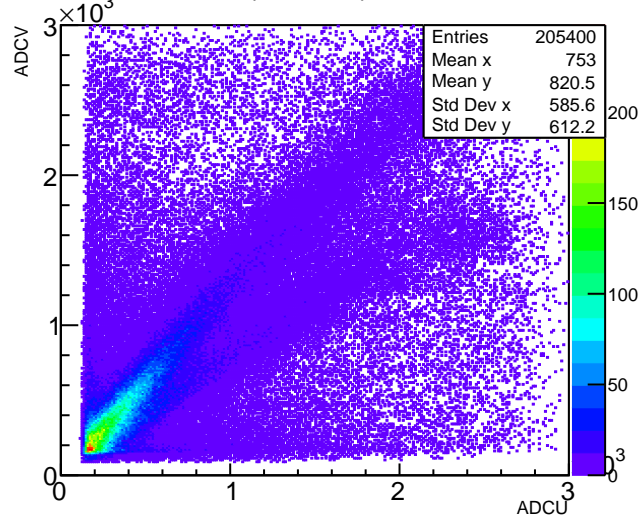| Entries | 205400 |
Mean x	3.922
Mean y	7534
Std Dev x	2.653
Std Dev y	5617
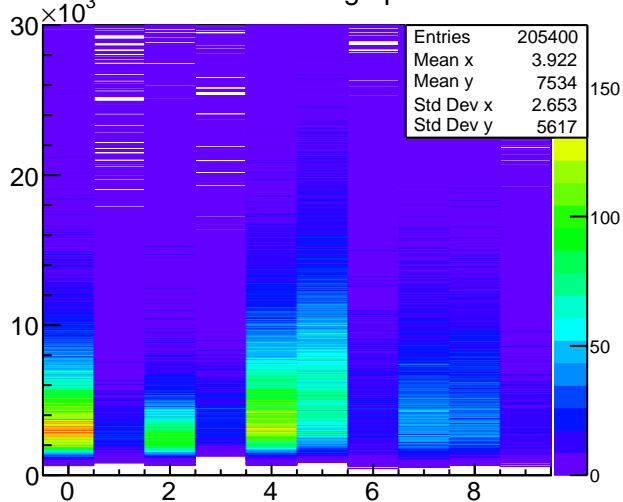
Corrected ADC Cluster Sum V vs U for all hits

Entries	205400
Mean x	6789
Mean y	6835
Std Dev x	4893
Std Dev y	4877
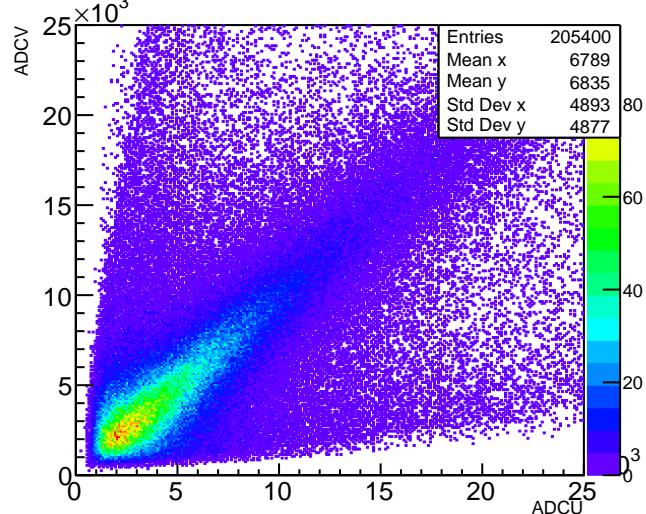
Corrected Max Strip Sum V vs U for all hits

Entries	205400
Mean x	3349
Mean y	3667
Std Dev x	2685
Std Dev y	2829
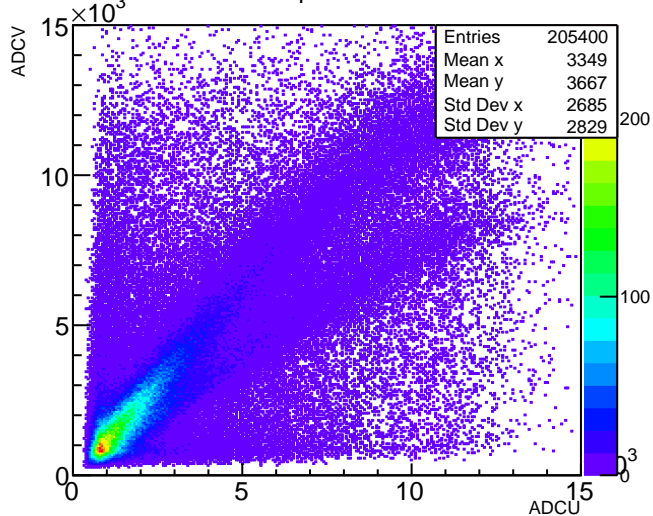
Corrected Max Strip Max Sample V vs U for all hits

Entries	205400
Mean x	753
Mean y	820.5
Std Dev x	585.6
Std Dev y	612.2

U-Strip ADC Sum per module

Entries	231708
Mean x	3.913
Mean y	3280
Std Dev x	2.637
Std Dev y	2670

V-Strip ADC Sum per module

Entries	231708
Mean x	3.913
Mean y	3558
Std Dev x	2.637
Std Dev y	2806

U-Strip ADC Max per module

Entries	231708
Mean x	3.913
Mean y	738.6
Std Dev x	2.637
Std Dev y	583

V-Strip ADC Max per module

Entries	231708
Mean x	3.913
Mean y	797.6
Std Dev x	2.637
Std Dev y	604.4

Summary Plots(Run #12313) 3: Corrected Strip ADC Sums and Max per module

Corrected U-Strip ADC Sum per module

Entries	205400
Mean x	3.93
Mean y	3365
Std Dev x	2.65
Std Dev y	2709
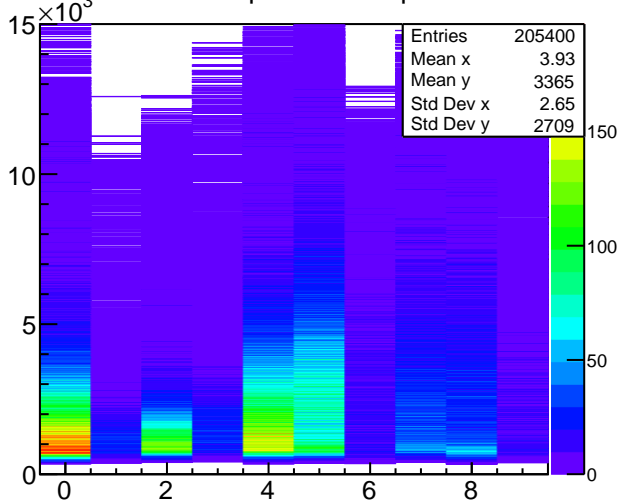
Corrected V-Strip ADC Sum per module

Entries	205400
Mean x	3.931
Mean y	3669
Std Dev x	2.652
Std Dev y	2832
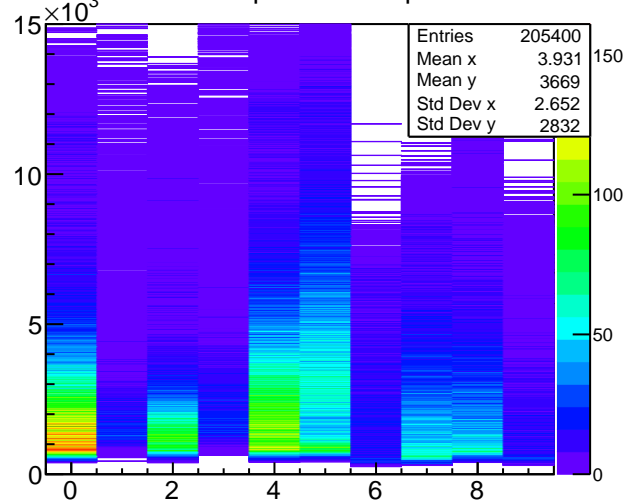
Corrected U-Strip ADC Max per module

Entries	205400
Mean x	3.93
Mean y	757.9
Std Dev x	2.65
Std Dev y	591.8
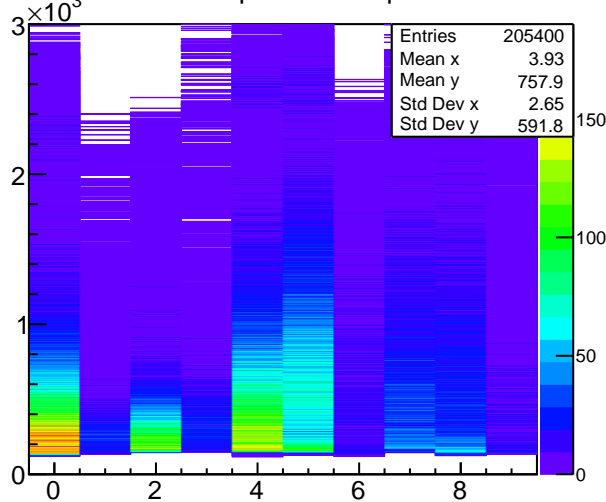
Corrected V-Strip ADC Max per module

Entries	205400
Mean x	3.931
Mean y	820.9
Std Dev x	2.654
Std Dev y	612.7
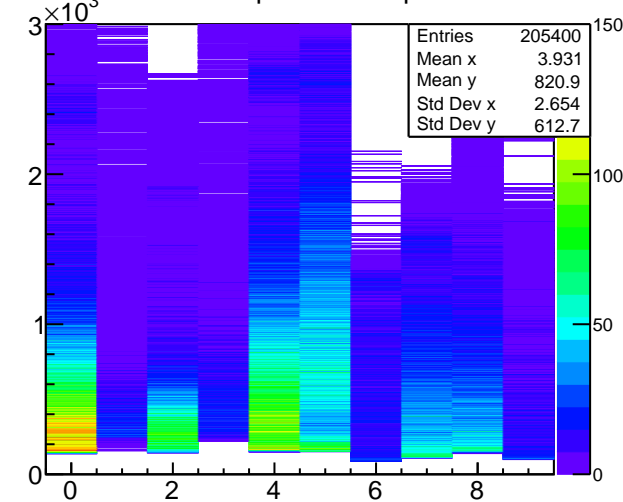
Summary Plots(Run #12313) 4: ADC Asymmetry by module

Module 0 — Entries 46956, Mean 0.03047, Std Dev 0.3211
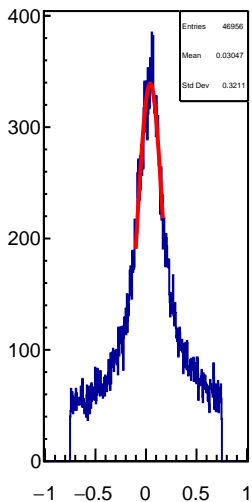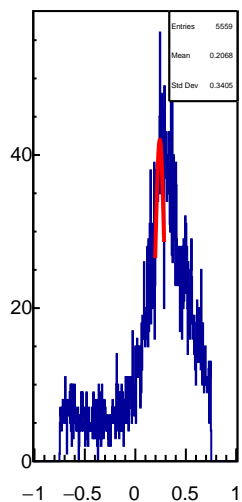
Module 1 — Entries 5559, Mean 0.2068, Std Dev 0.3405

Module 2 — Entries 20555, Mean 0.005132, Std Dev 0.3202

Module 3 — Entries 8153, Mean 0.1988, Std Dev 0.3133

Module 4 — Entries 47577, Mean 0.01726, Std Dev 0.2926

Module 5 — Entries 48749, Mean 0.03133, Std Dev 0.3171

Module 6 — Entries 8774, Mean −0.1604, Std Dev 0.2646

Module 7 — Entries 20291, Mean −0.1243, Std Dev 0.3046

Module 8 — Entries 19117, Mean −0.001594, Std Dev 0.3181
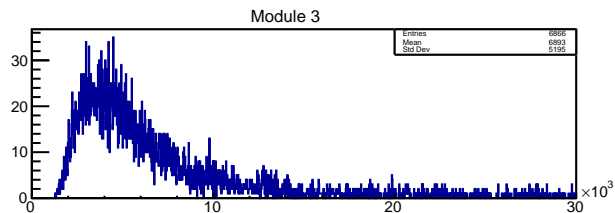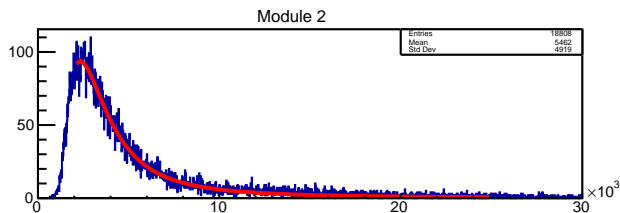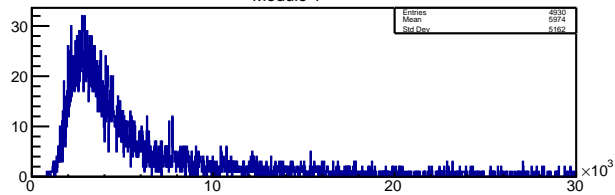
Module 9 — Entries 5977, Mean −0.1207, Std Dev 0.3038
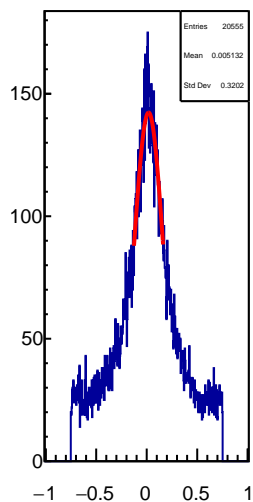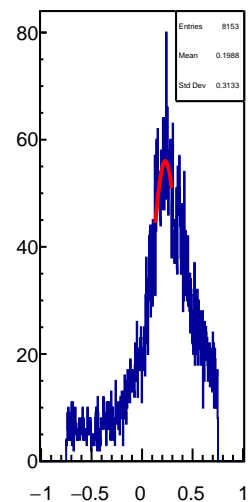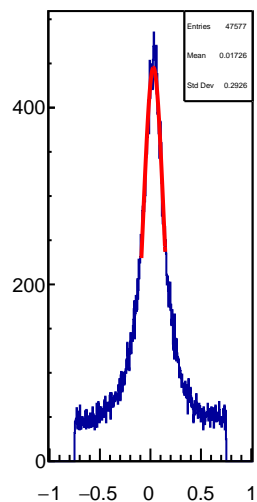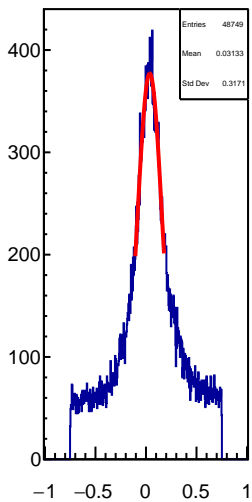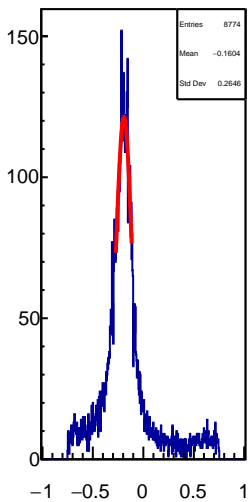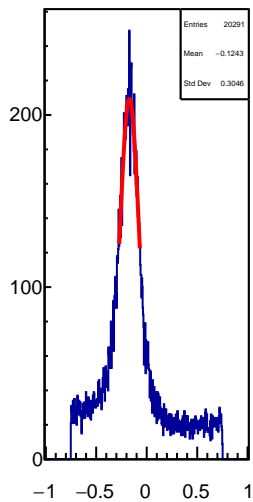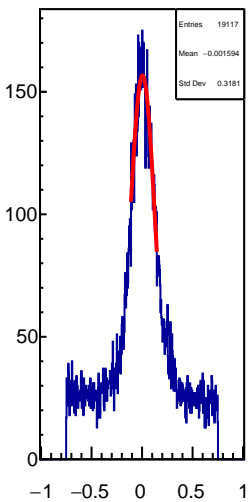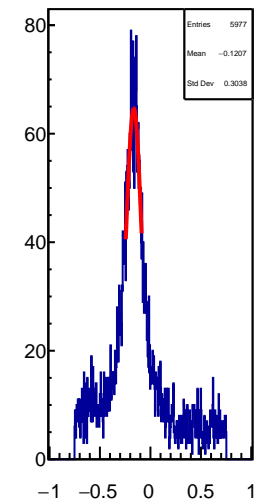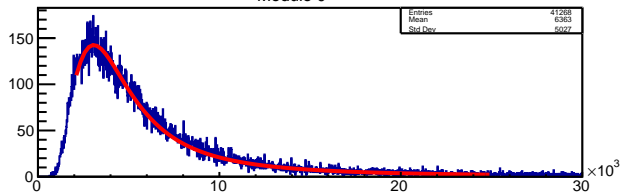
Summary Plots(Run #12313) 5: ADC Distribution by module
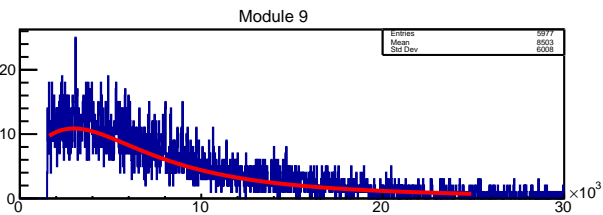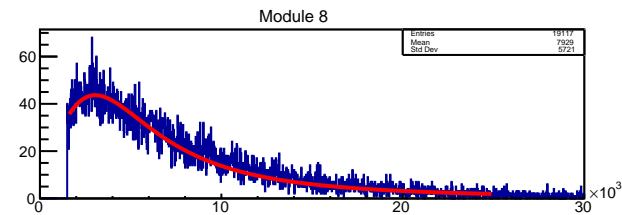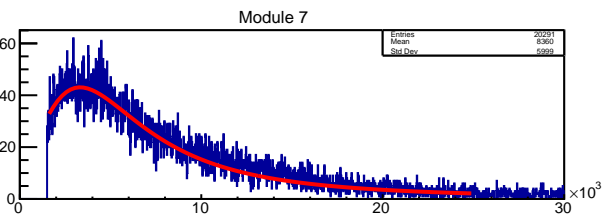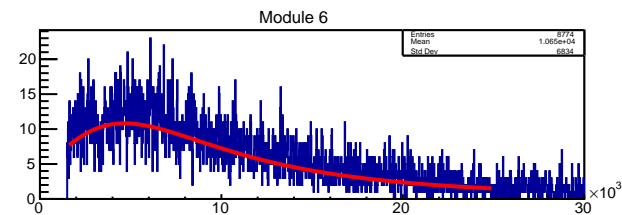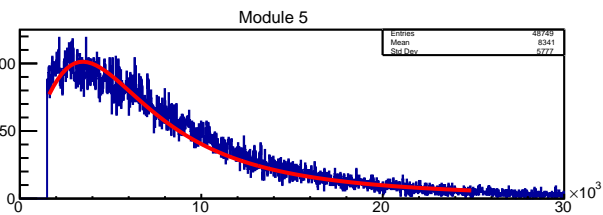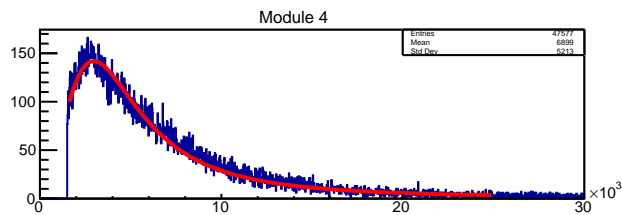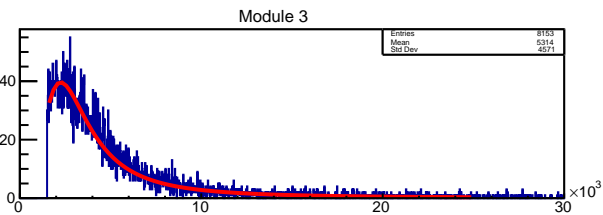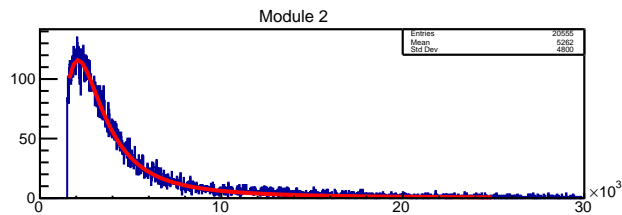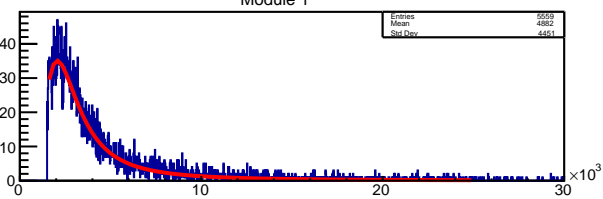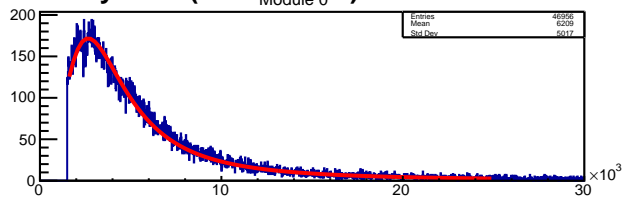
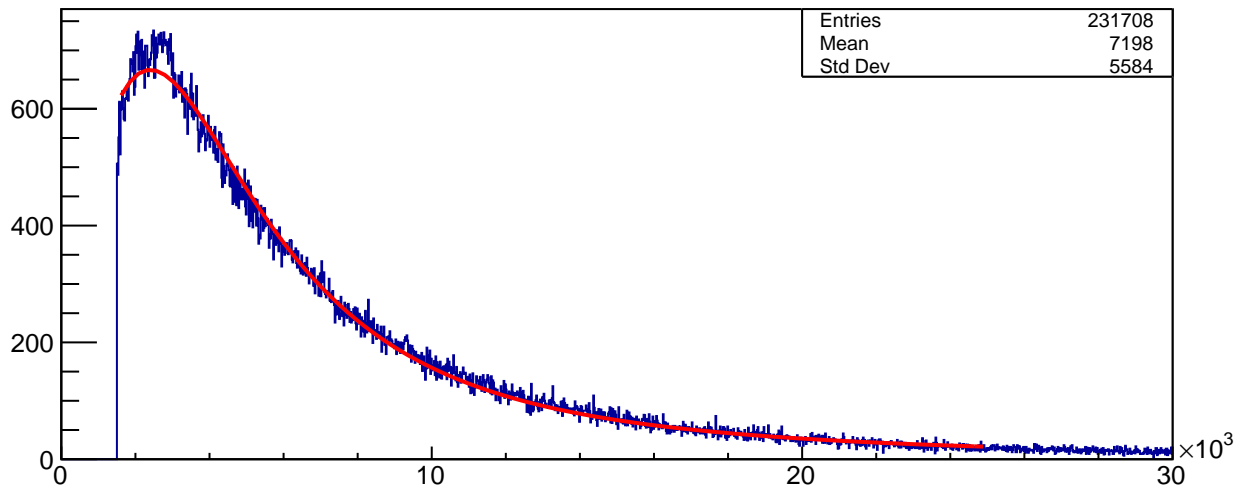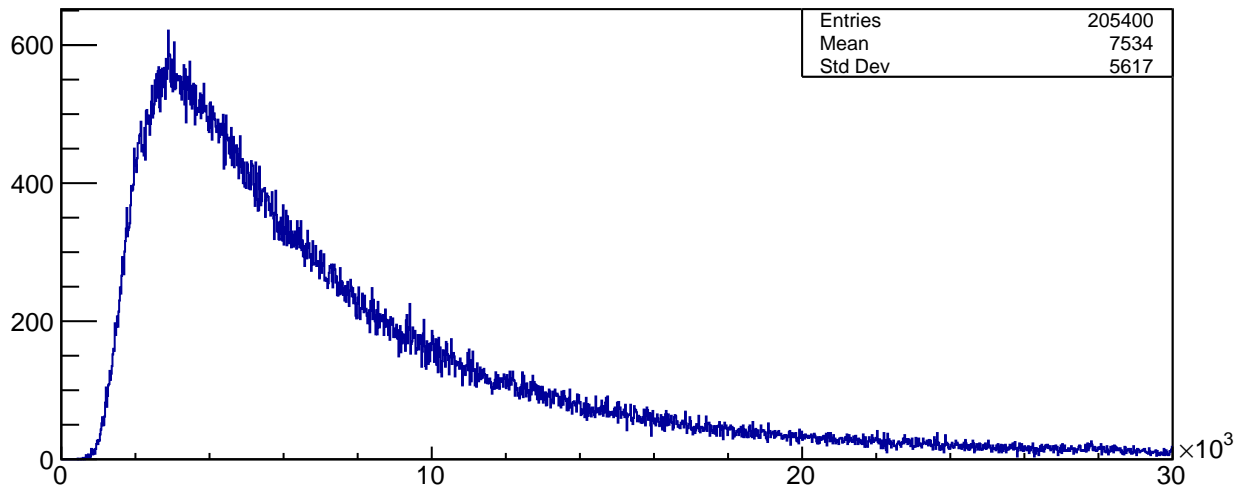# Summary Plots(Run #12313) 6: Corrected ADC Average per module
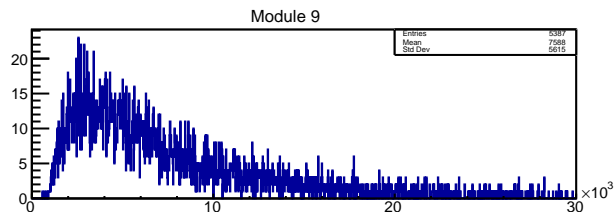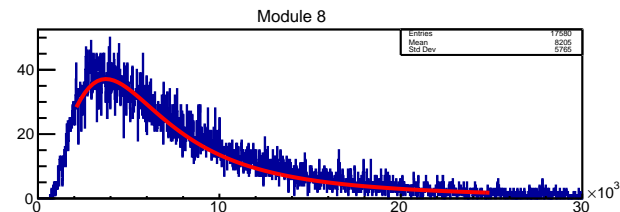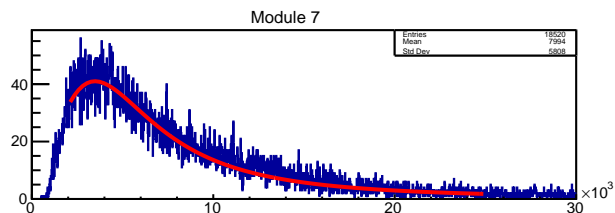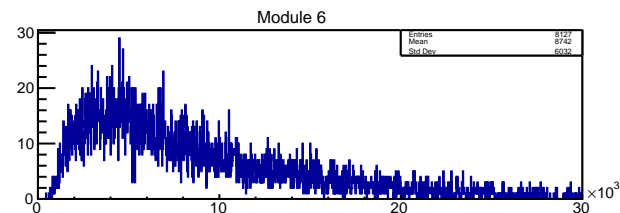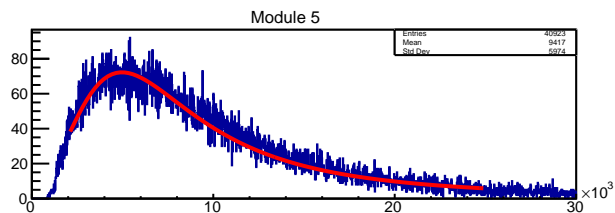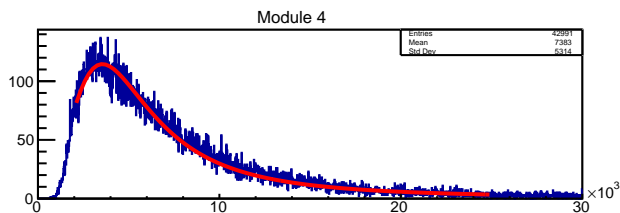
Uncorrected

Entries	231708
Mean	7198
Std Dev	5584

Corrected

Entries	205400
Mean	7534
Std Dev	5617

Summary Plots(Run #12313) 8: Corrected ADC Asymmetry all hits

Entries	205400
Mean	−0.003901
Std Dev	0.2769