

Summary Plots(Run #13442) 2: Strip ADC Sums and Max per module

U₃Strip ADC Sum per module

V₃St

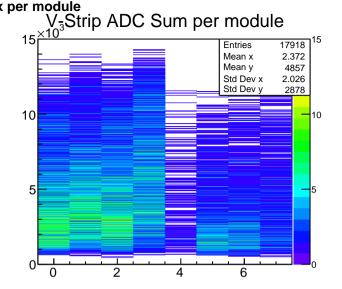
15

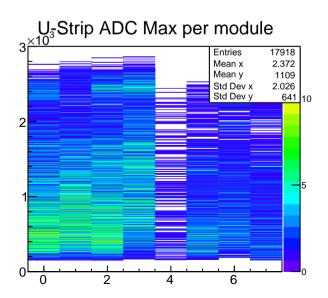
Entries 17918
Mean x 2.372
Mean y 4481
Std Dev y 2812

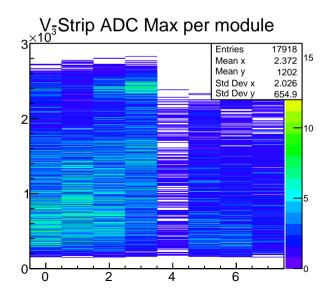
10

10

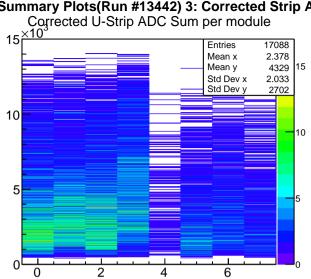
5

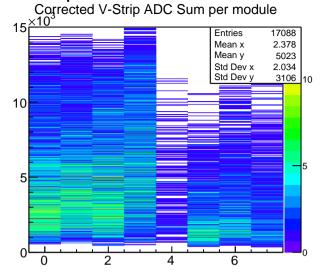


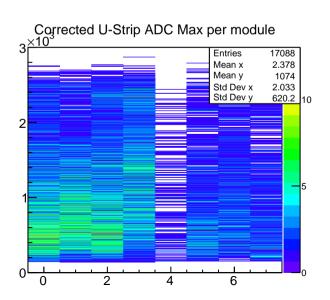


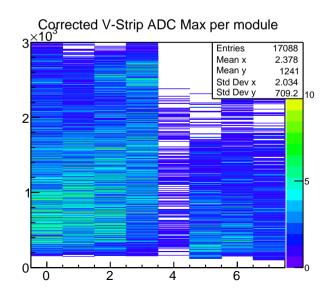


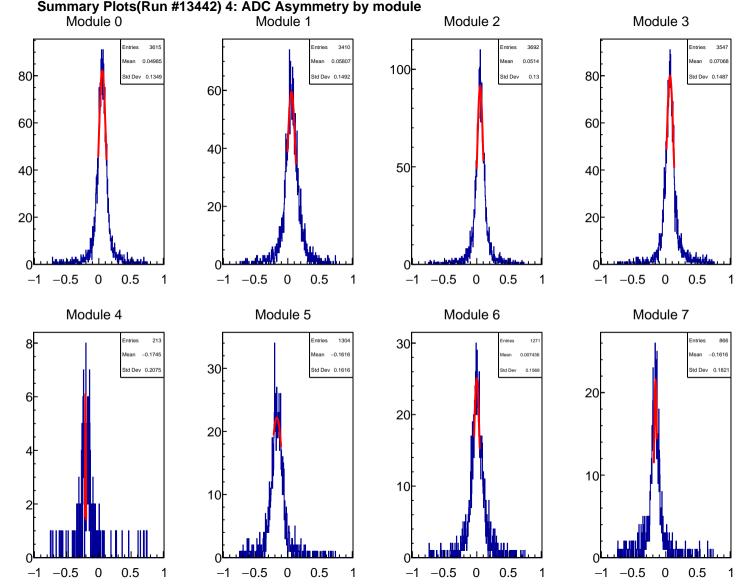
Summary Plots(Run #13442) 3: Corrected Strip ADC Sums and Max per module

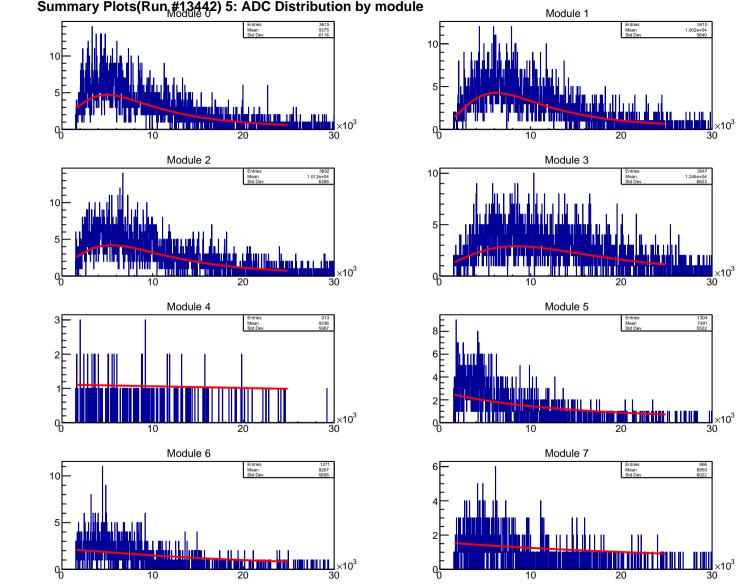


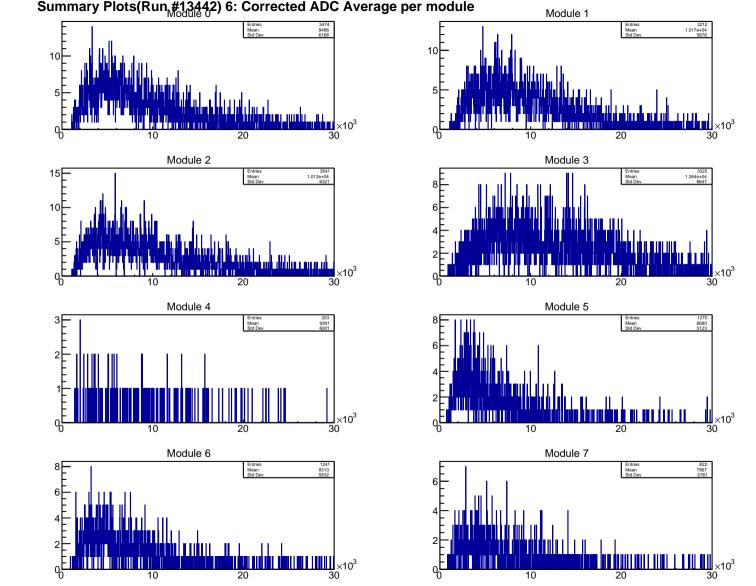












Summary Plots(Run #13442) 7: ADC Average for all hits Uncorrected

