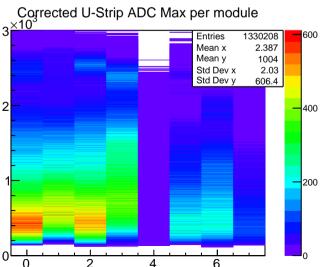
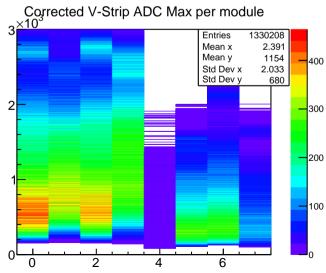
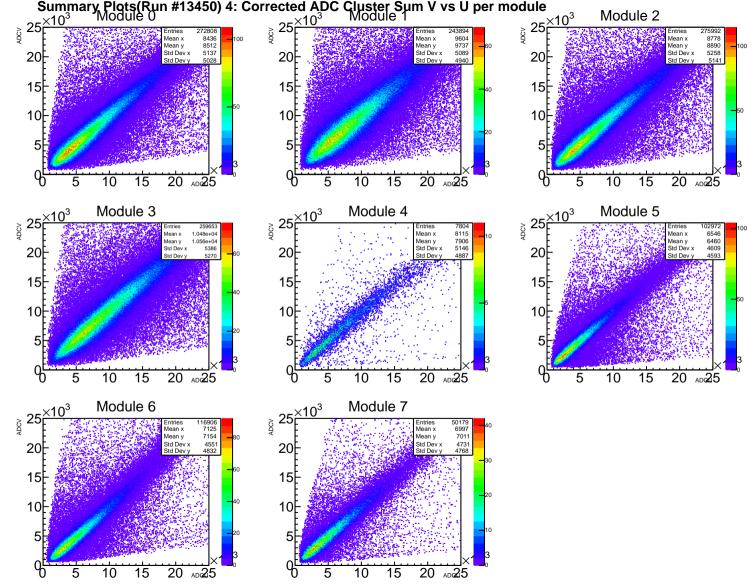
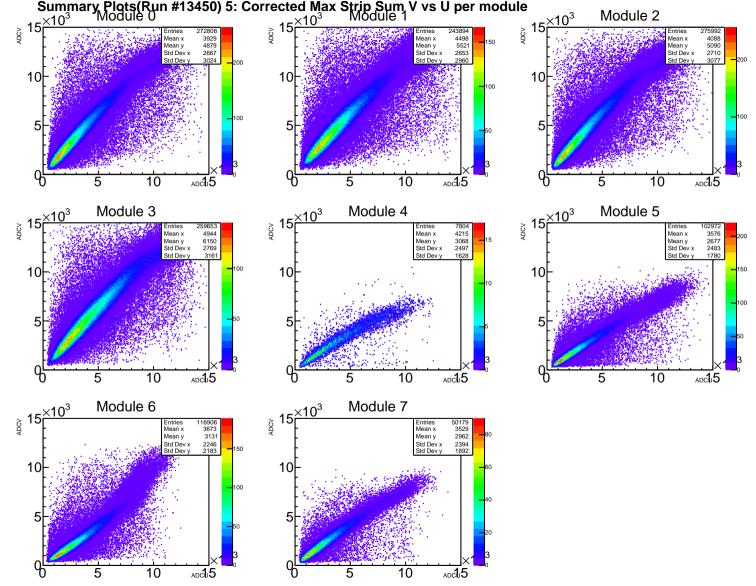


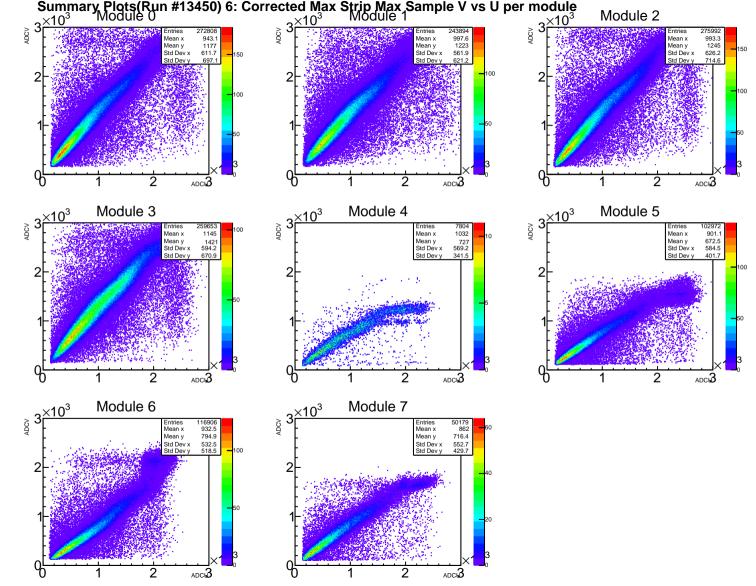
Summary Plots(Run #13450) 3: Corrected Strip ADC Sums and Max per module Corrected U-Strip ADC Sum per module 15×10³ Corrected V-Strip ADC Sum per module 15×10³ 1330208 1330208 Entries Entries Mean x 2.386 Mean x 2.387 Mean y 4214 Mean y 4881 600 Std Dev x 2.03 Std Dev x 2.031 Std Dev y 2695 Std Dev y 3069 400 10 10 400 200 200 4 2 Corrected U-Strip ADC Max per module Corrected V-Strip ADC Max per module 3×10 600 Entries 1330208 Entries 1330208 2.387 Mean x 2.391 Mean x Mean y 1004 Mean y 1154 2.033 Std Dev x 2.03 Std Dev x Std Dev y Std Dev y 606.4 680



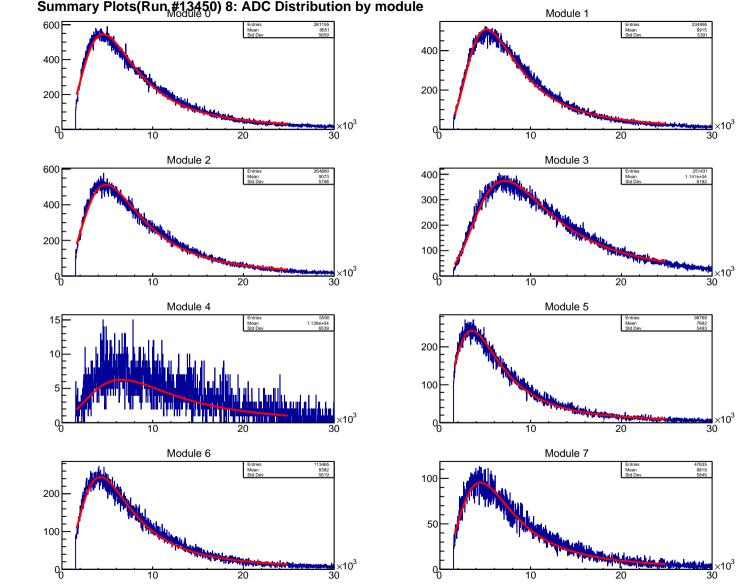


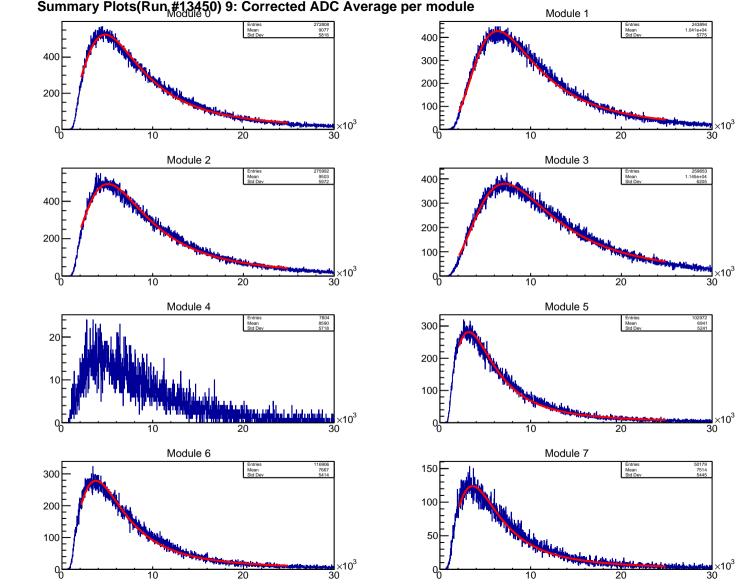


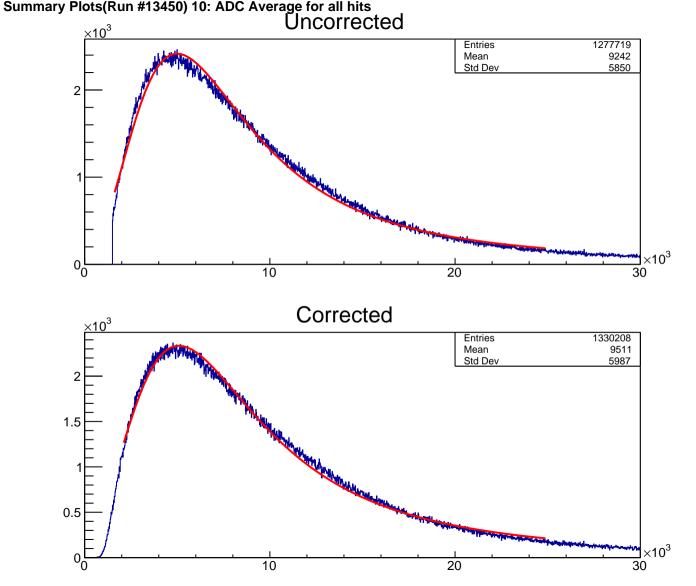


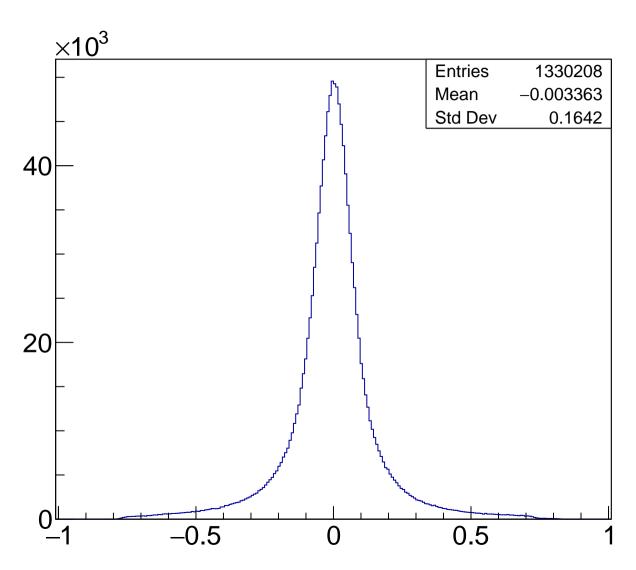


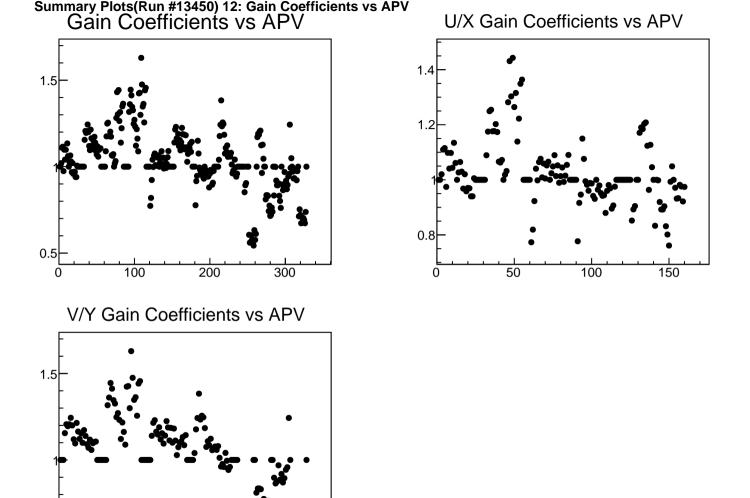
Summary Plots(Run #13450) 7: ADC Asymmetry by module $\frac{100}{\times 10^3}$ Module 0 $\times 10^3 \text{ Module 1}$ $\times 10^3$ Module 2 $\times 10^3$ Module 3 Entries 261155 Entries 234995 Mean 0.0416 Mean 0.04675 Mean 0.04089 Mean 0.05292 Std Dev 0.1616 Std Dev 0.1793 Std Dev 0.1586 Std Dev 0.1591 -0.50 0.5 -1 -0.50.5 -1 -0.50.5 -1 -0.50 0.5 0 0 $\times 10^3$ Module 5 $\times 10^3$ Module 6 Module 4 Module 7 Entries Entries 98768 Entries 113465 Entries 47635 Mean -0.1722 Mean 0.01695 Mean -0.215 Mean -0.1612 1.5 1.5 800 Std Dev 0.1557 Std Dev 0.1778 Std Dev 0.1803 Std Dev 0.1773 100 600 400 50 0.5 0.5 200 -1 -0.5-0.50.5 0.5 -1 -0.50.5 -0.50.5 0 0 0 0



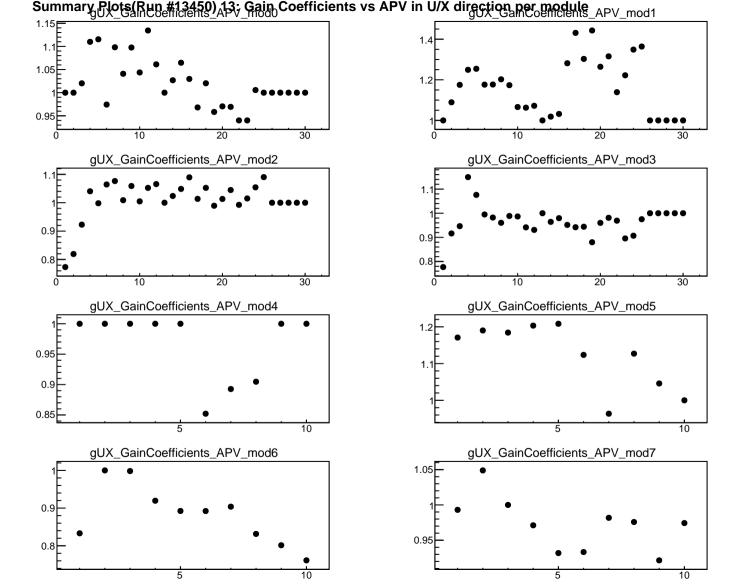


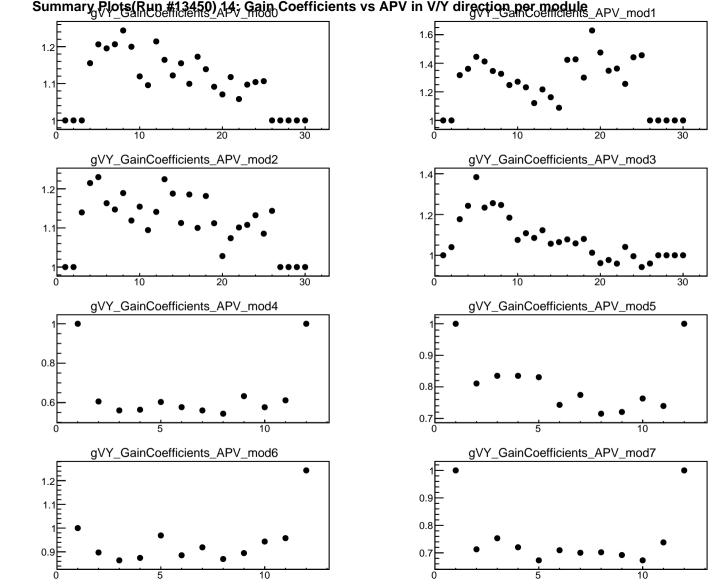






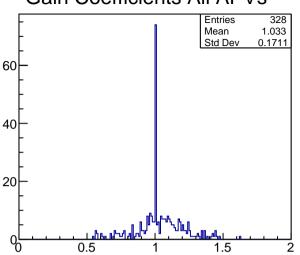
0.5

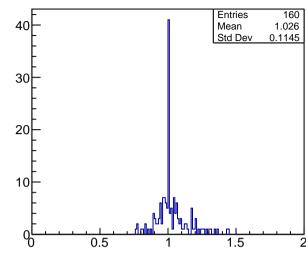




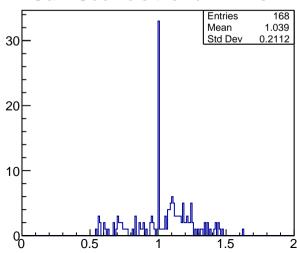
Summary Plots(Run #13450) 15: Gain Coefficient Histograms Gain Coefficients All APVs

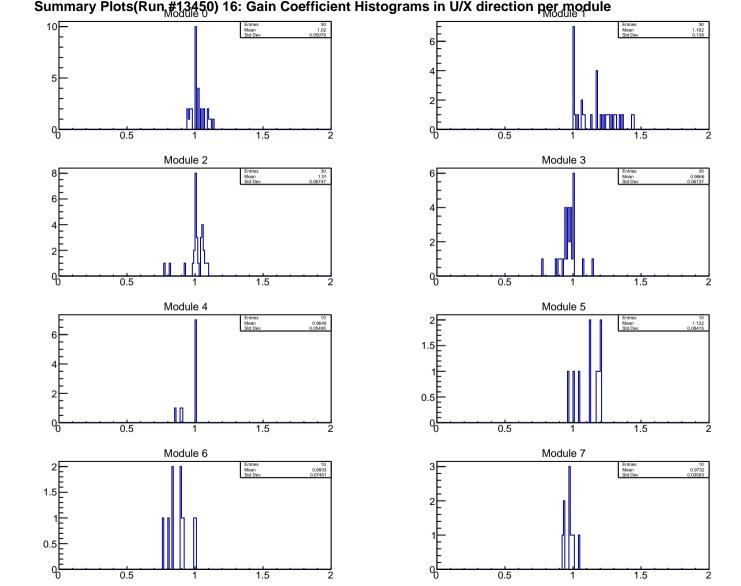
Gain Coefficients U/X APVs





Gain Coefficieitns V/Y APVs





Summary Plots(Run #13450) 17: Gain Coefficient Histograms in V/Y direction per module 0.5 1.5 0.5 Module 2 Module 3 Entries Mean Std Dev Entries Mean Std Dev 0.5 0.5 1.5 1.5 Module 4 Module 5 0.5 1.5 1.5 Module 6 Module 7 0.9431 0.09949 1.5 0.5 0.5 1.5 0.5 1.5

