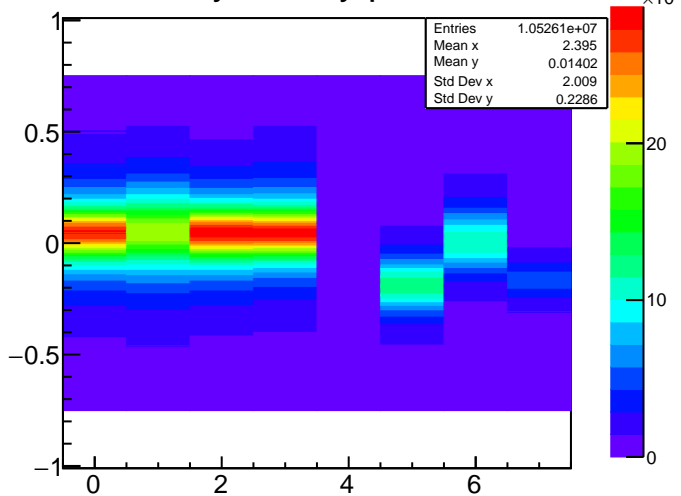
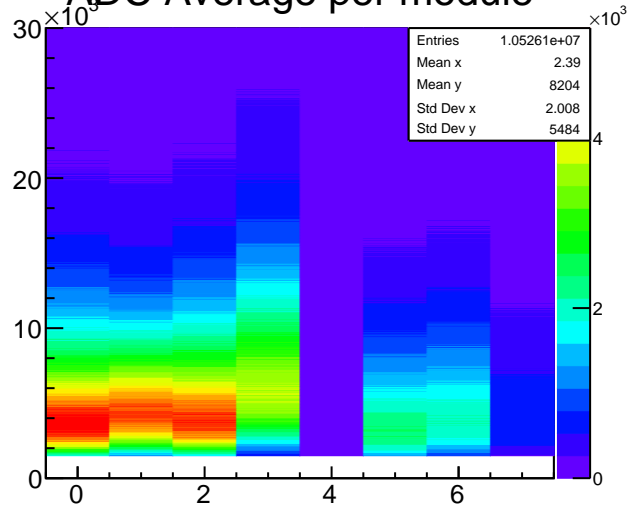


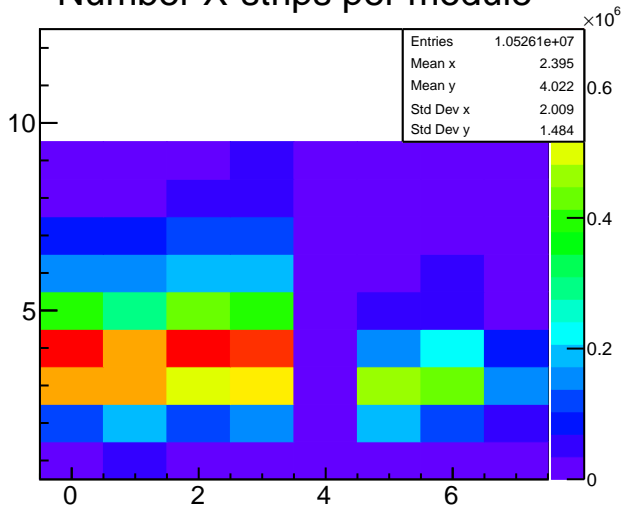
ADC Asymmetry per module



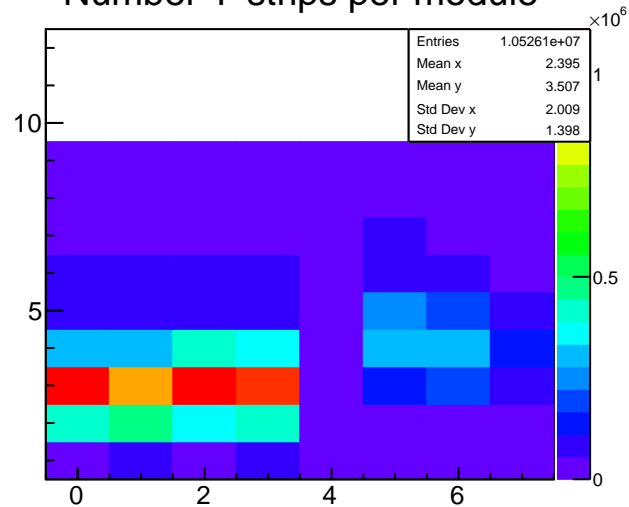
ADC Average per module



Number X-strips per module

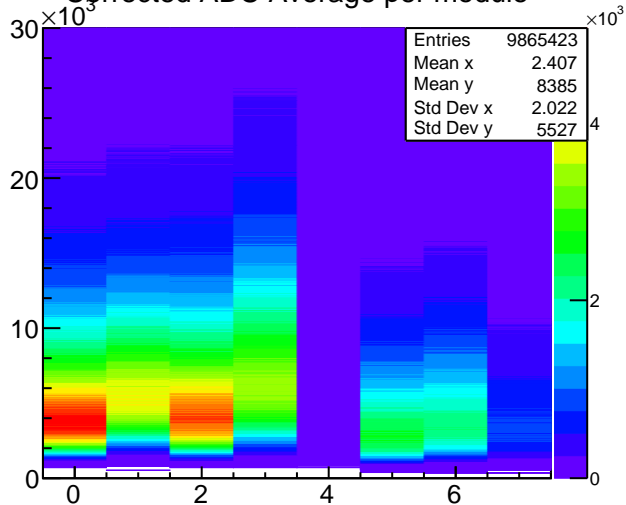


Number Y-strips per module

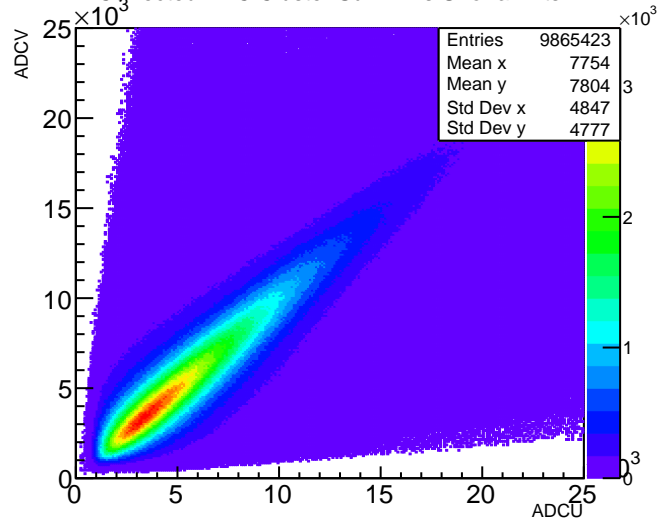


Summary Plots(Run #13459) 1: Page 1

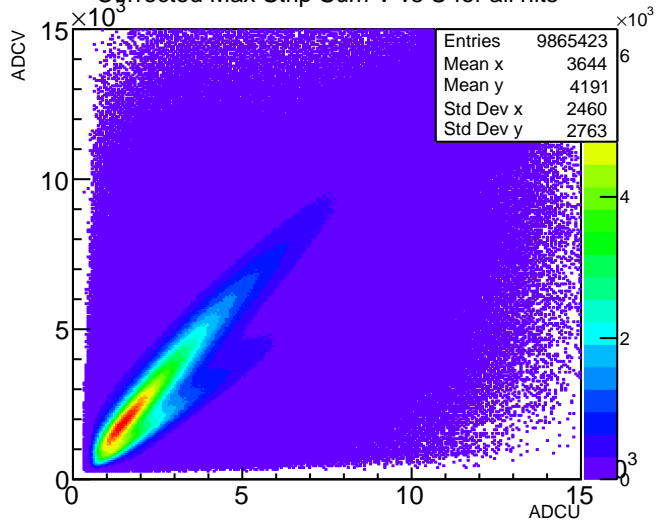
Corrected ADC Average per module



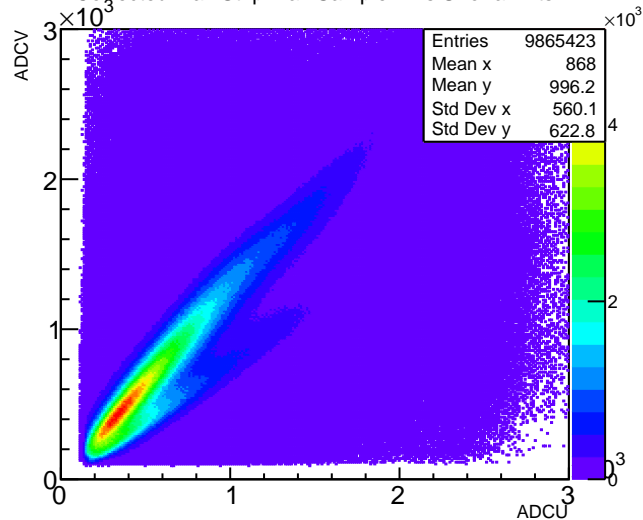
Corrected ADC Cluster Sum V vs U for all hits



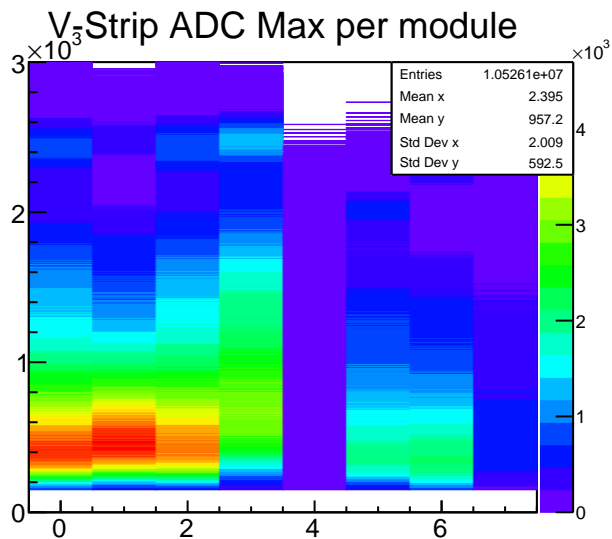
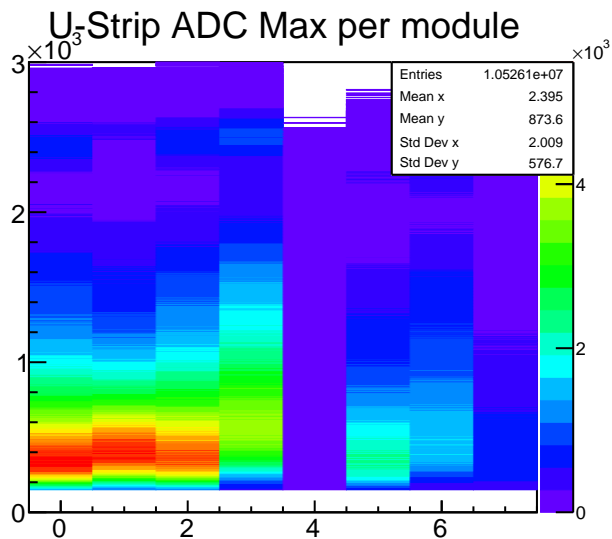
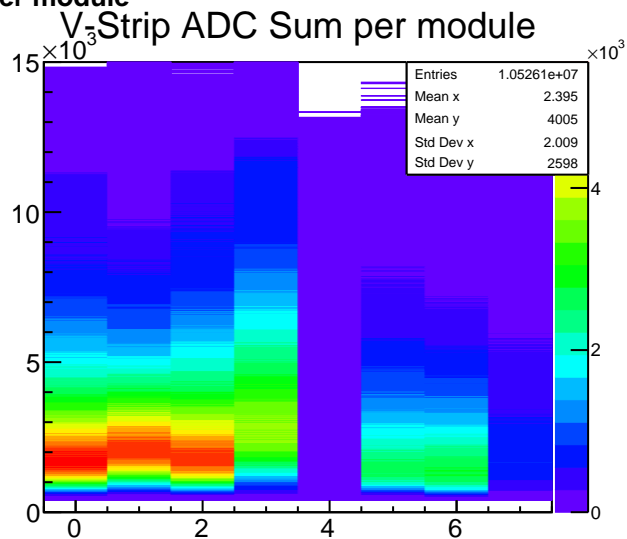
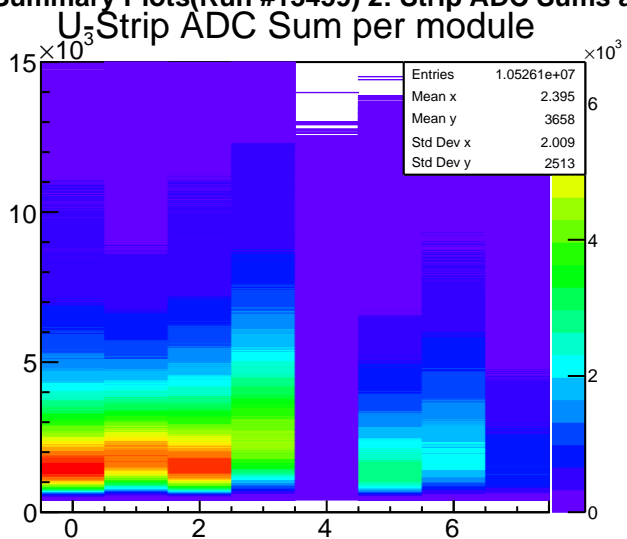
Corrected Max Strip Sum V vs U for all hits



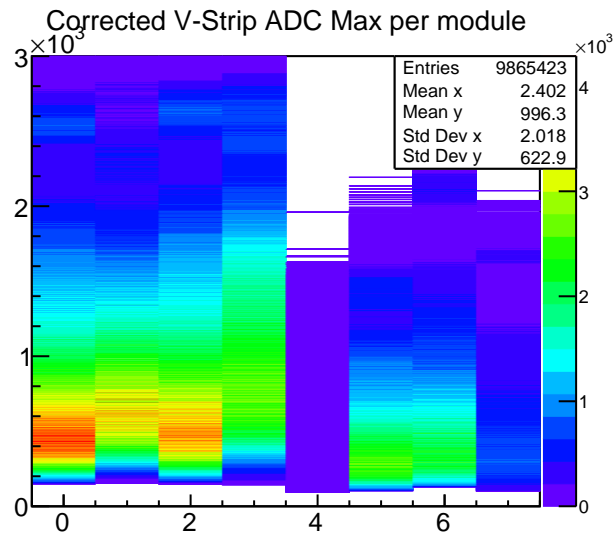
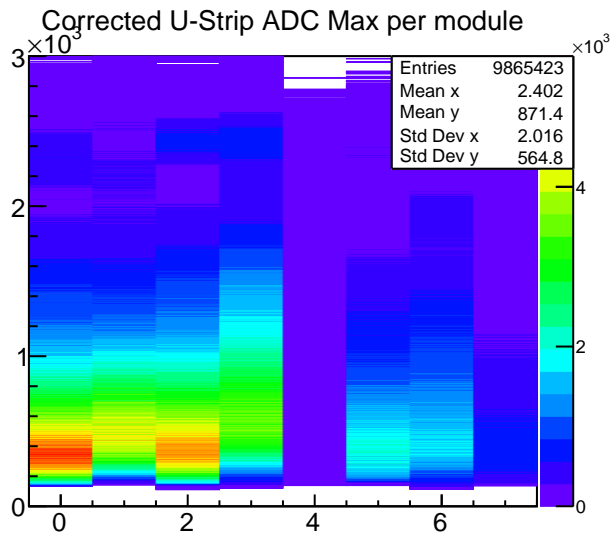
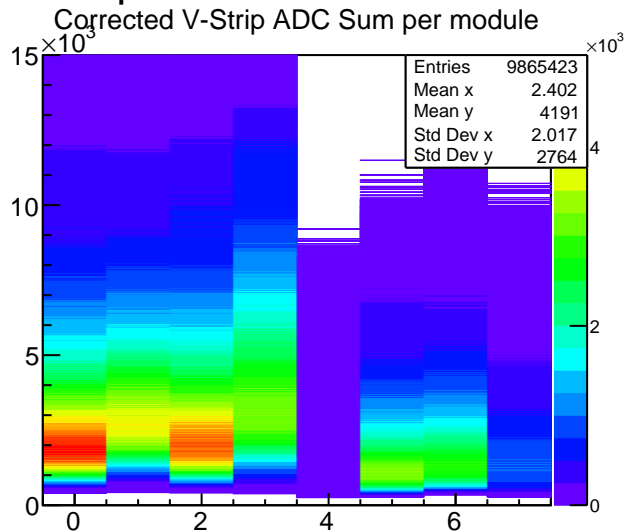
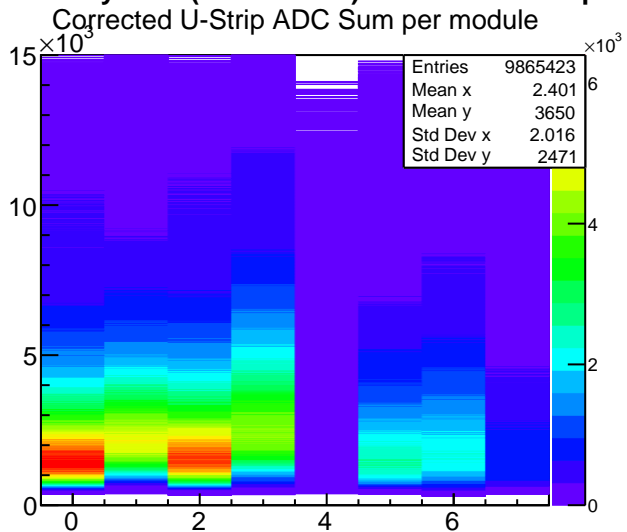
Corrected Max Strip Max Sample V vs U for all hits



Summary Plots(Run #13459) 2: Strip ADC Sums and Max per module

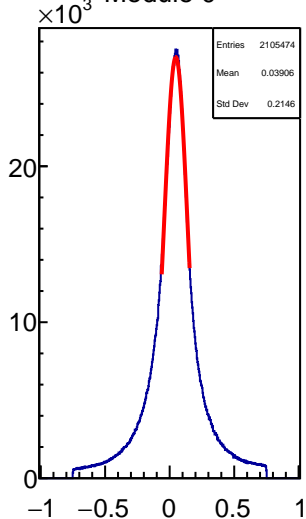


Summary Plots(Run #13459) 3: Corrected Strip ADC Sums and Max per module

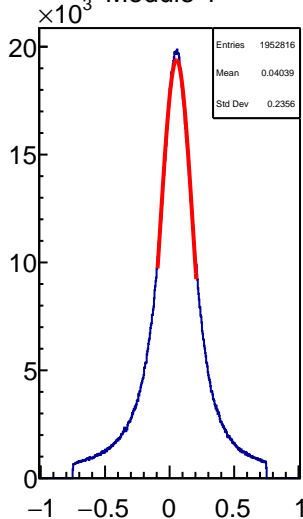


Summary Plots(Run #13459) 4: ADC Asymmetry by module

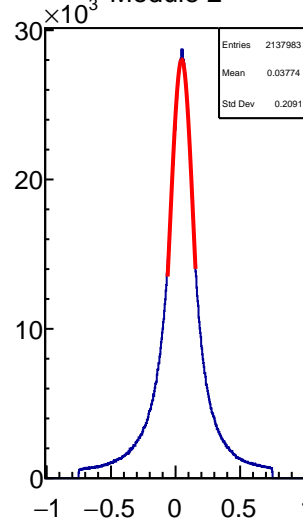
Module 0



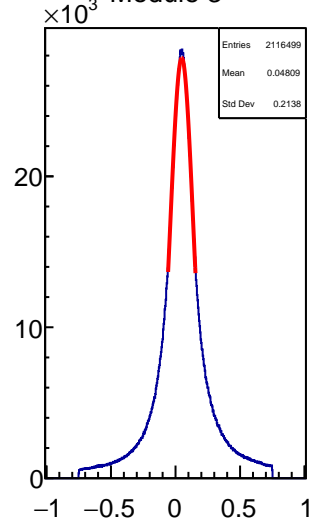
Module 1



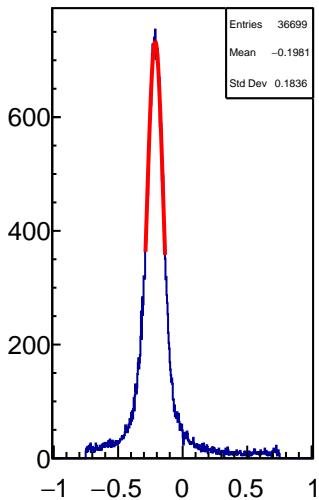
Module 2



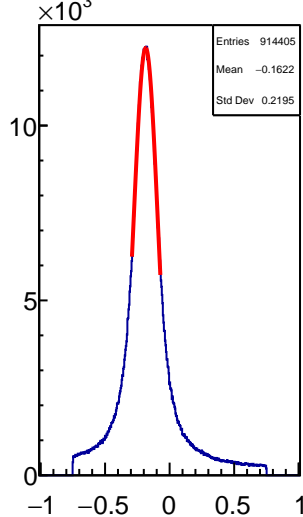
Module 3



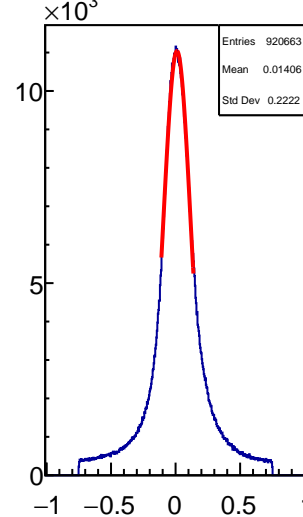
Module 4



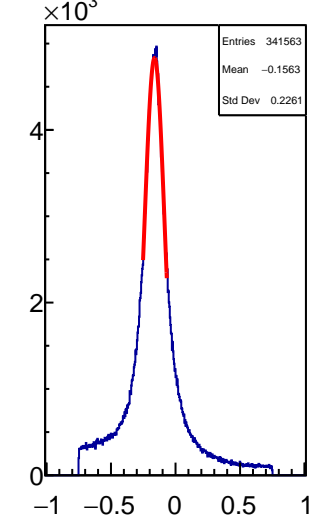
Module 5



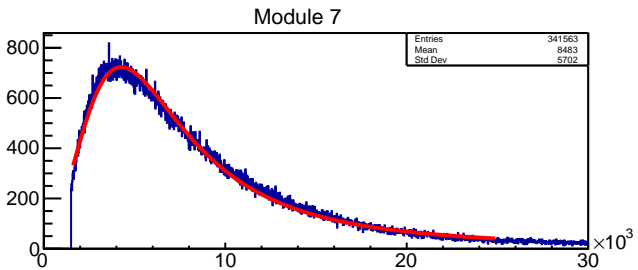
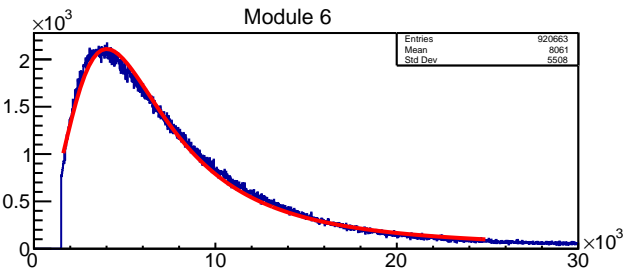
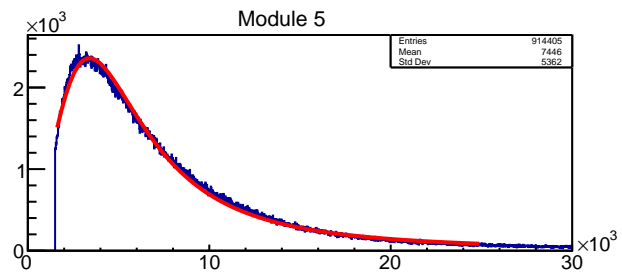
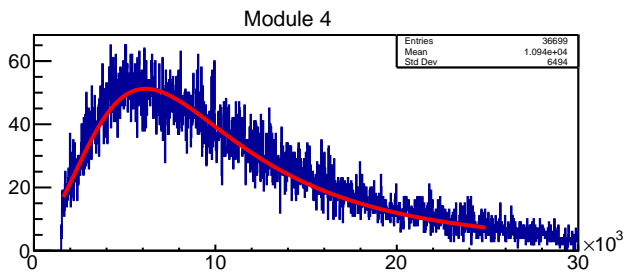
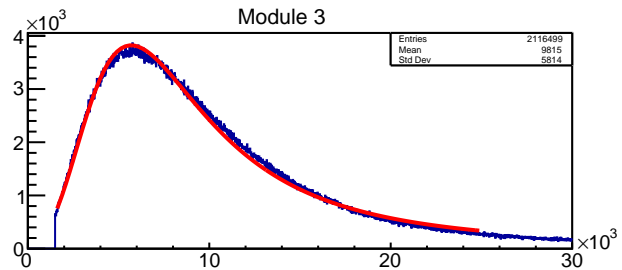
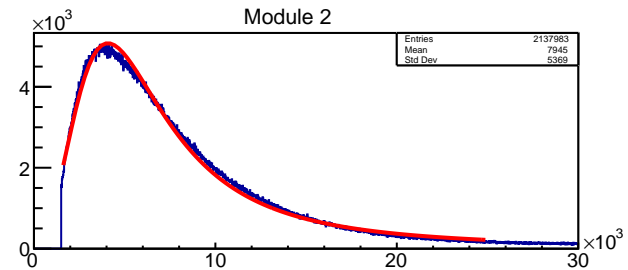
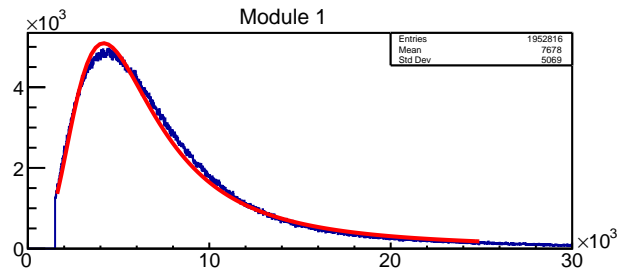
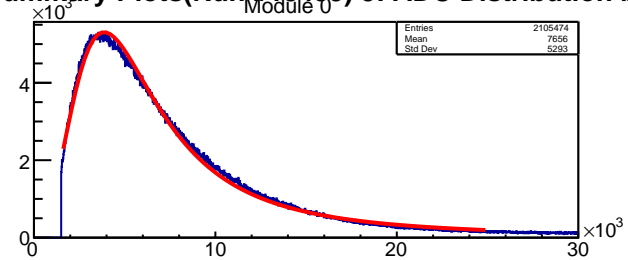
Module 6



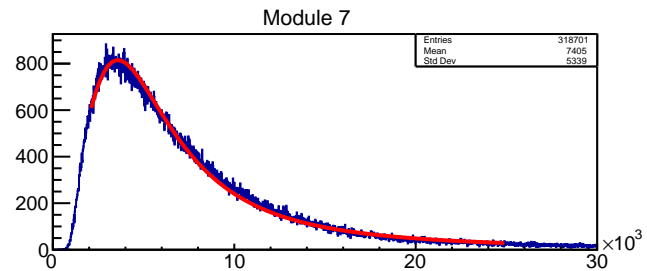
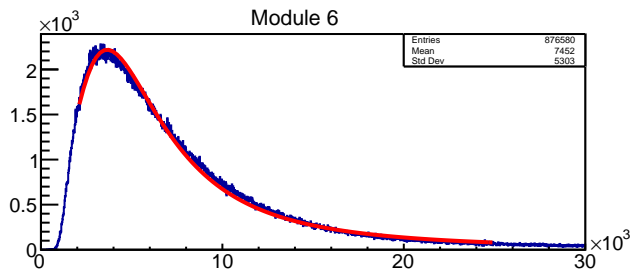
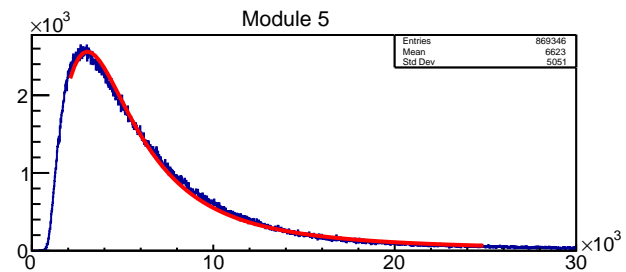
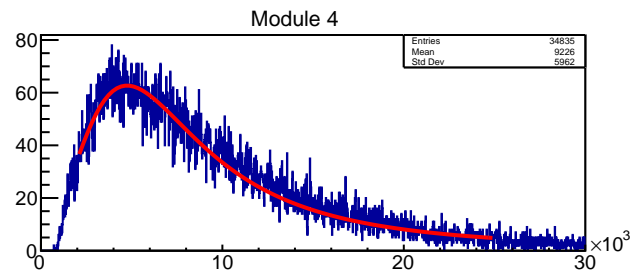
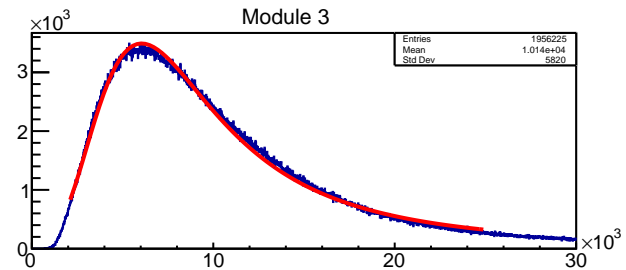
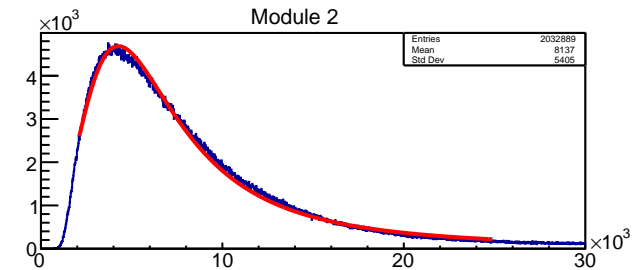
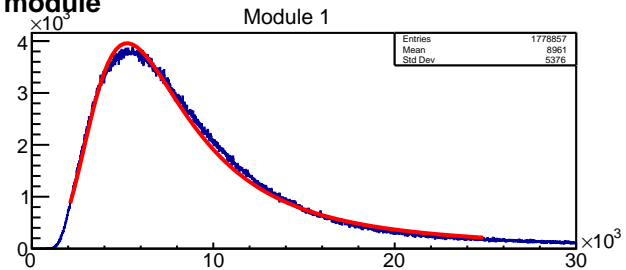
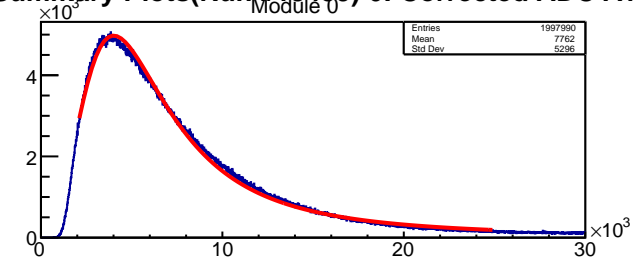
Module 7



Summary Plots(Run #13459) 5: ADC Distribution by module

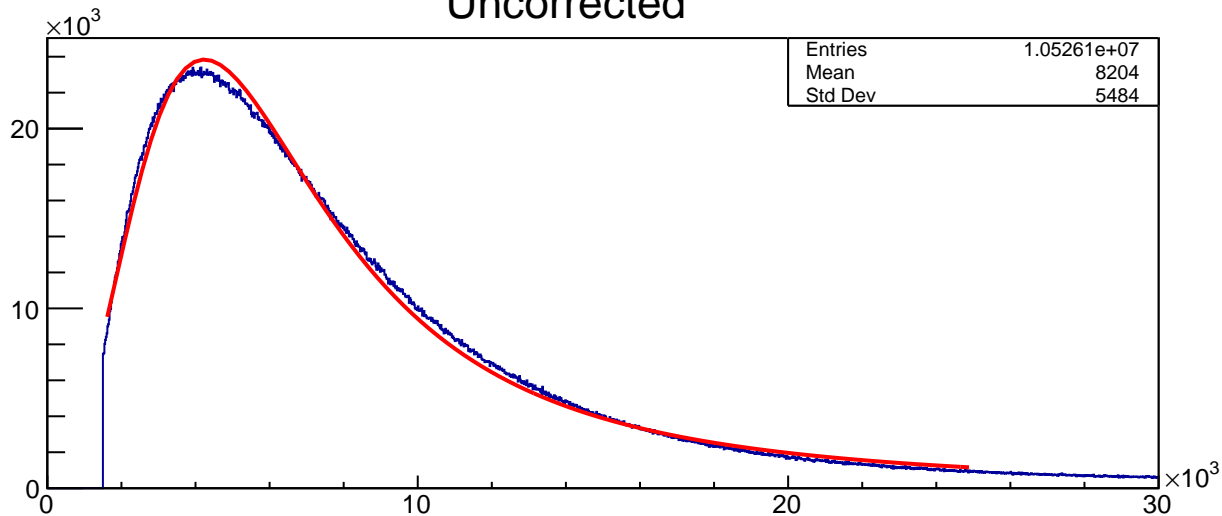


Summary Plots(Run #13459) 6: Corrected ADC Average per module

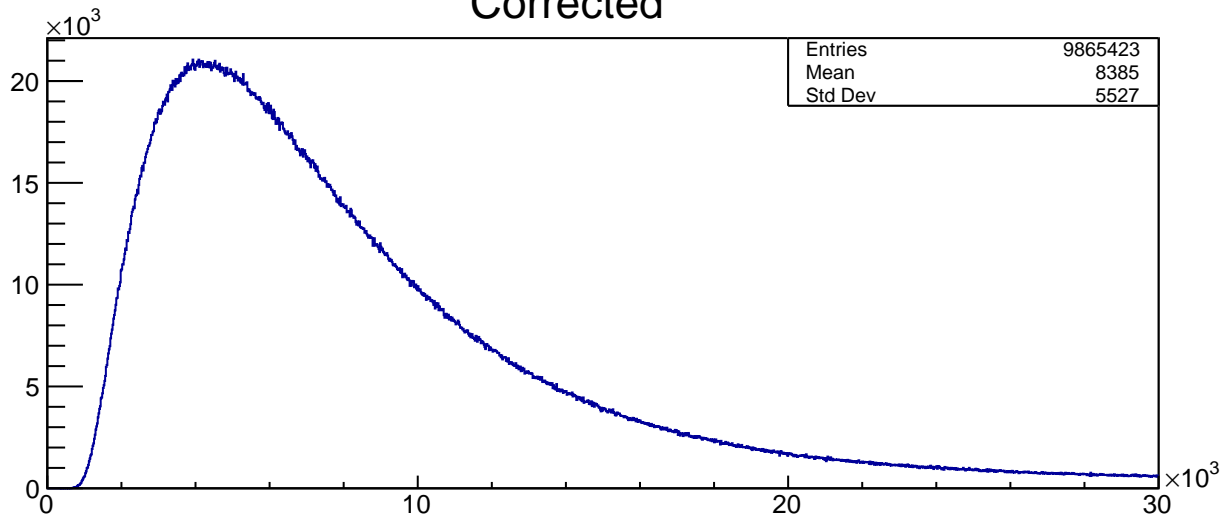


Summary Plots(Run #13459) 7: ADC Average for all hits

Uncorrected



Corrected



Summary Plots(Run #13459) 8: Corrected ADC Asymmetry all hits

