

Summary Plots(Run #12311) 2: Strip ADC Sums and Max per module

U₃Strip ADC Sum per module

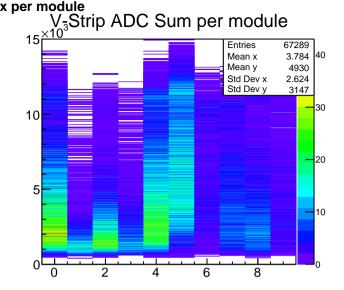
V₃Strip

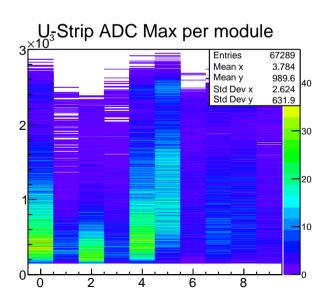
Entries 67289
Mean x 3.784
Mean y 4557
Std Dev x 2.624
Std Dev y 3012

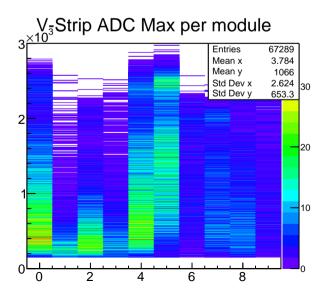
10

5

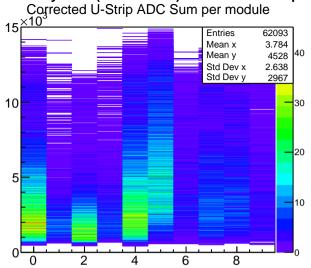
2

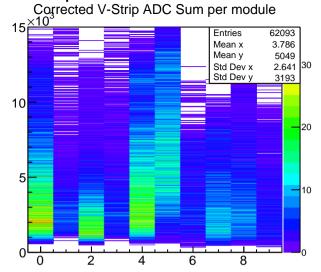


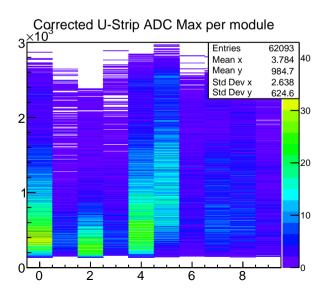


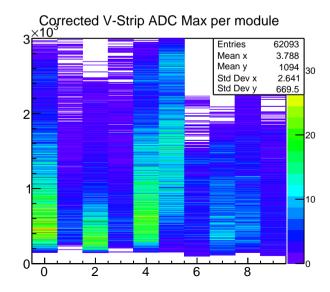


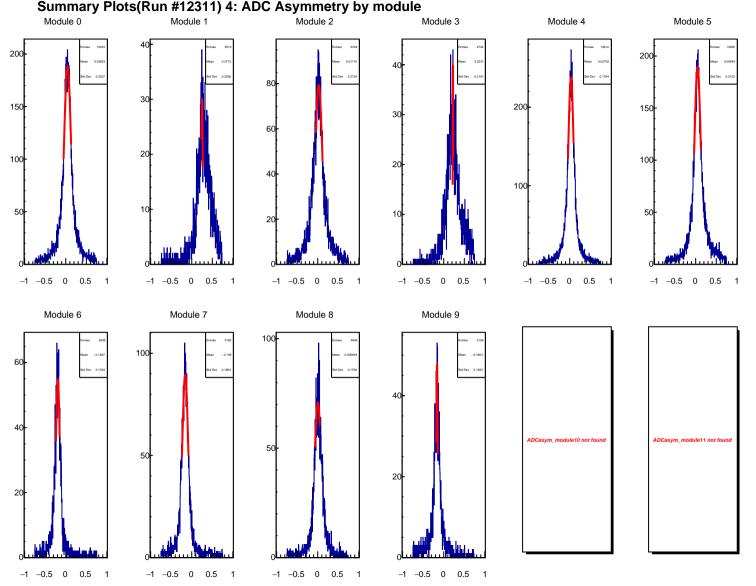
Summary Plots(Run #12311) 3: Corrected Strip ADC Sums and Max per module

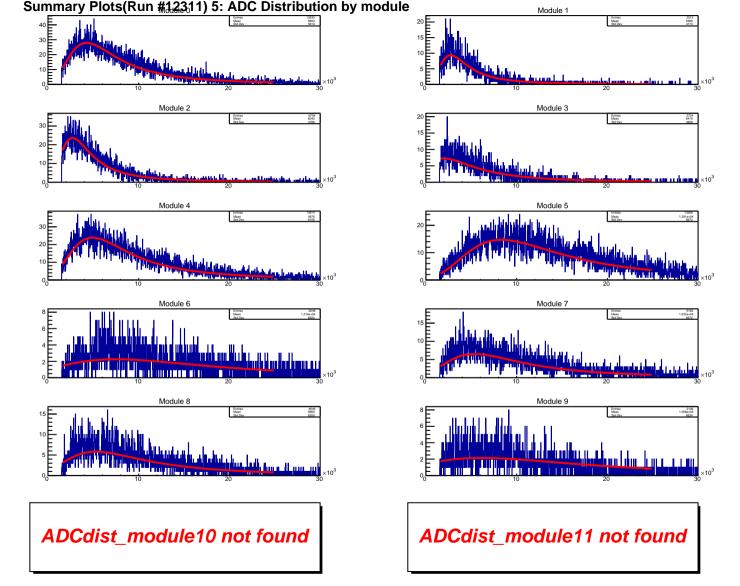


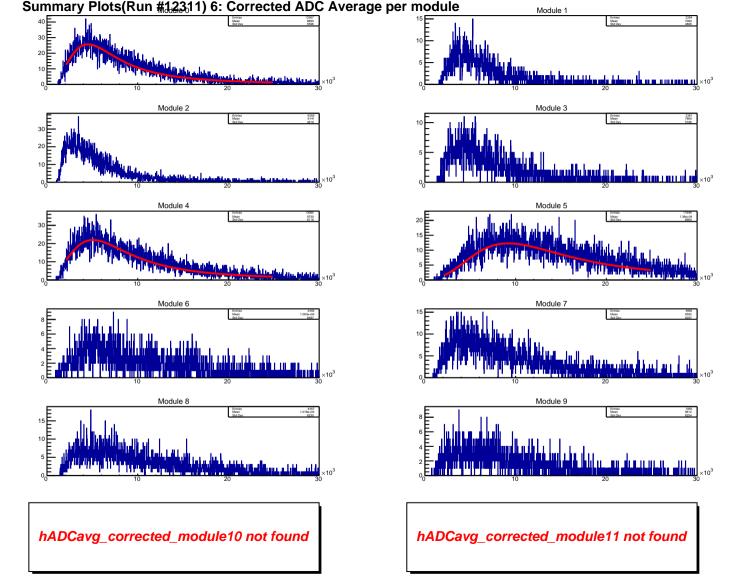












Summary Plots(Run #12311) 7: ADC Average for all hits Uncorrected

