

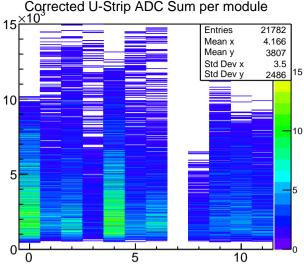
Summary Plots(Run #11990) 3: Corrected Strip ADC Sums and Max per module

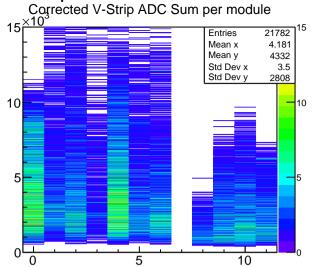
Cogrected U-Strip ADC Sum per module

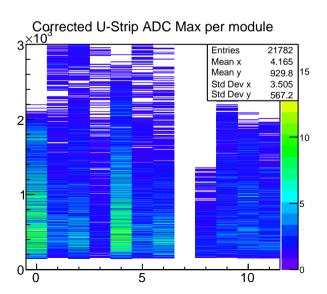
Cogrected V-Strip A

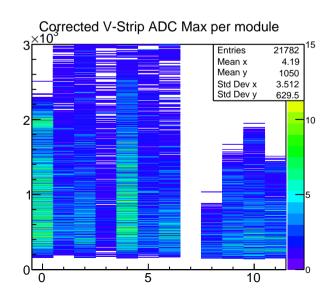
15

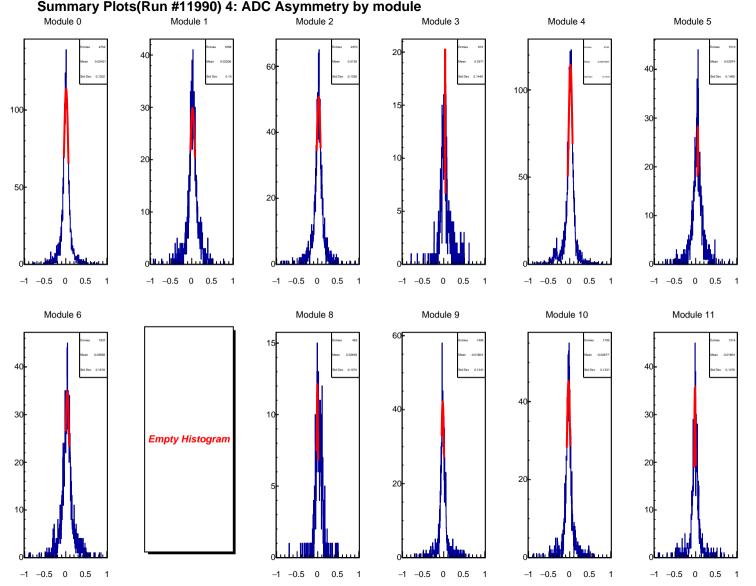
Entries 21782
Mean x 4.166
Mean y 3807
Std Dev x 3.5
Std Dev y 2486

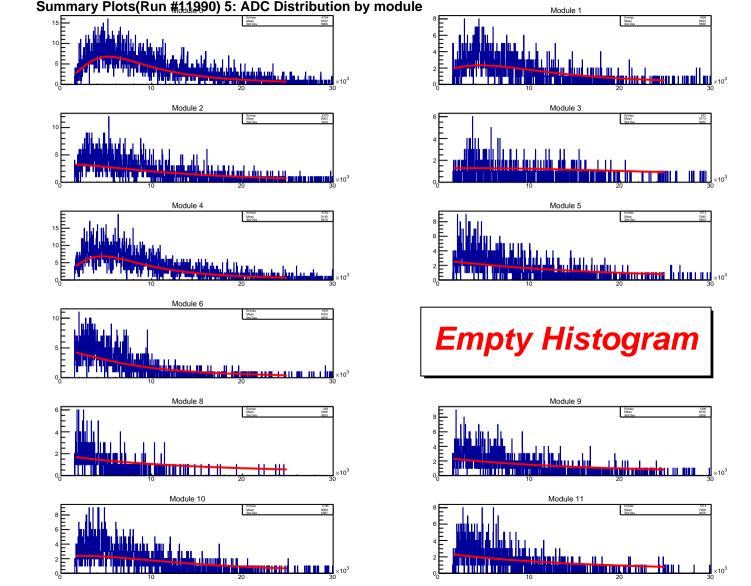


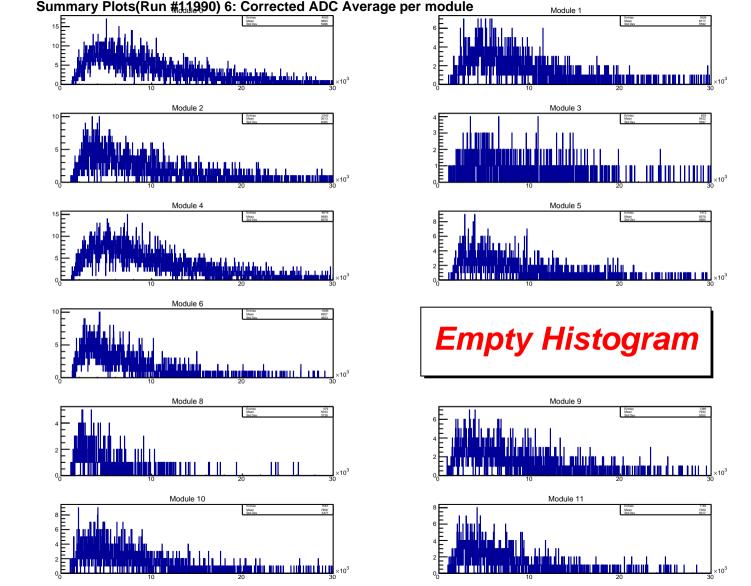












## Summary Plots(Run #11990) 7: ADC Average for all hits Uncorrected

