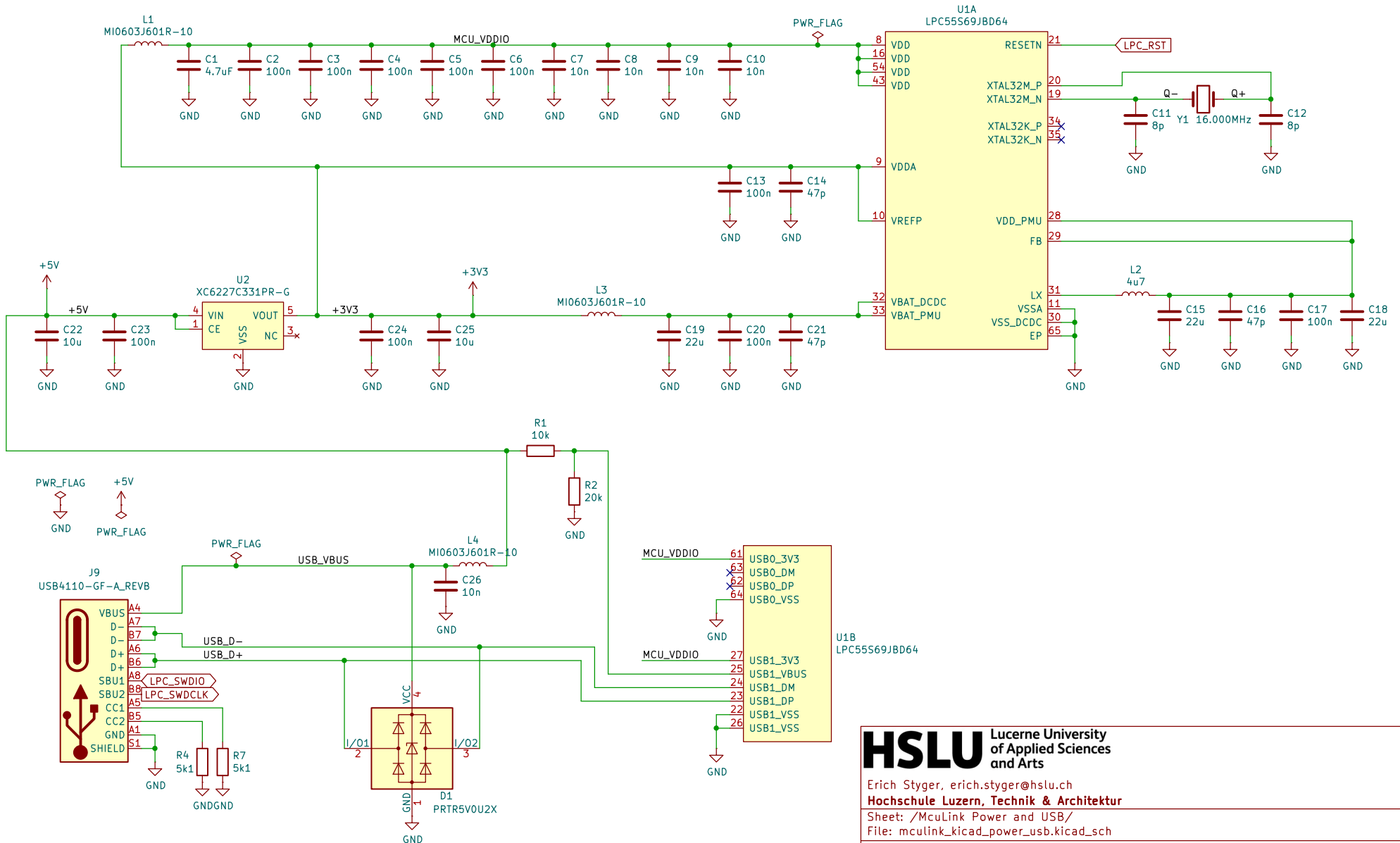


LPC55S69 MCU Powering and USB



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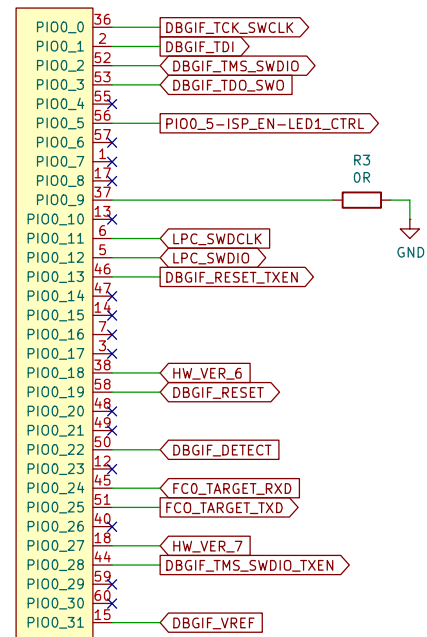
Sheet: /McuLink Power and USB/
File: mcuLink_kicad_power_usb.kicad_sch

Title: MCU-Link MR

Size: A4 Date: 2024-03-22
KiCad E.D.A. 8.0.1

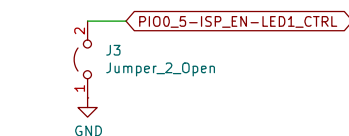
Rev: v0.1
Id: 2/5

U1C
LPC55569JBD64

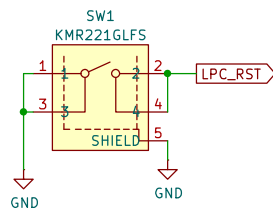


LPC55569 IO Ports

Enable ISP mode

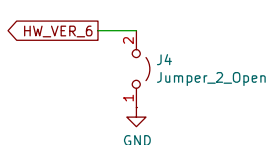


Open: Boot from internal FLASH
Closed: Enable ISP mode (USB or UART)



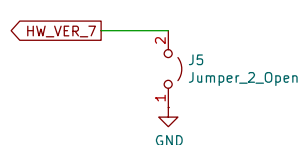
Feature set supported

VCOM disabled



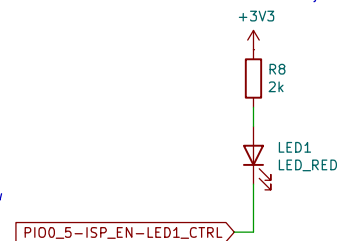
HW_VER_6: VCOM disabled when low

SWD disabled

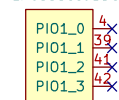


HW_VER_7: SWD debug disabled when low

LED: SWD activity



U1D
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Sheet: /LPC55569 IO Ports/

File: mculink_kicad_ioports.kicad_sch

Title: MCU-Link MR

Size: A4

Date: 2024-03-22

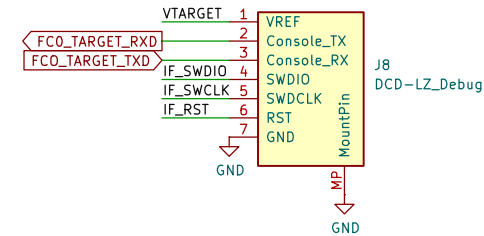
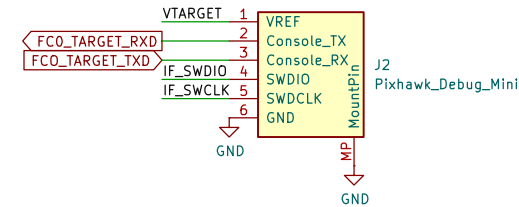
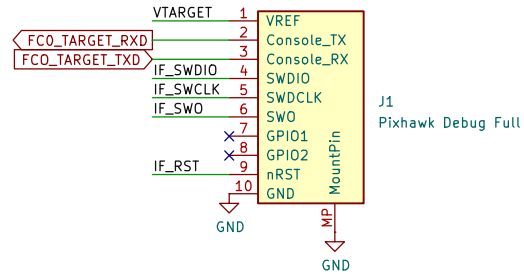
Rev: v0.1

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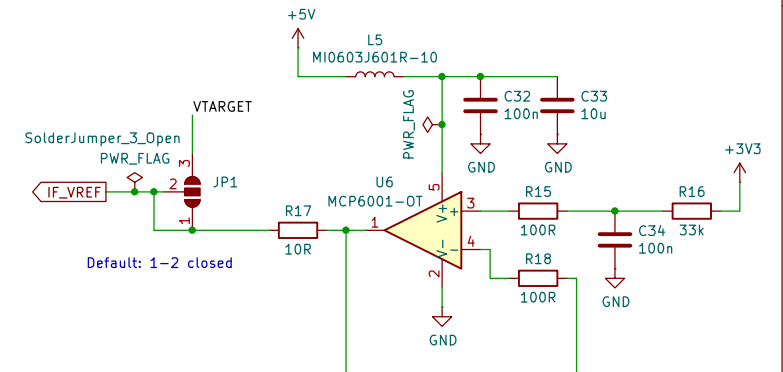
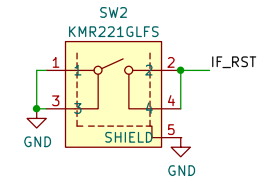
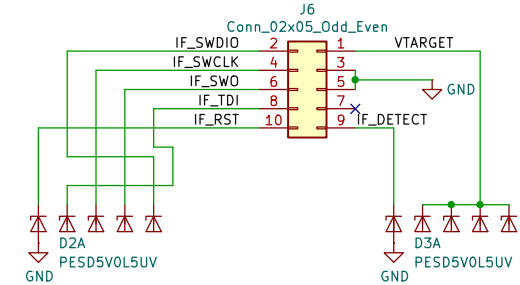
Id: 3/5

Debug Interface

Console: TX/RX is from PX4 point of view
FTDI Yellow: -> to PC
FTDI Orange: <- from PC



Target Cortex Debug (SWD/JTAG)



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Sheet: /Debug interface/

File: mculink_kicad_debug.kicad_sch

Title: MCU-Link MR

Size: A4 Date: 2024-03-22

KiCad E.D.A. 8.0.1

Rev: v0.1

Id: 4/5

<https://nxp.gitbook.io/navqplus/hardware/hardware-interfaces/uart/uart2-a53-debug>



J11
JST-GH_BM06B