/tb_i2c_top/U_I2C/i_RST
/tb_i2c_top/U_I2C/i_BT_A
/tb_i2c_top/U_I2C/i_BT_B
/tb_i2c_top/U_I2C/i_BT_C
/tb_i2c_top/U_I2C/i_DATA_SW   10101010 11111111 01010111 10101010 11111111 01010111 10101010
/tb_i2c_top/U_I2C/w_DATA_SL_A   10101010
/tb_i2c_top/U_I2C/w_DATA_SL_B   11111111
/tb_i2c_top/U_I2C/w_DATA_SL_C   01010111
/tb_i2c_top/U_I2C/w_ENABLE_SL_A
/tb_i2c_top/U_I2C/w_ENABLE_SL_B
/tb_i2c_top/U_I2C/w_ENABLE_SL_C
/tb_i2c_top/U_I2C/w_SCL

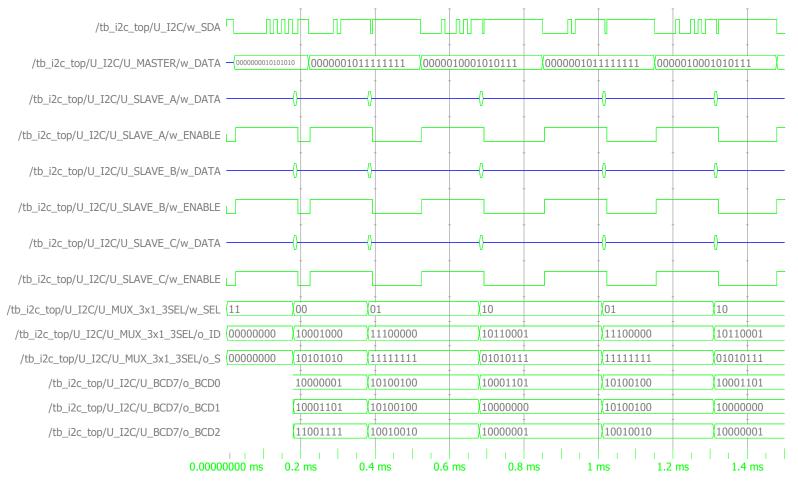0.00000000 ms   0.2 ms   0.4 ms   0.6 ms   0.8 ms   1 ms   1.2 ms   1.4 ms

Entity:tb_i2c_top Architecture:behavioral Date: Tue Dec 04 10:32:23 HorÃirio brasileiro de verÃ£o 2018   Row: 1 Page: 1

/tb_i2c_top/U_I2C/w_SDA

/tb_i2c_top/U_I2C/U_MASTER/w_DATA 0000000010101010 0000001011111111 0000010001010111 0000001011111111 0000010001010111

/tb_i2c_top/U_I2C/U_SLAVE_A/w_DATA

/tb_i2c_top/U_I2C/U_SLAVE_A/w_ENABLE

/tb_i2c_top/U_I2C/U_SLAVE_B/w_DATA

/tb_i2c_top/U_I2C/U_SLAVE_B/w_ENABLE

/tb_i2c_top/U_I2C/U_SLAVE_C/w_DATA

/tb_i2c_top/U_I2C/U_SLAVE_C/w_ENABLE

/tb_i2c_top/U_I2C/U_MUX_3x1_3SEL/w_SEL 11 00 01 10 01 10

/tb_i2c_top/U_I2C/U_MUX_3x1_3SEL/o_ID 00000000 10001000 11100000 10110001 11100000 10110001

/tb_i2c_top/U_I2C/U_MUX_3x1_3SEL/o_S 00000000 10101010 11111111 01010111 11111111 01010111

/tb_i2c_top/U_I2C/U_BCD7/o_BCD0 10000001 10100100 10001101 10100100 10001101

/tb_i2c_top/U_I2C/U_BCD7/o_BCD1 10001101 10100100 10000000 10100100 10000000

/tb_i2c_top/U_I2C/U_BCD7/o_BCD2 11001111 10010010 10000001 10010010 10000001

0.00000000 ms   0.2 ms   0.4 ms   0.6 ms   0.8 ms   1 ms   1.2 ms   1.4 ms

Entity:tb_i2c_top Architecture:behavioral Date: Tue Dec 04 10:32:23 HorÃrio brasileiro de verÃ£o 2018   Row: 1 Page: 2