Microcontroladores

Intro

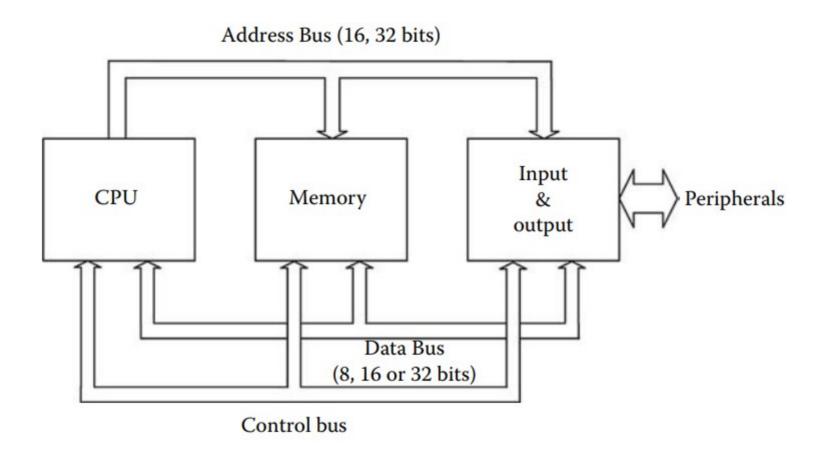


Fig 1.1 Diagrama genérico de una microcomputadora

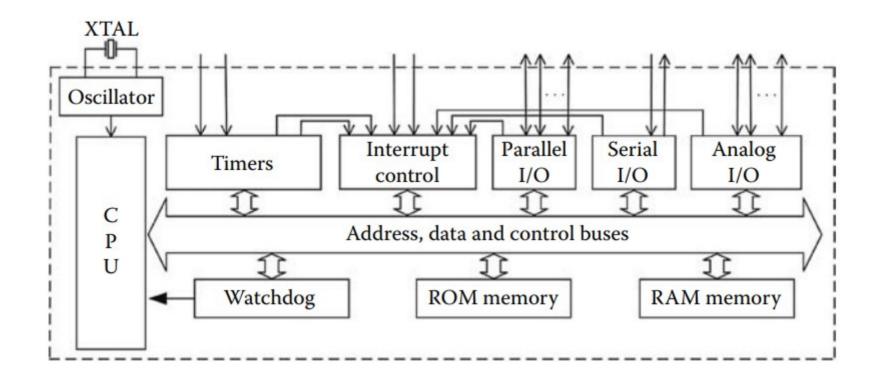
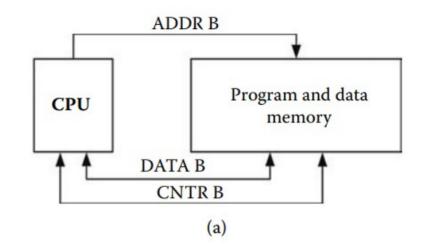


Fig 1.2 Diagrama básico de un microcontrolador

Fig 1.3

- (a) Von Neumann
- (b) Arquitectura Hardvard



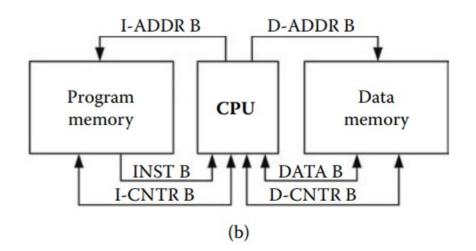


Fig 1.4
Visualización
Complex Instruction Set Computing vs
Reduced Instruction Set Computing

CISC (Complex instruction)

10100100101

RISC (Simple instruction)

10101101010101011101 01010101001001011001

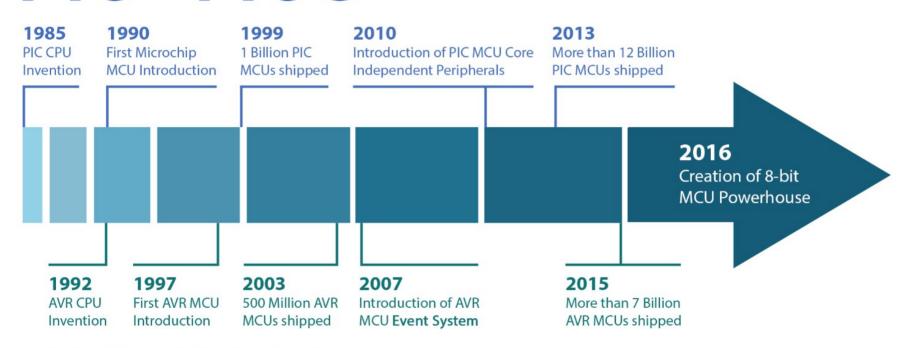
11001001111000101100 10010000011111001010



History of Inovation

PIC® MCU

AVR® MCU



Motorola's MPC555 PowerPC™ Microcontroller TOUCAN QSMCM TOUCAN SRAM 10K 192K FLASH 256K **FLASH** SRAM 16K L2U BBC PPC CORE USIU FP