

Enhanced Experiment (ARM)

Configuration	anly-1.c	anly-11.c	anly-2.c	anly-5.c	anly-6.c	anly-8.c	anly-9.c	cast-1.c	cast-2.c	cast-3.c	cast-4.c	cast-6.c	concur-3.c	std-1.c	std-2.c	std-3.c	std-4.c
clang_O0_x86_64	✗	✗	✗	✗	✗	✗	✗	✗	✗	✗	✗	✗	✗	✗	✗	✗	✗
clang_O1_x86_64	✗	✗	✓	✗	✓	✓	✓	✓	✓	✗	✗	✓	✓	✗	✗	✓	✗
clang_O2_x86_64	✗	✓	✓	✗	✓	✓	✓	✓	✓	✗	✗	✓	✓	✗	✗	✓	✗
clang_O3_x86_64	✗	✓	✓	✗	✓	✓	✓	✓	✓	✗	✗	✓	✓	✗	✗	✓	✗
gcc_O0_x86_64	✗	✗	✓	✗	✗	✓	✗	✗	✗	✗	✗	✗	✗	✗	✗	✗	✗
gcc_O1_x86_64	✗	✓	✓	✗	✗	✓	✗	✓	✓	✗	✗	✓	✓	✓	✓	✗	✗
gcc_O2_x86_64	✓	✓	✓	✓	✗	✓	✗	✓	✓	✓	✓	✓	✓	✓	✓	✓	✗
gcc_O3_x86_64	✓	✓	✓	✓	✗	✓	✗	✓	✓	✓	✓	✓	✓	✓	✓	✓	✗
gcc_O4_x86_64	✓	✓	✓	✓	✗	✓	✗	✓	✓	✓	✓	✓	✓	✓	✓	✓	✗

✓: CISB Present ✗: CISB Not Present ?: Test Case Invalid