

# Termisk vakumkammer for test av satellitt og komponenter

TFE4850 EIT: Romteknologi

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TFE4850

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## References

- [1] David A. Johns, Ken Martin, *Analog integrated circuit design*. John Wiley and sons Inc, 1997.
- [2] Trond Ytterdal c, *Project description*, TFE4187 Analog CMOS1, fall 2013
- [3] Trond Ytterdal c, *Lecture material*, TFE4187 Analog CMOS1, fall 2013