LoRaIoT.PrjPcb

Variant: V1

13.12.2023

Released

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DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There should not be any mistakes. Tell the engineer if you find one.

RELEASED - A board with this schematic has been sent to production.

Title: Cover page		
Page contents: [1] COVER PAGE.SchDoc	Version: V1	Issue: 0
Size: A3	Date: 13.12.2023	Drawn by: Ernest Rychlicki
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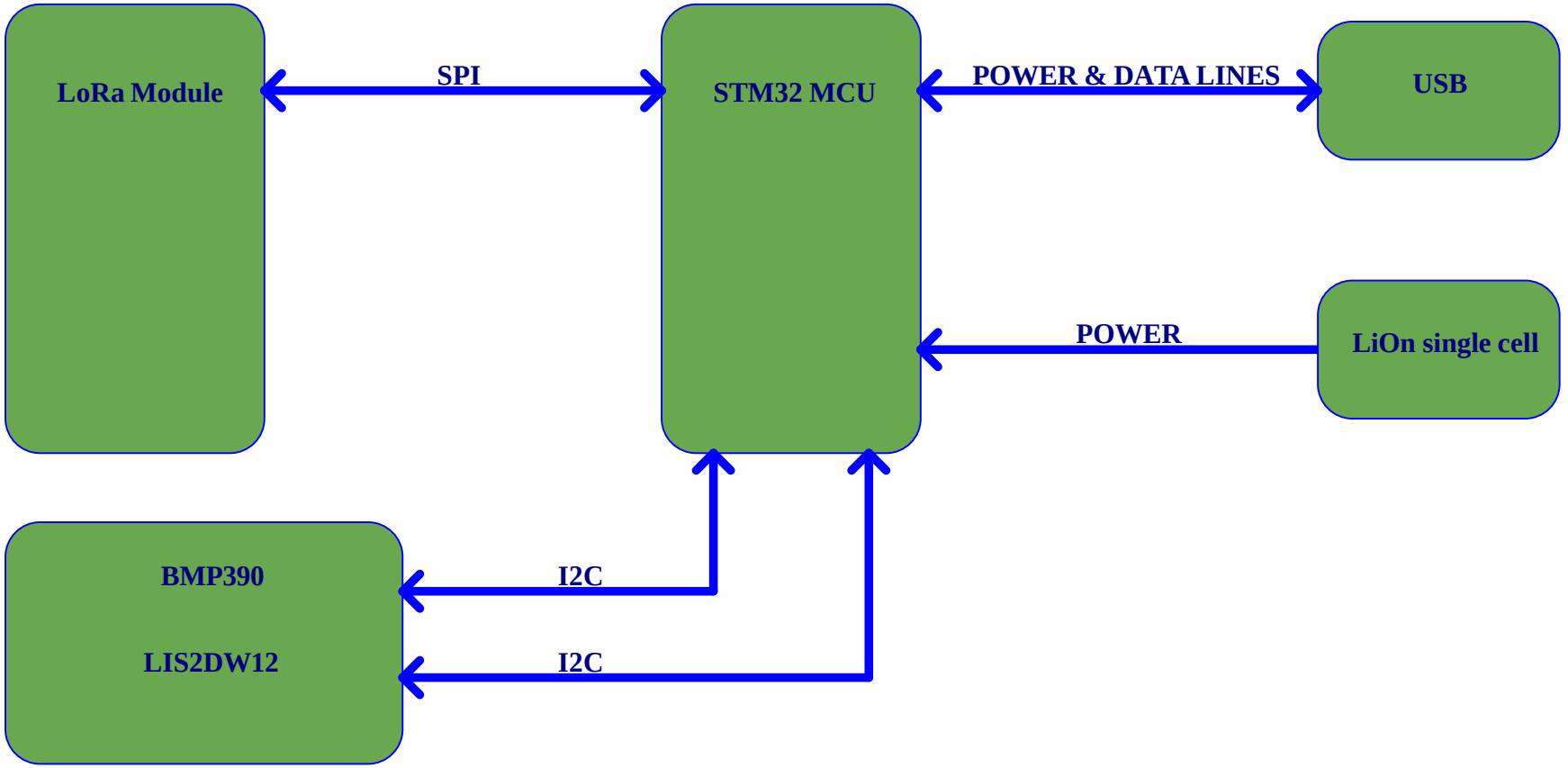
Block diagram



Title: Block diagram			
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Size: A4	Date: 13.12.2023	Drawn by: Ernest Rychlicki	
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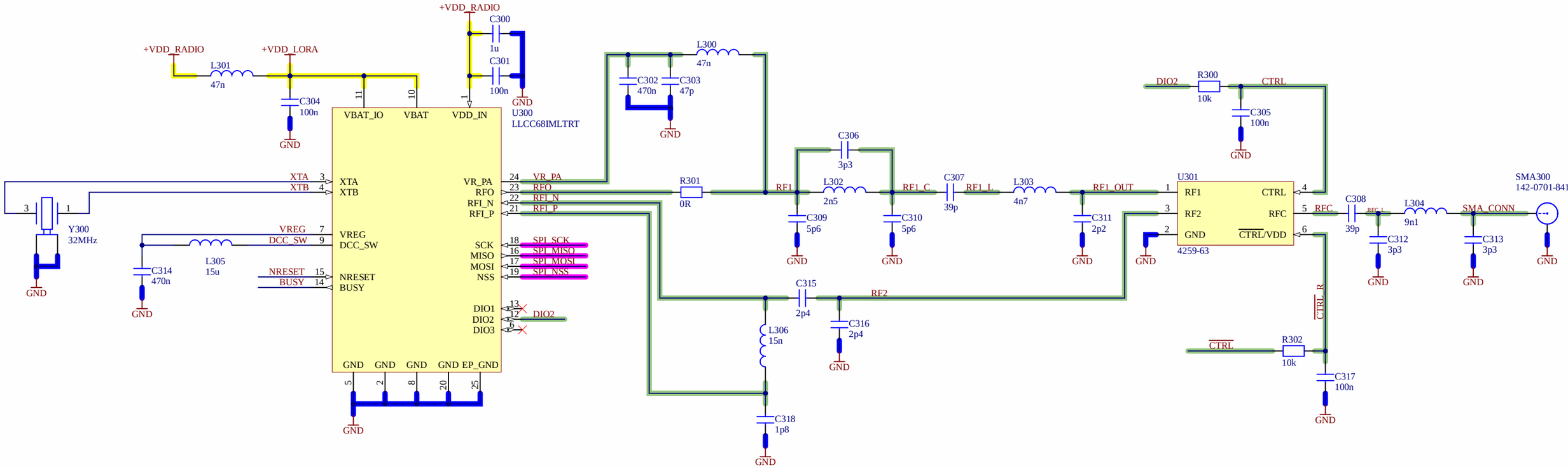
LLCC68 LoRa IC



Title: LLCC68 LoRa IC		
Page contents: [3] LORA MODULE.SchDoc	Version: V1	Issue: 0
Size: A3	Date: 13.12.2023	Drawn by: Ernest Rychlicki
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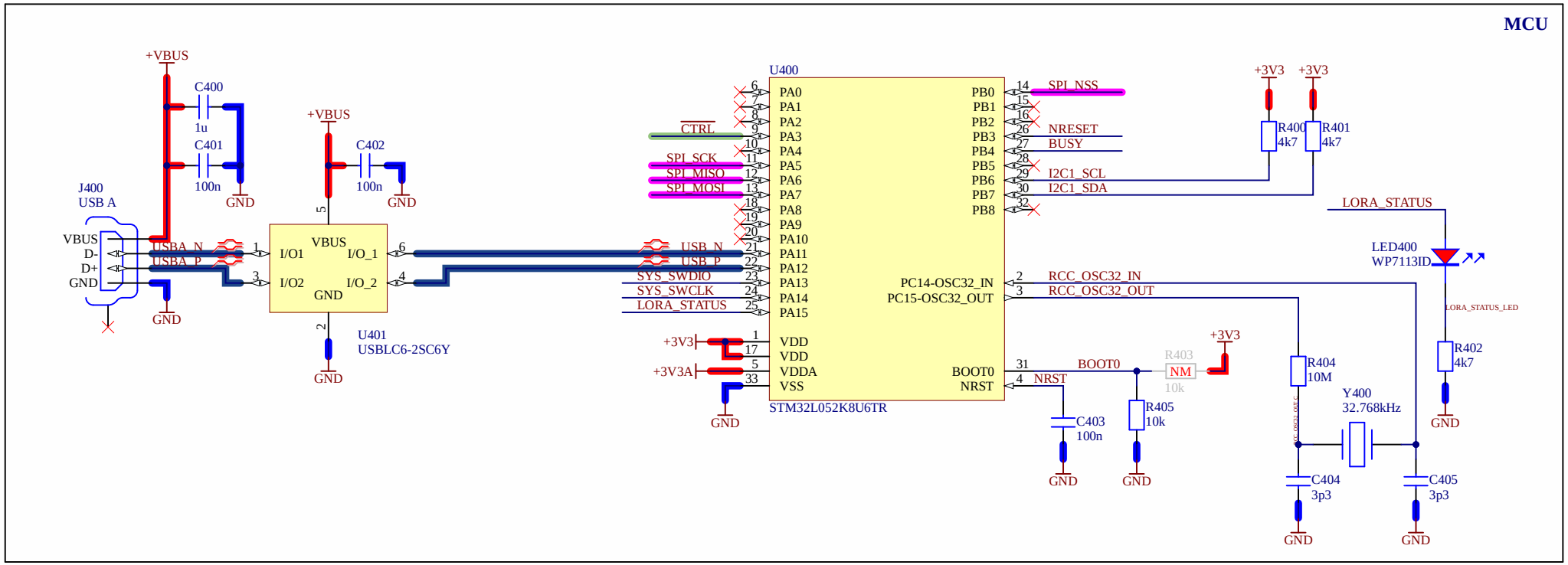
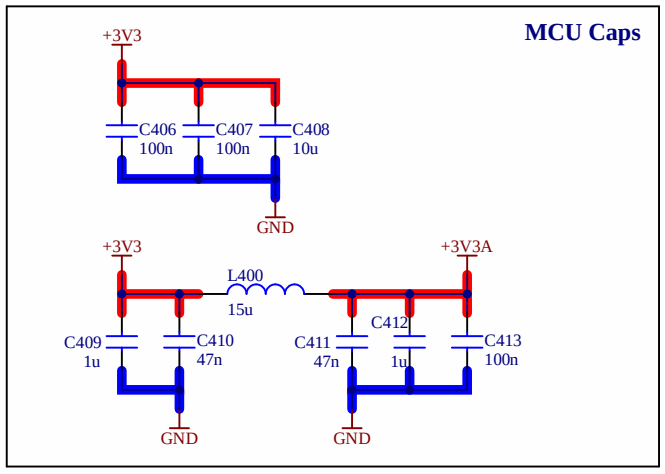
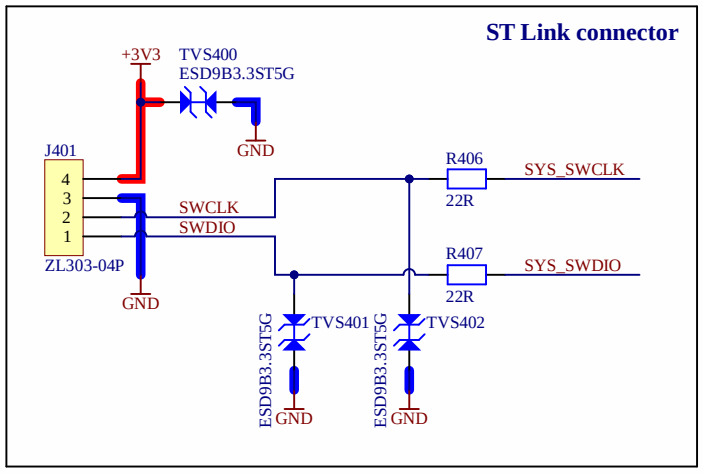
MCU STM32L052K8U6TR







Title: MCU STM32L052K8U6TR		
Page contents: [4] MCU.SchDoc	Version: V1	Issue: 0
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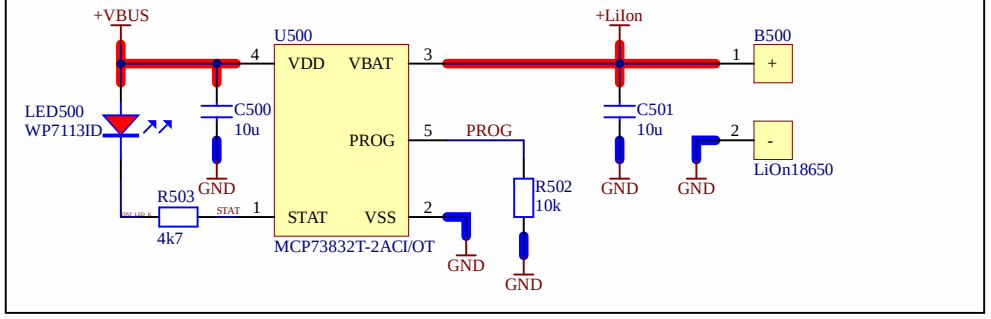
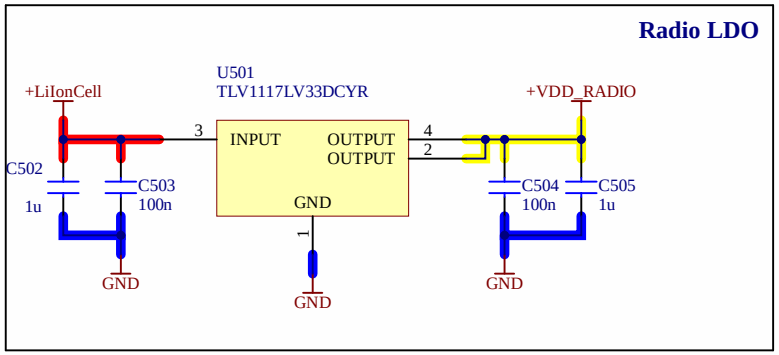
Power

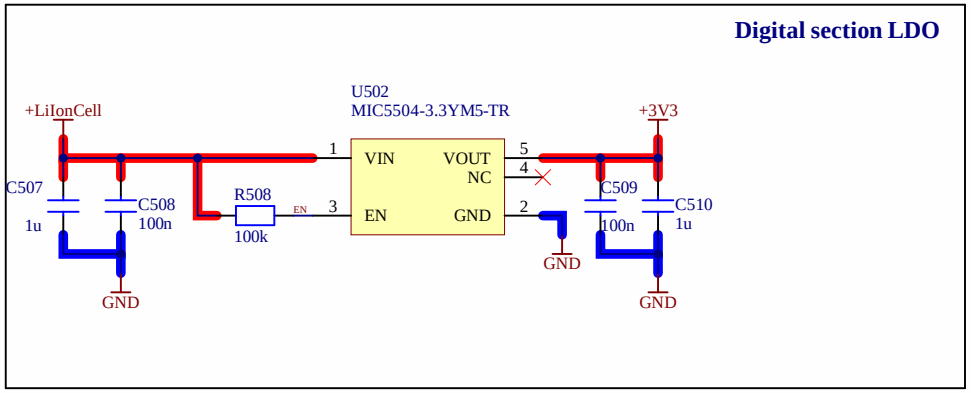
Single cell BMS



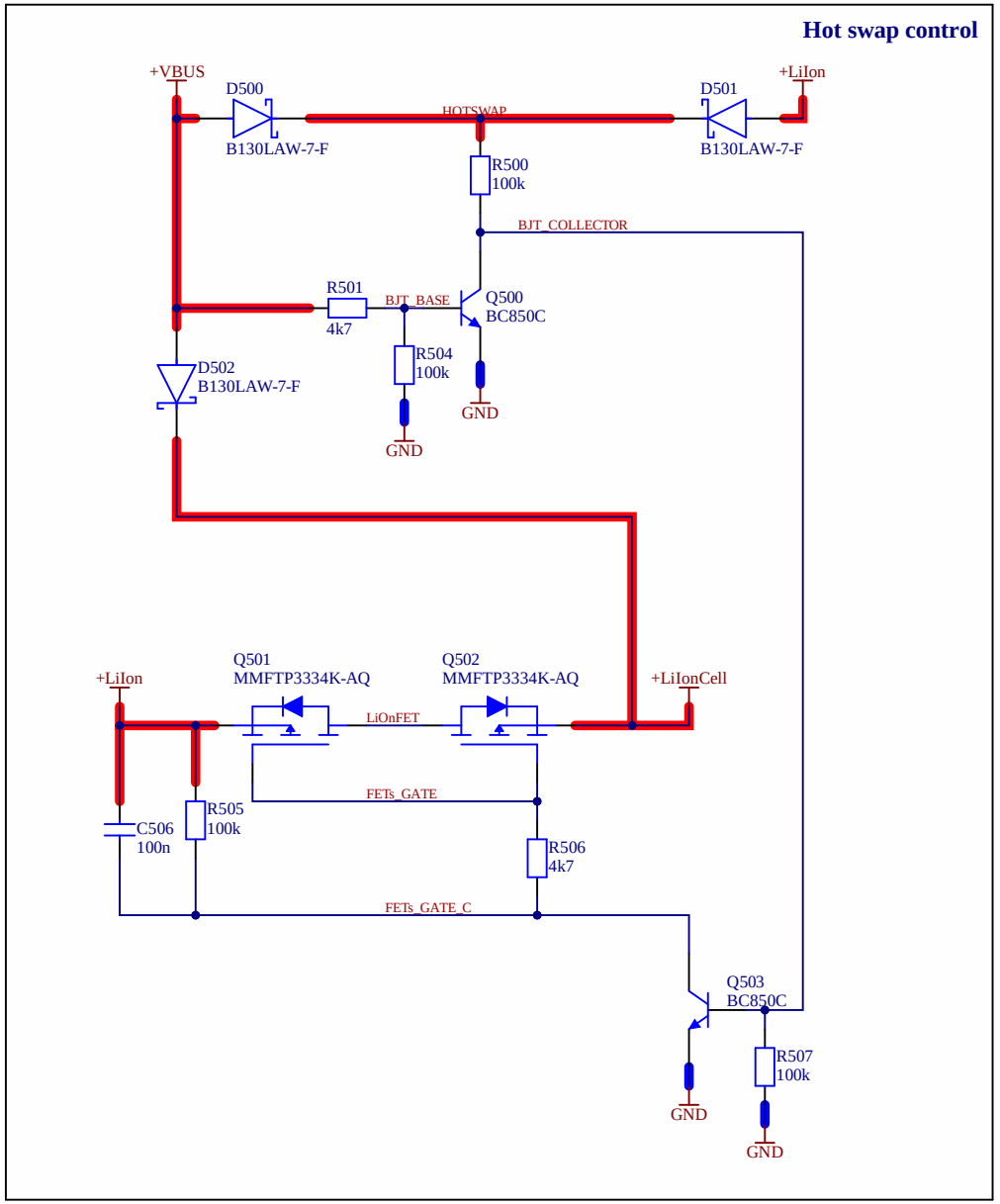
Radio LDO



Digital section LDO



Hot swap control



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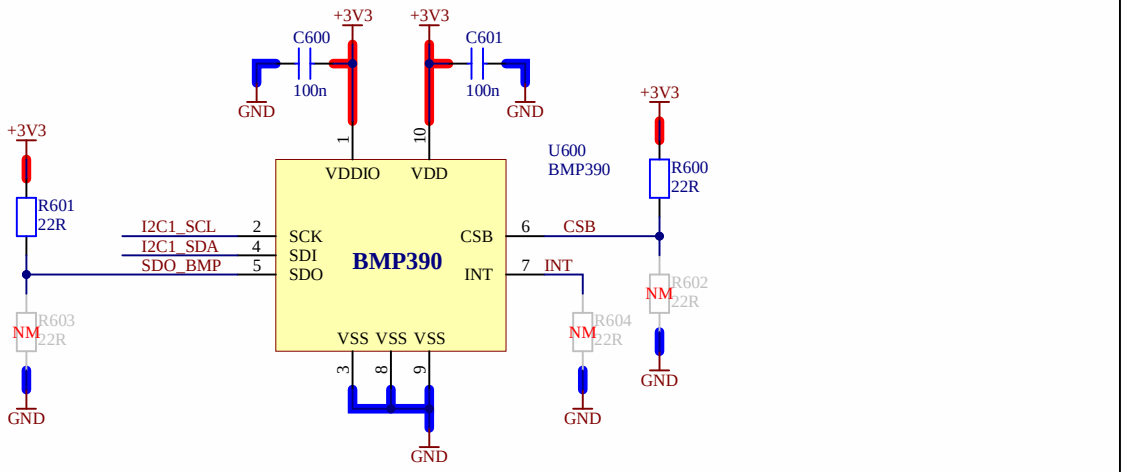
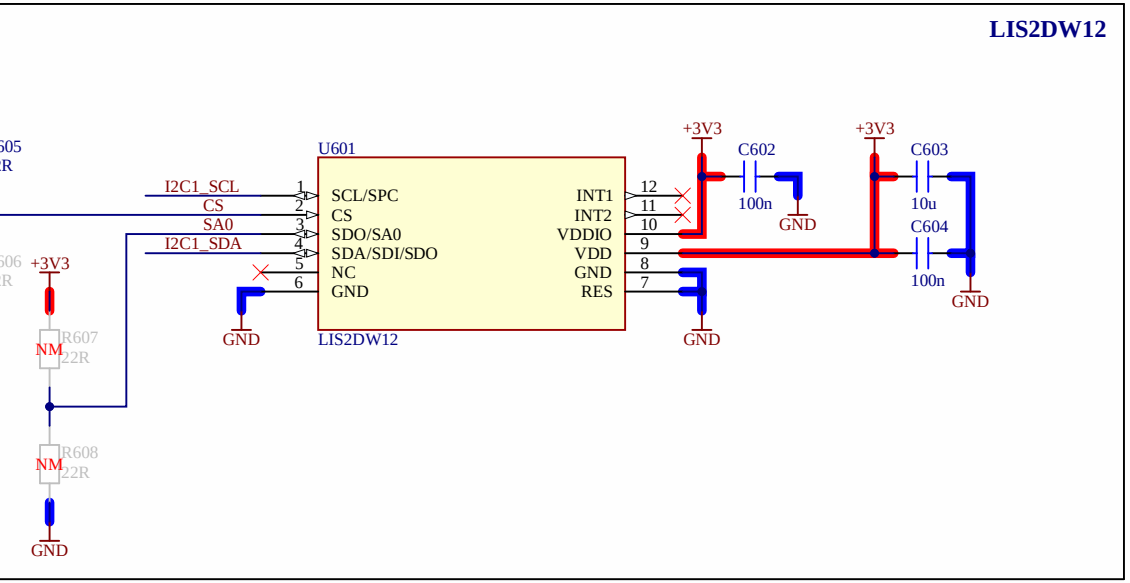
Sensors

BMP390



LIS2DW12



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