

INSTITUTO POLITÉCNICO NACIONAL ESCUELA SUPERIOR DE CÓMPUTO



U.A: Arquitectura de Computadoras

"Práctica 12: Microinstrucciones"

Grupo: 3CV11

Profesor: Vega García Nayeli

Sánchez Becerra Ernesto Daniel

Microinstrucciones

1. Obtenga la microinstrucción de cada una de las instrucciones del set y complete la siguiente tabla. Esta tabla puede modificarla a conveniencia

												ALUOP											
Instrucción	SD MP	UP	DW	WPC	SR2	SWD	SEXT	SHE	DIR	WR	SOP1	SOP2	[3]	[2]	[1]	[0]	SDMD	WD	SR	LF			
LI	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0			
LWI	0	0	0	0	0	1	0	0	0	1	0	0	0	0	0	0	1	0	0	0			
LW	0	0	0	0	0	1	1	0	0	1	0	1	0	0	1	1	0	0	0	1			
SWI	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0			
SW	0	0	0	0	1	0	1	0	0	0	0	1	0	0	1	1	0	1	0	1			
ADD	0	0	0	0	0	1	0	0	0	1	0	0	0	0	1	1	0	0	1	1			
SUB	0	0	0	0	0	1	0	0	0	1	0	0	0	1	1	1	0	0	1	1			
ADDI	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	1	0	0	1	1			
SUBI	0	0	0	0	0	1	0	0	0	1	0	1	0	1	1	1	0	0	1	1			
AND	0	0	0	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	1	1			
OR	0	0	0	0	0	1	0	0	0	1	0	0	0	0	0	1	0	0	1	1			
XOR	0	0	0	0	0	1	0	0	0	1	0	0	0	0	1	0	0	0	1	1			
NAND	0	0	0	0	0	1	0	0	0	1	0	0	1	1	0	1	0	0	1	1			
NOR	0	0	0		0	1	0	0	0	1	0	0	1	1	0	0	0	0	1	1			
XNOR	0	0	0	0	0	1	0	0	0	1	0	0	1	0	1	0	0	0	1	1			
NOT	0	0	0	0	0	1	0	0	0	1	0	0	1	1	0	1	0	0	1	1			
ANDI	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	0	0	1	1			

ORI	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	1	0	0	1	1
XORI	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	0	0	1	1
NANDI	0	0	0	0	0	1	0	0	0	1	0	1	1	1	0	1	0	0	1	1
NORI	0	0	0	0	0	1	0	0	0	1	0	1	1	1	0	0	0	0	1	1
XNORI	0	0	0	0	0	1	0	0	0	1	0	1	1	0	1	0	0	0	1	1
SLL	0	0	0	0	0	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0
SRL	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0
Verificación	0	0	0	0	1	0	0	0	0	0	0	0	0	1	1	1	0	0	0	1
BEQI	1	0	0	1	0	0	0	0	0	0	1	1	0	0	1	1	0	0	1	1
BNEI	1	0	0	1	0	0	0	0	0	0	1	1	0	0	1	1	0	0	1	1
BLTI	1	0	0	1	0	0	0	0	0	0	1	1		0	1	1	0	0	1	1
BLETI	1	0	0	1	0	0	0	0	0	0	1	1	0	0	1	1	0	0	1	1
BGTI	1	0	0	1	0	0	0	0	0	0	1	1	0	0	1	1	0	0	1	1
BGETI	1	0	0	1	0	0	0	0	0	0	1	1	0	0	1	1	0	0	1	1
В	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CALL	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RET	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NOP	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0