# Dvé Appendix: Coherence Protocol Specification and Verification

## 1. Summary

In this appendix, we describe how we model and verify the allow-based and deny-based protocol variants. We also provide the complete specification of the allow-based replica directory controller including transient states and actions, as a sample specification.

## 2. Assumptions

The cache hierarchy is strictly inclusive and all of the generated protocols use a strict hierarchical intervention-forwarding communication pattern. This means a last-level cache (LLC), for a request associated with the replica directory (replica-LLC), communicates only with the replica directory and never communicates directly with the home directory. On receipt of a request, the replica directory issues a request to the home directory if required. Similarly, responses from the home directory are sent to the replica directory first, which then forwards it to the LLC. In addition, an ordered network is used to ensure point-to-point ordering.

#### 3. Verification

We have verified the complete specifications of the allow-based and the deny-based protocols using the  $\operatorname{Mur}\phi$  model checker for safety and deadlock-freedom. A protocol is deemed to be safe if it enforces the Single-Writer-Multiple-Reader invariant (at any time there exists a single writer or multiple readers) and the data-value-invariant (a read always returns the value of the most recent write) across all of the caches, and the 2 replicas. Note that these two invariants implicitly ensure that the data and replica are in sync.

We first verified the protocols for our system model in our evaluation: i.e., 2 sockets, with one replica directory  $(R_{\rm D})$  and one home directory  $(H_{\rm D})$  serving requests to a single LLC each.

In addition to the reference system discussed in the paper, we have also verified an allow-based and a deny-based protocol implementation for a system with 4 sockets and 2 replicas: i.e., one replica directory  $(R_D)$  and one home directory  $(H_D)$  which each manage two LLCs.

## 4. Allow-based Replica Directory Spec

The complete specification of an allow-based replica directory with a single LLC is given in tables 1 and 2.

Table 1 lists the requests and responses from the LLC to the replica directory. In addition to the LLC state ma-

chine transition events/messages, an evict event (second column – R<sub>D</sub>, evict) is defined for the replica directory. This evict event is required because the the replica directory might hold an entry for a block which is not cached in the LLC. If the replica directory gets a request for an untracked cache block when it has no free entries, it must be able to evict another entry. As long as the LLC maintains a copy of the cache block, the replica directory remains in state S. An entry in the S<sup>NS</sup> (Shared-NoSharers) state implies that the replica directory can serve a GetS request without consulting the home directory again. Since this is a single LLC implementation we used two different states (S and S<sup>NS</sup>) to track if the LLC maintains a shared copy of the cache block or not. However, for an implementation with multiple LLCs, we use a sharer vector implementation with a single state S. Note that if a dirty cache block is either evicted from the LLC or is requested by another LLC with shared permissions, it is written back to the replica and the home node's memory.

Table 2 lists remote requests and responses from the home directory. All messages apart from Fwd\_WB are part of a standard MSI protocol. The Fwd\_WB message is sent from the home directory, if its local LLC writes back a dirty cache block, thus requiring a writeback to the replica memory.

### 5. Notation

- Receive message: ?Message
- Send message: !Message(Destination, \*Payload)
  \*Payload is optional
- Next state transition:  $\rightarrow$  Next State
- If a message arrives in a state where the table entry is grey it is considered to be stalled.
- Write to replicate memory: RepMemWr
- Read from replicate memory: RepMemRd
- Last level cache: LLC
- Replica Directory: RD
- Home Directory: H<sub>D</sub>

Table 1: Allow-Based Protocol: Eviction and LLC requests

	R <sub>D</sub>	LLC									
State	evict	?GetS	?GetX	?PutS	?PutX	?Inv_Ack	?GetS_Ack	?GetX_Ack			
I		!GetS(H <sub>D</sub> )	!GetX(H <sub>D</sub> )								
		$\rightarrow I^{S}$	$\rightarrow$ I <sup>M</sup>								
S			$\begin{array}{c} !GetX(H_D) \\ \to S^M \end{array}$	$  Put\_Ack(LLC) \rightarrow S^{NS}$							
S <sup>NS</sup>	$\begin{array}{c} !PutS(H_D) \\ \to S^I \end{array}$	RepMemRd !GetS_Ack(LLC, Data) $\rightarrow$ S	$\begin{array}{c} !GetX(H_D) \\ \to S^M \end{array}$								
S <sup>F</sup>						$\begin{array}{c} !Inv\_Ack(H_D) \\ \to I \end{array}$					
S <sup>IF</sup>						$\begin{array}{c} !Inv\_Ack(H_D) \\ \to S^I \end{array}$					
S <sup>MF</sup>						$\exists Inv\_Ack(H_D)$ $\rightarrow I^M$					
М					$ \begin{array}{c} RepMemWr \\ !PutX(H_D, \\ Data) \\ \to M^I \end{array} $						
M <sup>F</sup>							$\begin{array}{l} RepMemWr \\ !GetS\_Ack(H_D, \\ Data) \\ \to S \end{array}$	$\begin{array}{l} RepMemWr \\ !GetX\_Ack(H_D, \\ Data) \\ \to I \end{array}$			
M <sup>IF</sup>							$ \begin{array}{l} RepMemWr \\ !GetS\_Ack(H_D, \\ Data) \\ \to M^{IS} \end{array} $	$ \begin{array}{l} RepMemWr \\ !GetX\_Ack(H_D, \\ Data) \\ \to M^{II} \end{array} $			

Table 2: Allow-Based Protocol: Home Directory forwarded requests and responses

	H <sub>D</sub>										
State	?Inv	?Fwd_GetS	?Fwd_GetX	?GetS_Ack	?GetX_Ack	?Put_Ack	?Fwd_WB				
I							RepMemWr				
_							!WB_Ack(H <sub>D</sub> )				
I <sup>S</sup>				RepMemWr !GetS_Ack(LLC, Data) → S			RepMemWr !WB_Ack(H <sub>D</sub> )				
I <sub>M</sub>					RepMemWr !GetX_Ack(LLC, Data) → M		RepMemWr !WB_Ack(H <sub>D</sub> )				
S	$\exists Inv(LLC)$ $\rightarrow S^{IF}$										
S <sup>NS</sup>	$\begin{array}{c} !Inv\_Ack(H_D) \\ \to I \end{array}$										
S <sup>I</sup>	!Inv_Ack(H <sub>D</sub> )					$\begin{array}{c} !Put\_Ack(LLC) \\ \to I \end{array}$	RepMemWr !WB_Ack(H <sub>D</sub> )				
S <sup>M</sup>	$\begin{array}{c} !Inv(LLC) \\ \rightarrow S^{MF} \end{array}$				RepMemRd !GetX_Ack(LLC, Data) → M						
М		$\begin{array}{l} !Fwd\_GetS(LLC) \\ \to S^F \end{array}$	$\begin{array}{c} !Fwd\_GetX(LLC) \\ \to M^F \end{array}$								
MI		$\begin{array}{l} !Fwd\_GetS(LLC) \\ \to S^IF \end{array}$	$\begin{array}{l} \text{!Fwd\_GetX(LLC)} \\ \rightarrow \ \text{M}^{\text{IF}} \end{array}$			$\begin{array}{l} !Put\_Ack(LLC) \\ \to S \end{array}$					
M <sup>II</sup>						$\begin{array}{c} !Put\_Ack(LLC) \\ \to I \end{array}$	RepMemWr !WB_Ack(H <sub>D</sub> )				
M <sup>IS</sup>	$\begin{array}{c} !Inv(LLC) \\ \to S^{IF} \end{array}$					$\begin{array}{c} !Put\_Ack(LLC) \\ \to S \end{array}$					