Erriez DHT22 library for Arduino 1.2.0

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Chapter 1

DS1302 RTC (Real Time Clock) library for Arduino

This is an optimized 3-wire DS1302 RTC (Real Time Clock) library for Arduino.

Library features

- · Read / write RTC date and time.
- Read / write 31 Bytes battery backupped RTC RAM.
- Programmable trickle charge to charge super-caps / lithium batteries.
- · Optimized IO interface for Atmel AVR platform.
- · Tested on platforms:
 - 8-bit Atmel AVR (Arduino UNO/Nano/Mini/Micro/Leonardo/Mega2560)
 - 32-bit ESP8266 (WeMos D1 & R2/Node MCU ESP12E)
 - 32-bit ESP32 (WeMos LOLIN32 + OLED)
- · Supported IDE's:
 - Arduino IDE (v1.8.5)
 - CLion (2018.1)
 - Atom / PlatformIO with CI (Continuous Integration)
 - Atmel Studio (7.0)

DS1302 specifications

IMPORTANT NOTES:

- The DS1302 RTC time may deviate >1 minute each day, so this device is not recommended for designs with high precision requirements.
- \bullet The high precision DS3231 I2C RTC is recommended for new designs.
- The 3-wire interface is **NOT** compatible with SPI.

Examples

Arduino IDE | File | Examples | Erriez DS1302 RTC:

- Alarm: Program one or more alarms.
- Benchmark: Benchmark library.
- GettingStarted: Getting started example.
- PrintDateTime: Print date and time with PROGMEM strings.
- RAM: Read/write RTC RAM.
- SetDateTime: Set date time.
- SetTrickleCharger: Program trickle battery/capacitor charger.
- SquareWave1Hz: 1Hz square wave output on DIGITAL pin.
- Terminal and Python script to set date time.

Documentation

- Online HTML
- Download PDF.
- DS1302 datasheet.

Usage

Initialization

```
#include <ErriezDS1302.h>
// Connect DS1302 data pin to Arduino DIGITAL pin
#if defined(ARDUINO_ARCH_AVR)
#define DS1302_CLK_PIN
#define DS1302_IO_PIN
#define DS1302_CE_PIN
#elif defined(ARDUINO_ARCH_ESP8266)
#define DS1302 CLK PIN
#define DS1302_IO_PIN
#define DS1302_CE_PIN
                             D3
#elif defined(ARDUINO_ARCH_ESP32)
#define DS1302_CLK_PIN
                             0
#define DS1302_IO_PIN
                             4
#define DS1302_CE_PIN
#else
#error #error "May work, but not tested on this target"
// Create DS1302 RTC object
DS1302 rtc = DS1302(DS1302_CLK_PIN, DS1302_IO_PIN, DS1302_CE_PIN);
void setup()
    bool running;
    // Initialize RTC
    running = rtc.begin();
```

Set date and time

```
{C++}
DS1302_DateTime dt;

// Set initial date and time
dt.second = 0;
dt.minute = 41;
dt.hour = 22;
dt.dayWeek = 6; // 1 = Monday
dt.dayMonth = 21;
dt.month = 4;
dt.year = 2018;
rtc.setDateTime(&dt);
```

Get date and time

Set time

```
{c++}
// Set time
rtc.setTime(12, 0, 0);
```

Get time

```
{c++}
uint8_t hour;
uint8_t minute;
uint8_t second;
char buf[10];

// Read RTC time
if (!rtc.getTime(&hour, &minute, &second)) {
        Serial.println(F("Error: DS1302 read failed"));
} else {
        // Print time
        snprintf(buf, sizeof(buf), "%d:%02d:%02d", hour, minute, second);
        Serial.println(buf);
}
```

Write to RTC RAM

```
{c++}
// Write Byte to RTC RAM
rtc.writeByteRAM(0x02, 0xA9);

// Write buffer to RTC RAM
uint8_t buf[NUM_DS1302_RAM_REGS] = { 0x00 };
rtc.writeBufferRAM(buf, sizeof(buf));
```

Read from RTC RAM

```
{c++}
// Read byte from RTC RAM
uint8_t dataByte = rtc.readByteRAM(0x02);

// Read buffer from RTC RAM
uint8_t buf[NUM_DS1302_RAM_REGS];
rtc.readBufferRAM(buf, sizeof(buf));
```

Set Trickle Charger

Please refer to the datasheet how to configure the trickle charger.

```
{c++}
// Disable (default)
rtc.writeClockRegister(DS1302_REG_TC, DS1302_TCS_DISABLE);
// Minimum 2 Diodes, 8kOhm
rtc.writeClockRegister(DS1302_REG_TC, 0xAB);
// Maximum 1 Diode, 2kOhm
rtc.writeClockRegister(DS1302_REG_TC, 0xA5);
```

Set RTC date and time using Python

Flash Terminal example.

Set COM port in examples/Terminal/Terminal.py Python script.

Run Python script:

```
{c++}
// Install Pyserial
python3 pip -m pyserial
// Set RTC date and time
python3 Terminal.py
```

Pin configuration

Note: ESP8266 pin D4 is high during a power cycle / reset / flashing which may corrupt RTC registers. For this reason, pins D2 and D4 are swapped.

DS1302 Pin	DS1302 IC	Atmel AVR	ESP8266	ESP32
4	GND	GND	GND	GND
8	VCC2	5V (or 3.3V)	3V3	3V3
7	SCLK (CLK)	2 (DIGITAL pin)	D4	0
6	I/O (DAT)	3 (DIGITAL pin)	D2	5
5	CE (RST)	4 (DIGITAL pin)	D2	4

Benchmark results

Arduino UNO (AVR F_CPU = 16MHz)

```
DS1302 RTC benchmark

rtc.begin(): 160us

rtc.writeProtect(false): 148us

rtc.halt(false): 144us

rtc.setDateTime(&dt): 720us

rtc.getDateTime(&dt): 496us

rtc.setTime(12, 0, 0): 1224us

rtc.getTime(&hour, &minute, &second): 272us

rtc.writeRAM(0x00, 0xFF): 144us

rtc.writeRAM(buf, sizeof(buf): 1796us

rtc.readRAM(0x00): 140us

rtc.readRAM(Muf, sizeof(buf)): 1812us
```

WeMos D1 & R2 (ESP8266 F_CPU = 80MHz)

```
DS1302 RTC benchmark

rtc.begin(): 180us

rtc.writeProtect(false): 112us

rtc.halt(false): 149us

rtc.setDateTime(&dt): 369us

rtc.getDateTime(&dt): 273us

rtc.setTime(12, 0, 0): 571us

rtc.getTime(&hour, &minute, &second): 154us

rtc.writeRAM(0x00, 0xFF): 86us

rtc.writeRAM(0x00, sizeof(buf): 852us

rtc.readRAM(0x00): 84us

rtc.readRAM(Muf, sizeof(buf)): 881us
```

WeMos D1 & R2 (ESP8266 F_CPU = 160MHz)

```
DS1302 RTC benchmark

rtc.begin(): 152us

rtc.writeProtect(false): 73us

rtc.halt(false): 108us

rtc.setDateTime(&dt): 257us

rtc.getDateTime(&dt): 187us

rtc.setTime(12, 0, 0): 373us

rtc.getTime(&hour, &minute, &second): 105us

rtc.writeRAM(0x00, 0xFF): 62us

rtc.writeRAM(buf, sizeof(buf): 553us

rtc.readRAM(0x00): 62us

rtc.readRAM(buf, sizeof(buf)): 568us
```

Library installation

Please refer to the Wiki page.

Other Arduino Libraries and Sketches from Erriez

• Erriez Libraries and Sketches

DS1302 RTC (Rea	Time	Clock) librar	y tor	Arduino
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Chapter 2

Class Index

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File Index

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Chapter 4

Class Documentation

4.1 DS1302 Class Reference

```
DS1302 RTC class.
```

```
#include <ErriezDS1302.h>
```

Public Member Functions

DS1302 (uint8_t clkPin, uint8_t ioPin, uint8_t cePin)

Constructor DS1302 RTC.

• virtual bool begin ()

Initialize DS1302.

• virtual void writeProtect (bool enable)

Set write protect flag.

virtual bool isWriteProtected ()

Get write protect state.

virtual void halt (bool halt)

Set RTC clock halted or running.

• virtual bool isHalted ()

Get RTC halt status.

virtual void setDateTime (DS1302_DateTime *dateTime)

Set RTC date and time.

virtual bool getDateTime (DS1302_DateTime *dateTime)

Get RTC date and time.

virtual void setTime (uint8_t hour, uint8_t minute, uint8_t second)

Set RTC time.

• virtual bool getTime (uint8_t *hour, uint8_t *minute, uint8_t *second)

Get RTC time.

• virtual void writeClockRegister (uint8_t reg, uint8_t value)

Write clock register.

virtual uint8_t readClockRegister (uint8_t reg)

Read clock register.

• virtual void writeByteRAM (uint8_t addr, uint8_t value)

Write a byte to RAM.

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```
    virtual void writeBufferRAM (uint8_t *buf, uint8_t len)
```

Write buffer to RAM address 0x00 (burst write)

virtual uint8_t readByteRAM (uint8_t addr)

Read byte from RAM.

virtual void readBufferRAM (uint8_t *buf, uint8_t len)

Read buffer from RAM address 0x00 (burst read)

Protected Member Functions

• virtual void transferBegin ()

Start RTC transfer.

virtual void transferEnd ()

End RTC transfer.

virtual void writeAddrCmd (uint8_t value)

Write address/command byte.

virtual void writeByte (uint8_t value)

Write byte.

• virtual uint8_t readByte ()

Read Byte from RTC.

virtual void readBuffer (void *buf, uint8_t len)

Read buffer from DS1302.

virtual uint8_t bcdToDec (uint8_t bcd)

BCD to decimal conversion.

virtual uint8_t decToBcd (uint8_t dec)

Decimal to BCD conversion.

Protected Attributes

```
• uint8 t clkPin
```

Clock pin.

• uint8_t _ioPin

Data pin.

uint8_t _cePin

Chip enable pin.

4.1.1 Detailed Description

DS1302 RTC class.

Definition at line 139 of file ErriezDS1302.h.

4.1.2 Constructor & Destructor Documentation

4.1.2.1 DS1302()

Constructor DS1302 RTC.

Parameters

clkPin	Clock pin
ioPin	I/O pin.
cePin	Chip select pin. (In previous versions RST pin which is the same)

Definition at line 44 of file ErriezDS1302.cpp.

4.1.3 Member Function Documentation

4.1.3.1 bcdToDec()

BCD to decimal conversion.

Parameters

bcd	BCD encoded value
-----	-------------------

Returns

Decimal value

Definition at line 485 of file ErriezDS1302.cpp.

4.1.3.2 begin()

```
bool DS1302::begin ( ) [virtual]
```

Initialize DS1302.

Call this function from setup().

Returns

true: RTC running false: RTC halted or not detected

Definition at line 70 of file ErriezDS1302.cpp.

4.1.3.3 decToBcd()

Decimal to BCD conversion.

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Parameters

Returns

BCD encoded value

Definition at line 497 of file ErriezDS1302.cpp.

4.1.3.4 getDateTime()

Get RTC date and time.

Parameters

dateTime I	Date and time structure
------------	-------------------------

Definition at line 183 of file ErriezDS1302.cpp.

4.1.3.5 getTime()

Get RTC time.

Parameters

hour	Hours			
minute	Minutes			
second	Seconds			

Definition at line 241 of file ErriezDS1302.cpp.

4.1.3.6 halt()

```
void DS1302::halt (
                bool halt ) [virtual]
```

Set RTC clock halted or running.

Parameters

```
halt true: Enable RTC clock false: Halt RTC clock
```

Definition at line 120 of file ErriezDS1302.cpp.

4.1.3.7 isHalted()

```
bool DS1302::isHalted ( ) [virtual]
```

Get RTC halt status.

Returns

true: RTC clock is halted false: RTC clock is running

Definition at line 143 of file ErriezDS1302.cpp.

4.1.3.8 isWriteProtected()

```
bool DS1302::isWriteProtected ( ) [virtual]
```

Get write protect state.

Returns

true: RTC registers are read only false: RTC registers are writable

Definition at line 105 of file ErriezDS1302.cpp.

4.1.3.9 readBuffer()

Read buffer from DS1302.

Parameters

buf	Buffer
len	Buffer length

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Definition at line 471 of file ErriezDS1302.cpp.

4.1.3.10 readBufferRAM()

Read buffer from RAM address 0x00 (burst read)

Parameters

buf	Data buffer
len	Buffer length

Definition at line 325 of file ErriezDS1302.cpp.

4.1.3.11 readByte()

```
uint8_t DS1302::readByte ( ) [protected], [virtual]
```

Read Byte from RTC.

Returns

Data Byte

Definition at line 444 of file ErriezDS1302.cpp.

4.1.3.12 readByteRAM()

Read byte from RAM.

Parameters

Returns

RAM byte 0..0xFF

Definition at line 306 of file ErriezDS1302.cpp.

4.1.3.13 readClockRegister()

Read clock register.

Parameters

```
reg RTC clock register (See datasheet)
```

Returns

Register value (See datasheet)

Definition at line 358 of file ErriezDS1302.cpp.

4.1.3.14 setDateTime()

Set RTC date and time.

Parameters

```
dateTime Date time structure
```

Definition at line 157 of file ErriezDS1302.cpp.

4.1.3.15 setTime()

Set RTC time.

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Parameters

hour	Hours
minute	Minutes
second	Seconds

Definition at line 224 of file ErriezDS1302.cpp.

4.1.3.16 writeAddrCmd()

Write address/command byte.

Parameters

value	Address/command byte
-------	----------------------

Definition at line 397 of file ErriezDS1302.cpp.

4.1.3.17 writeBufferRAM()

Write buffer to RAM address 0x00 (burst write)

Parameters

buf	Data buffer
len	Buffer length 0x010x1E

Definition at line 289 of file ErriezDS1302.cpp.

4.1.3.18 writeByte()

Write byte.

Parameters

yte
١

Definition at line 423 of file ErriezDS1302.cpp.

4.1.3.19 writeByteRAM()

Write a byte to RAM.

Parameters

addr	RAM address 00x1E
value	RAM byte 00xFF

Definition at line 274 of file ErriezDS1302.cpp.

4.1.3.20 writeClockRegister()

Write clock register.

Parameters

reg	RTC clock register (See datasheet)
value	Register value (See datasheet)

Definition at line 343 of file ErriezDS1302.cpp.

4.1.3.21 writeProtect()

Set write protect flag.

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Parameters

enable true: Enable RTC write protect false: Disable RTC write protect

Definition at line 94 of file ErriezDS1302.cpp.

The documentation for this class was generated from the following files:

- src/ErriezDS1302.h
- src/ErriezDS1302.cpp

4.2 DS1302_DateTime Struct Reference

Date time structure.

```
#include <ErriezDS1302.h>
```

Public Attributes

uint8_t second

Second 0..59.

• uint8_t minute

Minute 0..59.

• uint8_t hour

Hour 0..23.

uint8_t dayWeek

Day of the week (1 = Monday)

uint8_t dayMonth

Day of the month 1..31.

uint8_t month

Month 1..12.

• uint16_t year

Year 2000..2099.

4.2.1 Detailed Description

Date time structure.

Definition at line 127 of file ErriezDS1302.h.

The documentation for this struct was generated from the following file:

• src/ErriezDS1302.h

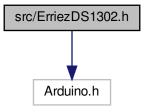
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File Documentation

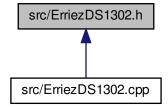
5.1 src/ErriezDS1302.h File Reference

DS1302 RTC library for Arduino.

#include <Arduino.h>
Include dependency graph for ErriezDS1302.h:



This graph shows which files directly or indirectly include this file:



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Classes

• struct DS1302_DateTime

Date time structure.

• class DS1302

DS1302 RTC class.

Macros

• #define DS1302 ACB 0x80

DS1302 address/command register.

• #define DS1302 ACB RAM 0x40

Address command RAM.

#define DS1302 ACB CLOCK 0x00

Address command clock.

#define DS1302 ACB READ 0x01

Address command read.

#define DS1302 ACB WRITE 0x00

Address command write.

#define DS1302_CMD_READ_CLOCK_REG(reg) (DS1302_ACB | DS1302_ACB_CLOCK | (((reg) & 0x1F) << 1) | DS1302_ACB_READ)

DS1302 read clock register.

#define DS1302_CMD_WRITE_CLOCK_REG(reg) (DS1302_ACB | DS1302_ACB_CLOCK | (((reg) & 0x1F) << 1) | DS1302_ACB_WRITE)

DS1302 write clock register.

#define DS1302_CMD_READ_CLOCK_BURST (DS1302_ACB | DS1302_ACB_CLOCK | 0x3E | DS1302 ←
 _ACB_READ)

DS1302 read clock register with burst.

DS1302 writeclock register with burst.

#define DS1302_CMD_READ_RAM(addr) (DS1302_ACB | DS1302_ACB_RAM | (((addr) & 0x1F) << 1) | DS1302_ACB_READ)

DS1302 read RAM register.

* #define DS1302_CMD_WRITE_RAM(addr) (DS1302_ACB | DS1302_ACB_RAM | (((addr) & 0x1F) << 1) | DS1302_ACB_WRITE)

DS1302 write RAM register.

#define DS1302_CMD_READ_RAM_BURST (DS1302_ACB | DS1302_ACB_RAM | 0x3E | DS1302_AC
 B_READ)

DS1302 read RAM register with burst.

#define DS1302_CMD_WRITE_RAM_BURST (DS1302_ACB | DS1302_ACB_RAM | 0x3E | DS1302_A⇔
 CB_WRITE)

DS1302 write RAM register with burst.

#define DS1302 REG SECONDS 0x00

DS1302 registers.

#define DS1302_REG_MINUTES 0x01

Minutes register.

#define DS1302_REG_HOURS 0x02

Hours register.

• #define DS1302 REG DAY MONTH 0x03

Day of the month register.

```
    #define DS1302_REG_MONTH 0x04

     Month register.
• #define DS1302_REG_DAY_WEEK 0x05
     Day of the week register.
• #define DS1302_REG_YEAR 0x06
     Year register.

    #define DS1302_REG_WP 0x07

     Write protect register.

    #define DS1302_REG_TC 0x08

     Tickle Charger register.
#define NUM_DS1302_RAM_REGS 31
     DS1302 number of RAM registers.

    #define DS1302_BIT_CH 7

     DS1302 register bit defines.

 #define DS1302 BIT WP 7

     Write protect bit.
• #define DS1302_BIT_READ 0
     Bit read.

    #define DS1302_TCS_DISABLE 0x5C

     Tickle Charger disable value.
#define DS1302_CLK_LOW() { digitalWrite(_clkPin, LOW); }
     CLK pin low.

    #define DS1302_CLK_HIGH() { digitalWrite(_clkPin, HIGH); }

     CLK pin high.

    #define DS1302_CLK_INPUT() { pinMode(_clkPin, INPUT); }

     CLK pin input.
#define DS1302_CLK_OUTPUT() { pinMode(_clkPin, OUTPUT); }
     CLK pin output.

    #define DS1302 IO LOW() { digitalWrite( ioPin, LOW); }

     IO pin low.

    #define DS1302_IO_HIGH() { digitalWrite(_ioPin, HIGH); }

     IO pin high.

    #define DS1302 IO INPUT() { pinMode( ioPin, INPUT); }

     IO pin input.

    #define DS1302_IO_OUTPUT() { pinMode(_ioPin, OUTPUT); }

     IO pin output.

    #define DS1302_IO_READ() ( digitalRead(_ioPin) )

     IO pin read.
#define DS1302_CE_LOW() { digitalWrite(_cePin, LOW); }
     CE pin low.

    #define DS1302_CE_HIGH() { digitalWrite(_cePin, HIGH); }

     CE pin high.
#define DS1302_CE_INPUT() { pinMode(_cePin, INPUT); }
     CE pin input.
#define DS1302_CE_OUTPUT() { pinMode(_cePin, OUTPUT); }
     CE pin output.

    #define DS1302 PIN DELAY()

     Delay between pin changes.
```

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5.1.1 Detailed Description

DS1302 RTC library for Arduino.

Source: https://github.com/Erriez/ErriezDS1302 Documentation: https://erriez. \leftarrow github.io/ErriezDS1302

5.1.2 Macro Definition Documentation

5.1.2.1 DS1302_ACB

#define DS1302_ACB 0x80

DS1302 address/command register.

Address command date/time

Definition at line 39 of file ErriezDS1302.h.

5.1.2.2 DS1302_BIT_CH

#define DS1302_BIT_CH 7

DS1302 register bit defines.

Clock halt bit

Definition at line 77 of file ErriezDS1302.h.

5.1.2.3 DS1302_REG_SECONDS

#define DS1302_REG_SECONDS 0x00

DS1302 registers.

Seconds register

Definition at line 63 of file ErriezDS1302.h.

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