#### Sega Game Gear on a Chip

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### First slide

some content here

## Sega Game Gear

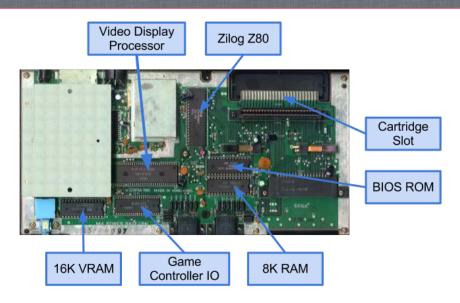


### Sega Game Gear

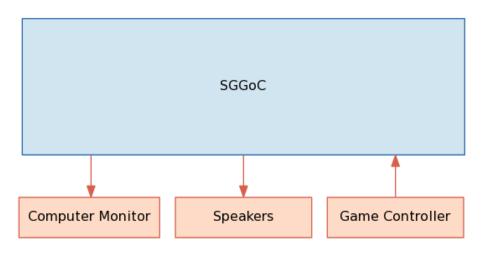
- Released April 1990
- Mobile version of the Sega Master System (functionally identical)
- Standard system architecture for the time (Z80 CPU, tri-state buses, etc...)



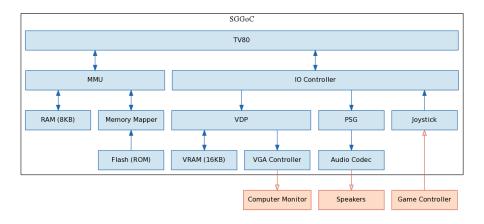
### Sega Master System PCB



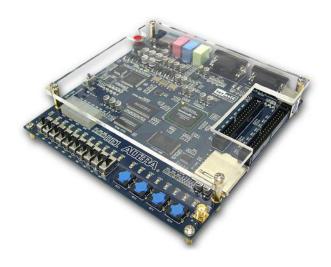
## Black Box Diagram



## Transparent Box Diagram

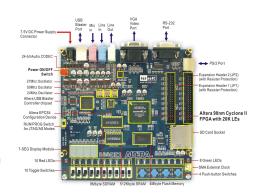


## **FPGA** Development Board



#### Altera DE1

- Cyclone II EP2C20F484C7
- VGA, Audio, SD Card, 4 MB Flash
- Command line development environment
- Extremely good documentation







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- Game data ROM
- Memory Mapper



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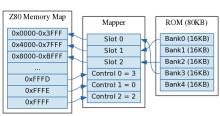
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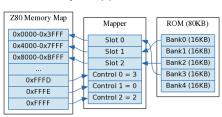
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Each game cartridge made up of at least two components:

- Game data ROM
- Memory Mapper





- 1. Hookup the actual cartridge
  - Straight forward
  - Don't have to re-implement the memory mappers
  - Defeats most the point of the project
- 2. Store them on a SD card
  - Extremely portable / convenient
  - Even more complicated
- 3. Store them on the 4MB flash chip
  - Fairly straightforward
  - Extremely non-portable
  - Flash chip looks just like original ROM chips

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- 1. Load RS232-to-ROM bridge into the FPGA
- 2. PC waits for FPGA to request a byte
- 3. PC send the next byte of ROM file
- 4. FPGA writes byte to flash
- 5. Go back to 2

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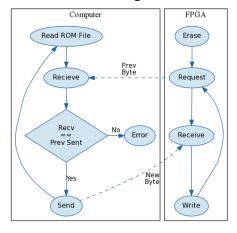
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#### Writing



#### Reading

