

Team Terminal

APPROACH



In order to give the push button enough time to debounce, we reset the flip flops every three seconds.



Voters must cast their ballots when there are three inputs for the AND gate.



The flip flop's toggle input is fed to a counter, which counts the number of votes cast for each candidate.

Using a comparator, we are comparing the votes of two candidates in a pair.

we are using a $2*1$ mux with 4 bit input to get a maximum of 2 votes.

The final output of the multiplexer will be the maximum votes, which are displayed through a 7 segment display, because we are again comparing the maximum votes.





Through logic probes, the two LEDs will demonstrate which candidate actually received the most votes and was declared the winner.

SCHEMATIC AND BOARD

we have implemented the same circuit on Eagle CAD and used the 555 timer as clock signal.

We have also designed the PCB for the same circuit.



Steps and precautions

Only when the AND gate's input is 3 should votes be cast using the toggle; otherwise, it is prohibited.

The subsequent voter must wait 3 seconds after each voter before casting his or her ballot.



Thank You

Team members

Shravani Mamidwar

Kritarth