Arya More

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EDUCATION

Vishwakarma Institute of Information Technology, Pune June 2025 CGPA: 8.2/10 **B-TECH Electronics and Telecommunication** June 2021 K.S.D Shanbhag Vidyalaya and Jr. College Satara **HSC** 85.17 % June 2019 K.S.D Shanbhaq Vidyalaya and Jr. College Satara SSC 89.60 %

SKILLS

Languages: Verilog, Java.

Frameworks/Tools: Cadence (Virtuoso), Hspice, Vivado, LTSpice, Arduino IDE, Git/Github, Tinker-CAD, Proteus.

Communication Protocol: TMDS, I2C, SPI, UART/USART.

Concepts: Analog Design, Digital Design, CMOS IC design, PCB design, Internet of Things (IoT), FPGA, Analog Circuit, Em-

bedded System.

PROJECTS

Design and Analysis of Interference in Mixed-Signal Circuits | Project Trainee - SCL- Mohali

07/2024-present

• Studying the interference and mitigating the interference between Analog and Digital Signals within a Mixed-Signal system.

Fan Speed Controller Using Temperature Sensor | Vivado IDE

08/2023-12/2023

· Implemented PWM-controlled CPU fan speed using Verilog and Vivado IDE, employed project on FPGA

Water Level Monitoring System with Nodemcu ESP8266 | IOT

08/2023-12/2023

- Implemented Water Level Monitoring System using Ultrasonic Sensor and Nodemcu ESP8266.
- · Programmed Node MCU using Arduino IDE.

Vehicles Gear Position Display | Digital Electronics

08/2022-12/2022

• Displayed Gear position on 7-segment display using CD4511 and diodes as a switch.

CO-CURRICULAR ACTIVITIES

Electronics Engineering Students Association (EESA, VIIT) | Ladies Representative(LR)|

08/2023-present

- · Organized workshops and seminars on various electronics topics such as circuit design, microcontrollers, and signal processing to enhance students' understanding and skills.
- · Established a platform for networking and collaboration among students, faculty, and industry professionals through regular meetings, discussions, and events.
- Arranged industry visits and guest lectures by professionals to provide insights into real-world applications and career opportunities in the field of electronics engineering.

CERTIFIED COURSE

NPTEL certificate for the course CMOS Digital VLSI Design Jan 2024- March 2024 Embedded Systems Bootcamp: RTOS, IoT, AI, Vision, and FPGA June 2023 - Oct 2023 Data Structures & Algorithms Using C and C++ Oct 2022 - Sep 2023 Fundamentals of IoT (Internet of Things) Aug 2023 - Oct 2023

EXTRA-CURRICULAR ACTIVITIES

- •6th Position in National Level Basketball Tournament
- •1st Position in Basketball Inter-Zonal University Tournament
- 3rd Position in District level Taekwondo Tournament