COMPUTER ARCHITECTURE
BSCS-4

Lab 12

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TASK 1:

START:
INP
OUT
CLA
OUT
HLT

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: 8
Output:  8
Output:  0
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

TASK 2:

START:
INP
CMA
OUT
HLT

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: 4
Output:  -5
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

4= 00000100
-5=11111011

TASK 3:

INP
INC
OUT
HLT

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: 4
Output:  7
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

TASK 4:

INP
SPA
OUT
HLT

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: 7
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

TASK 5:

INP
SNA
OUT
HLT

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: -7
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```