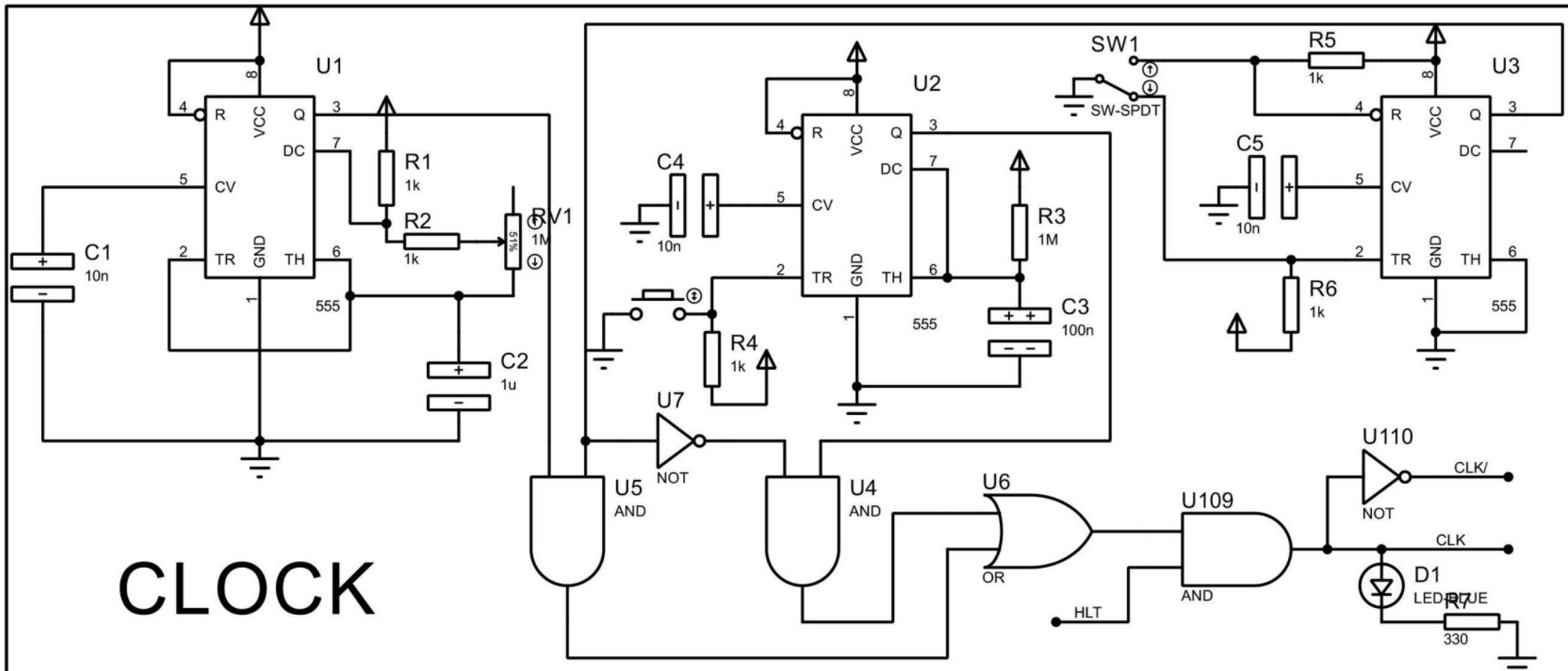
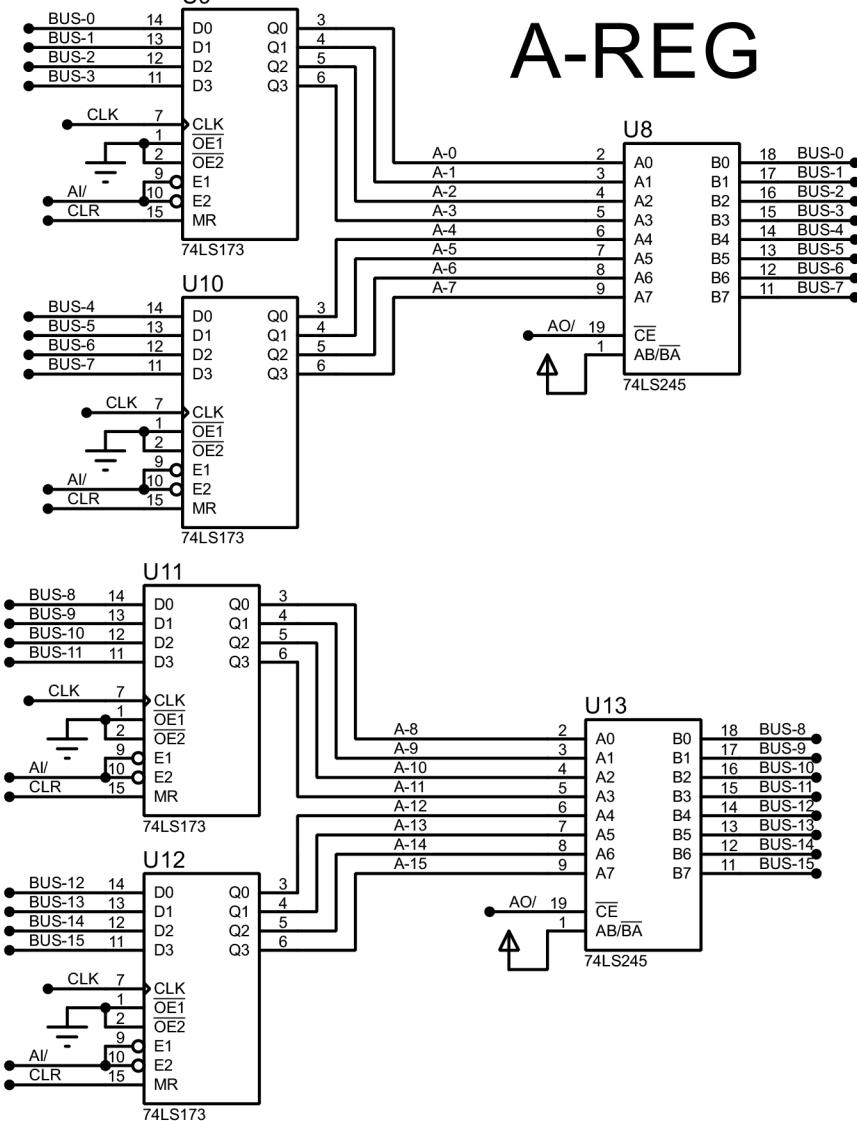


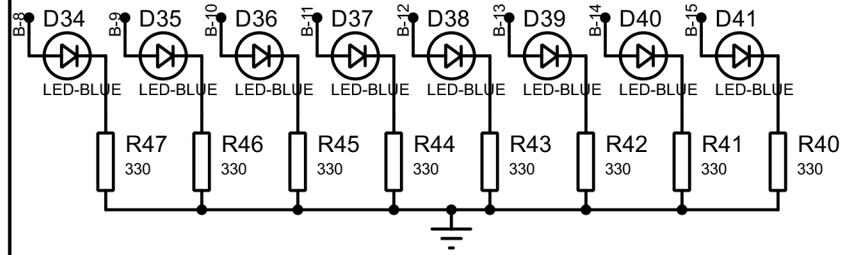
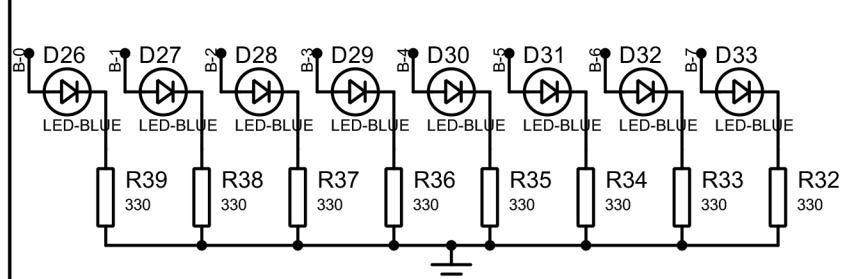
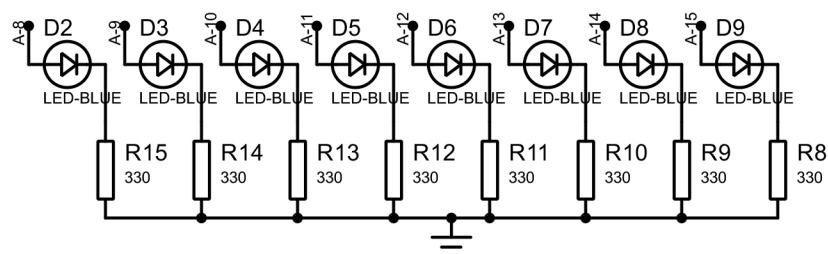
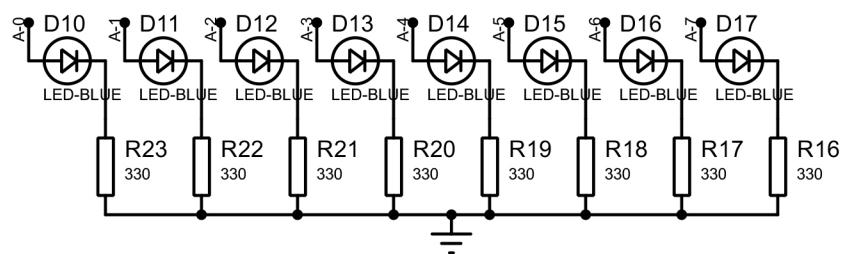
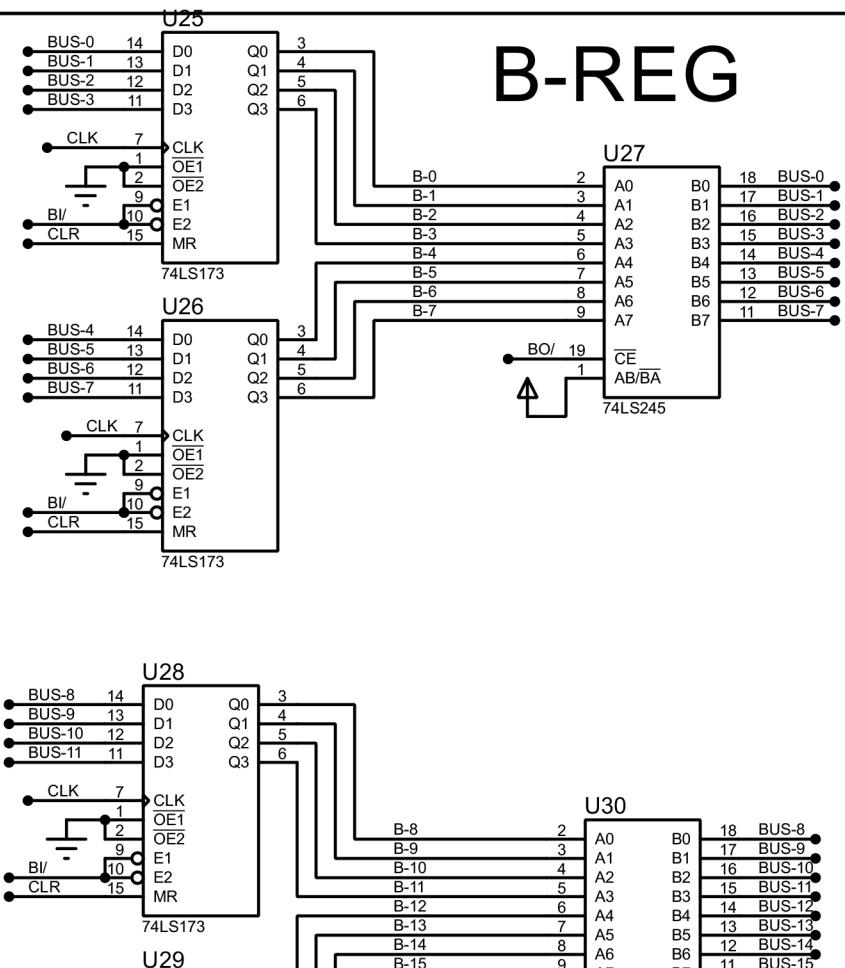
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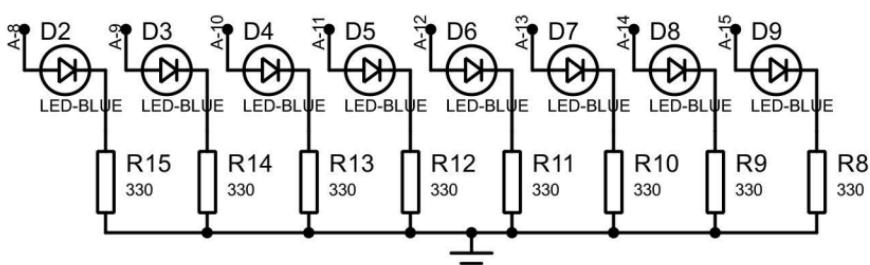
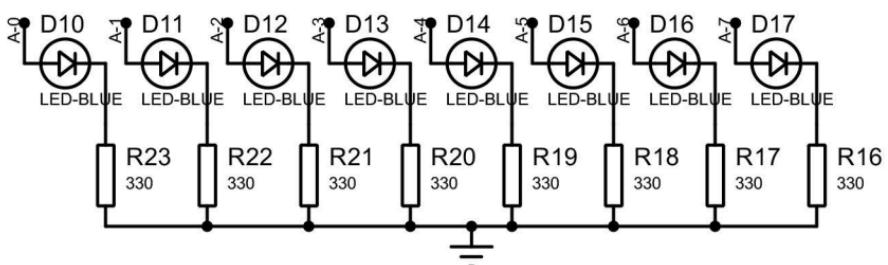
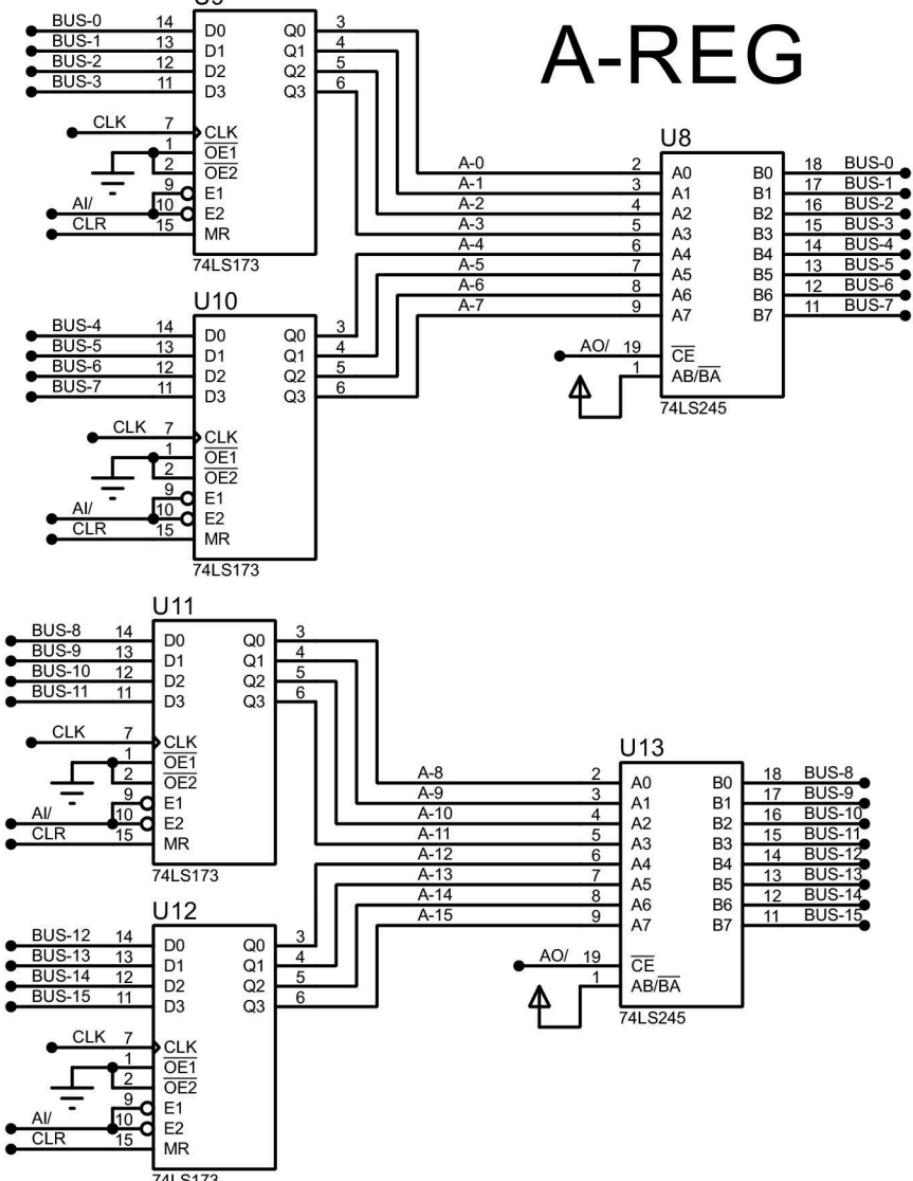
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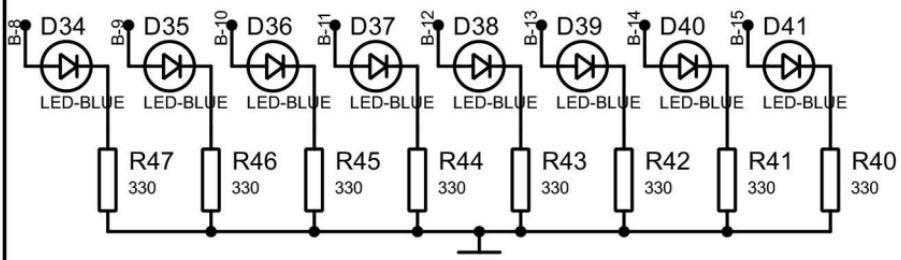
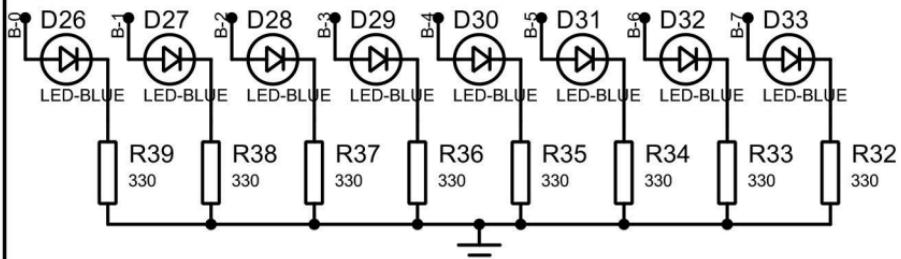
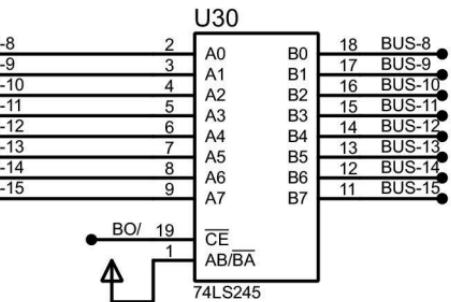
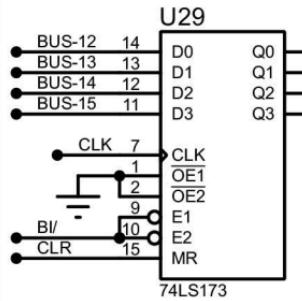
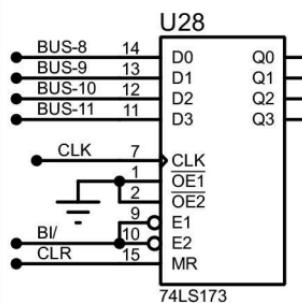
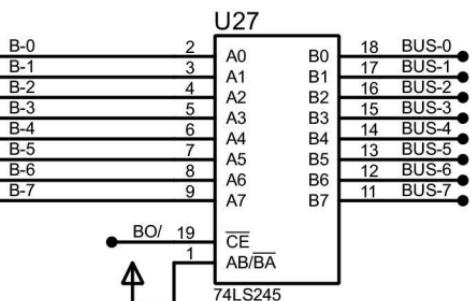
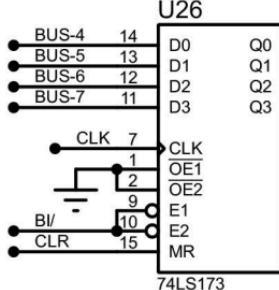
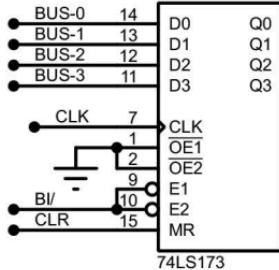
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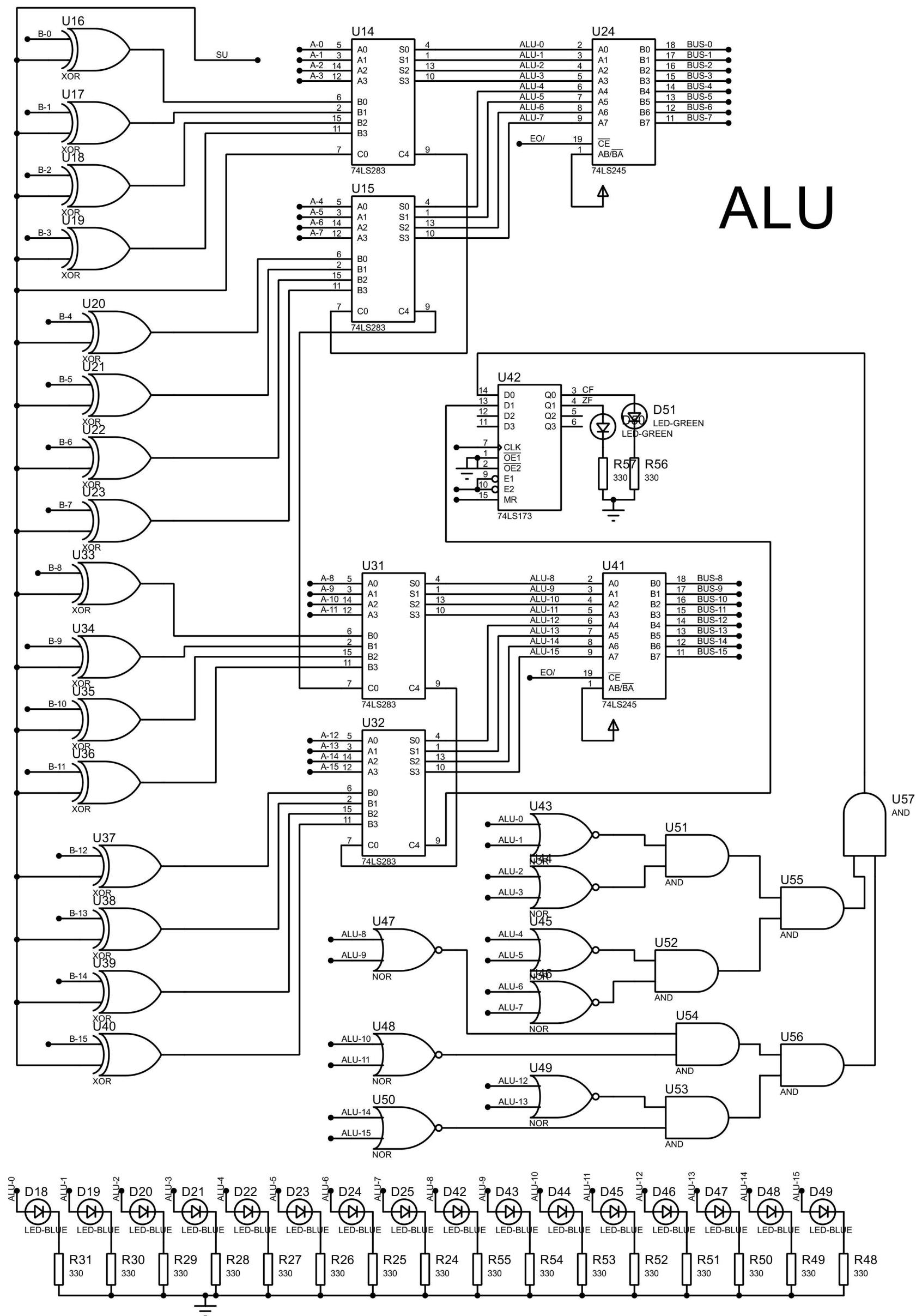
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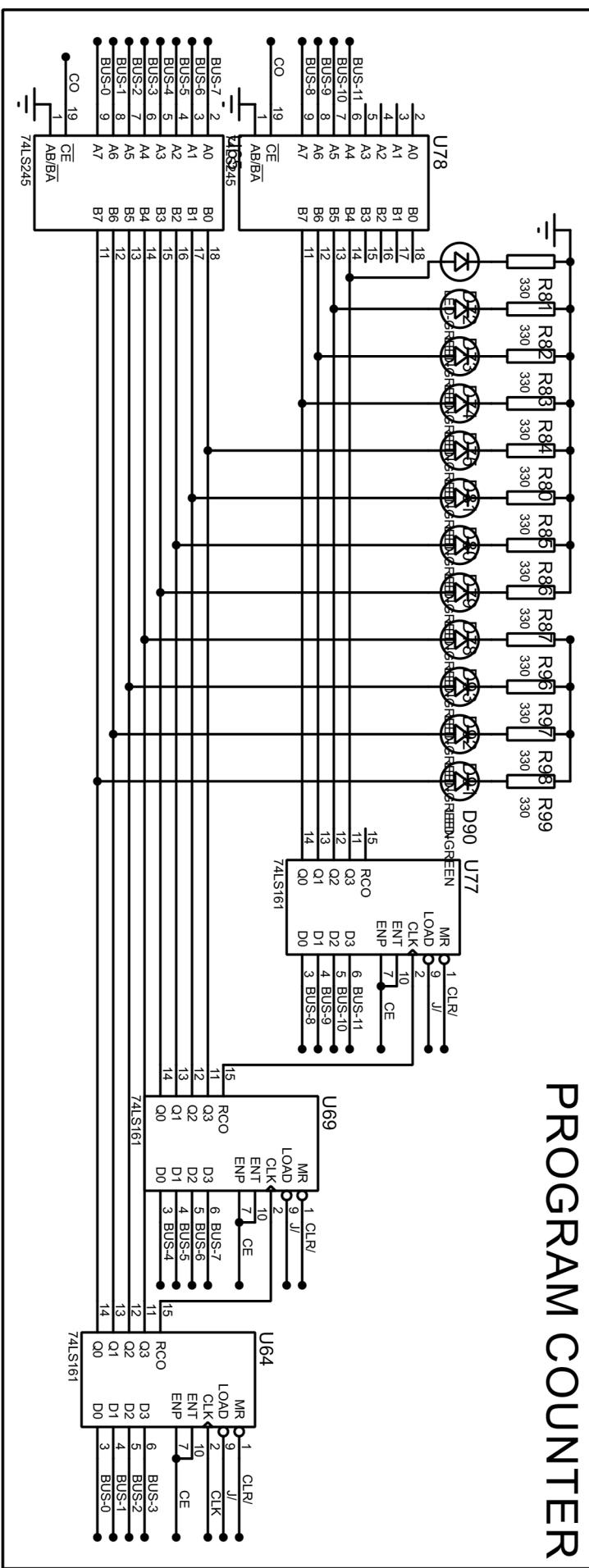
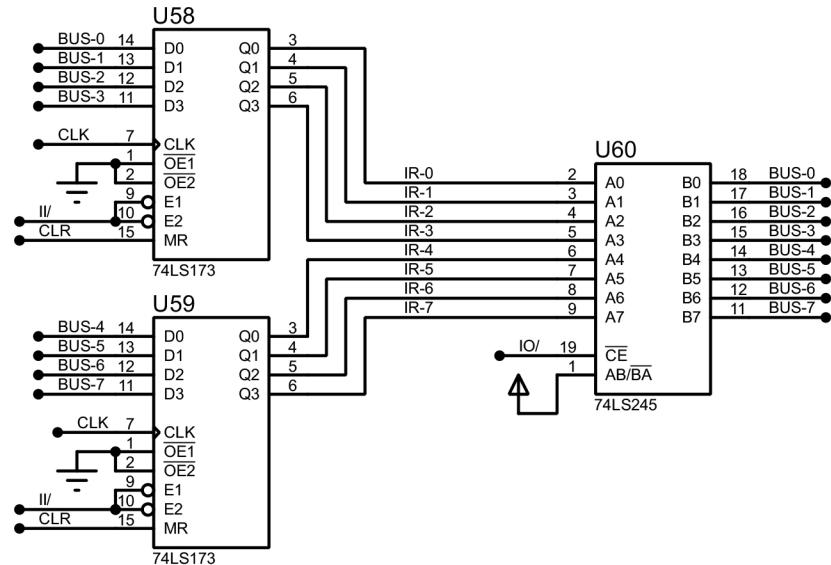


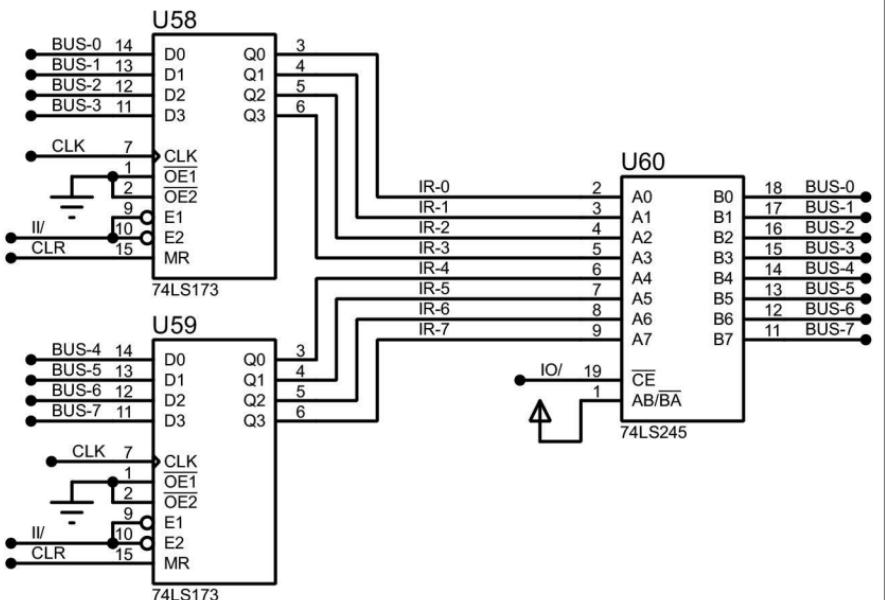
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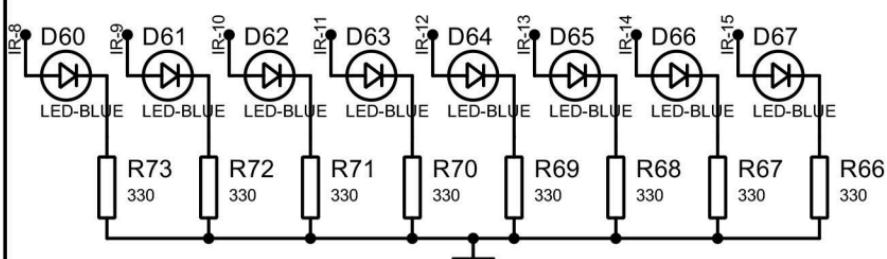
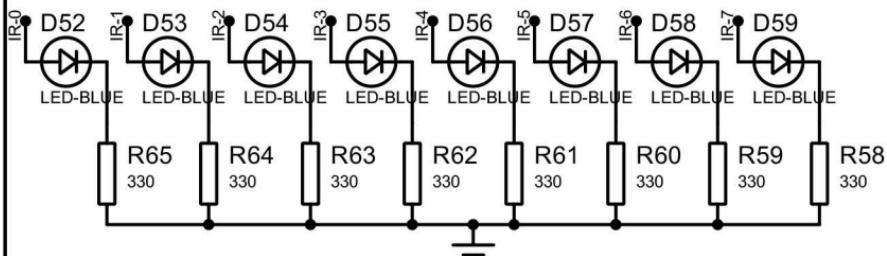
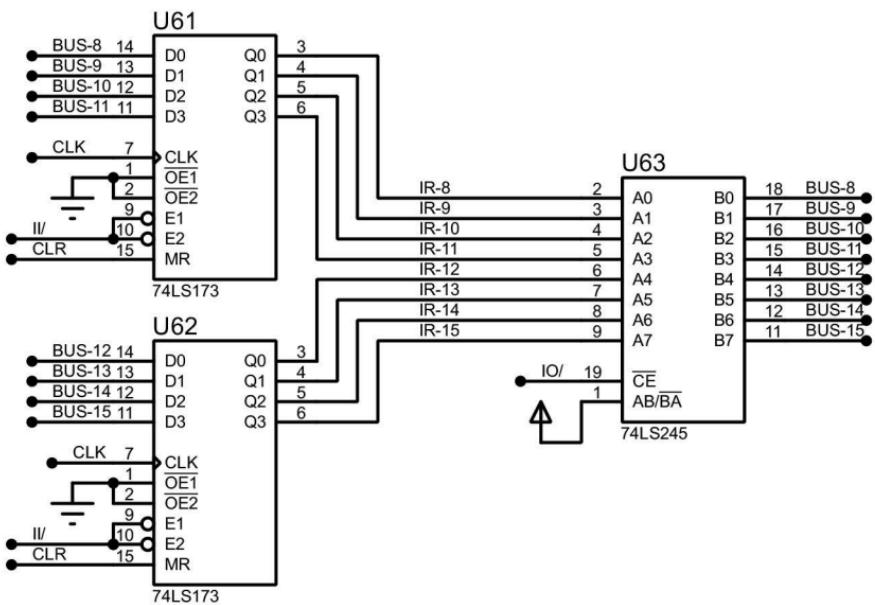
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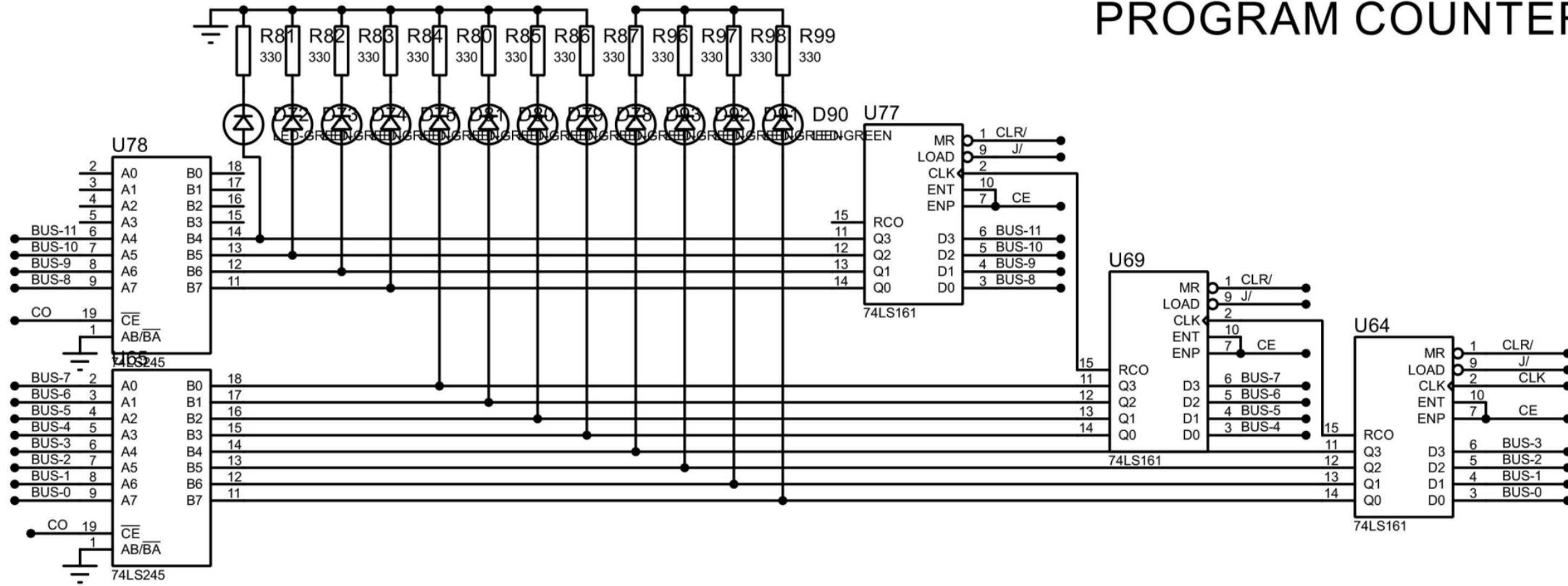


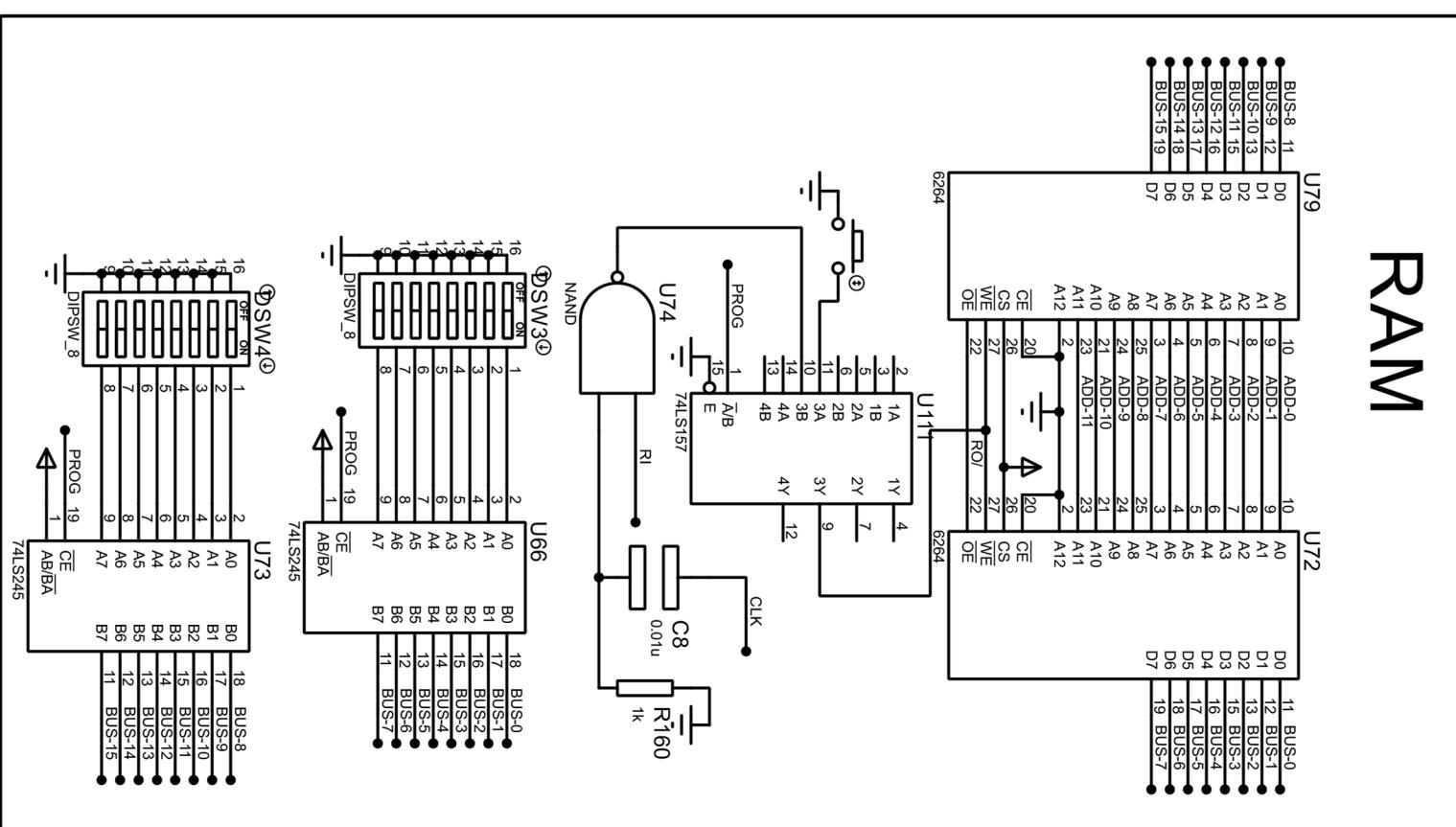
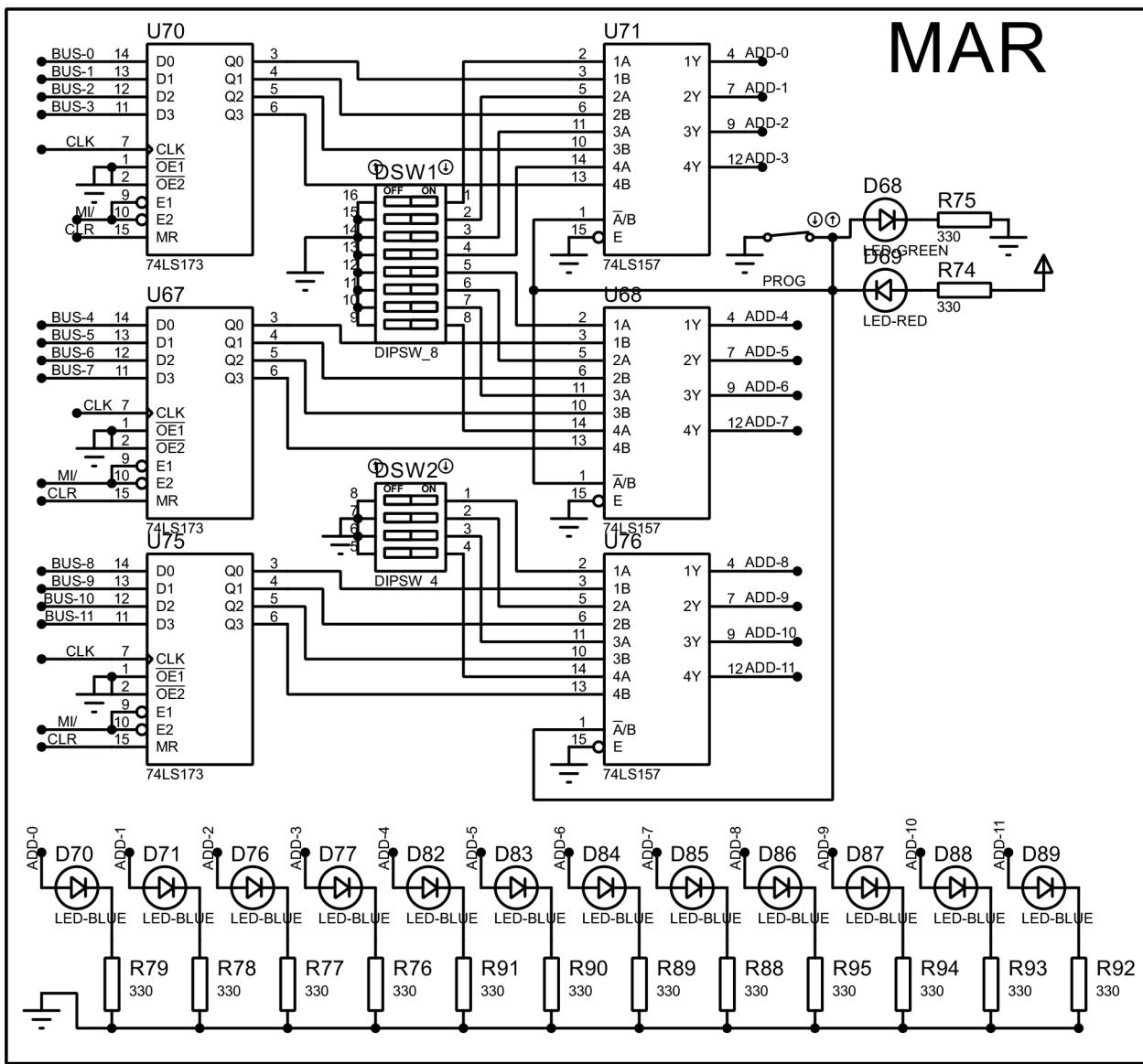


INSTRUCTION-REG

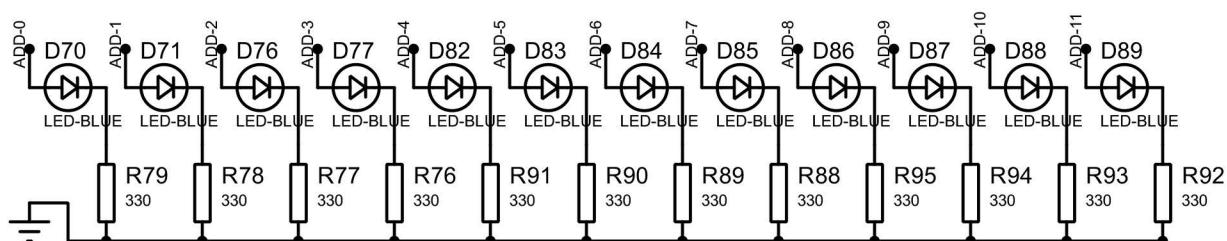
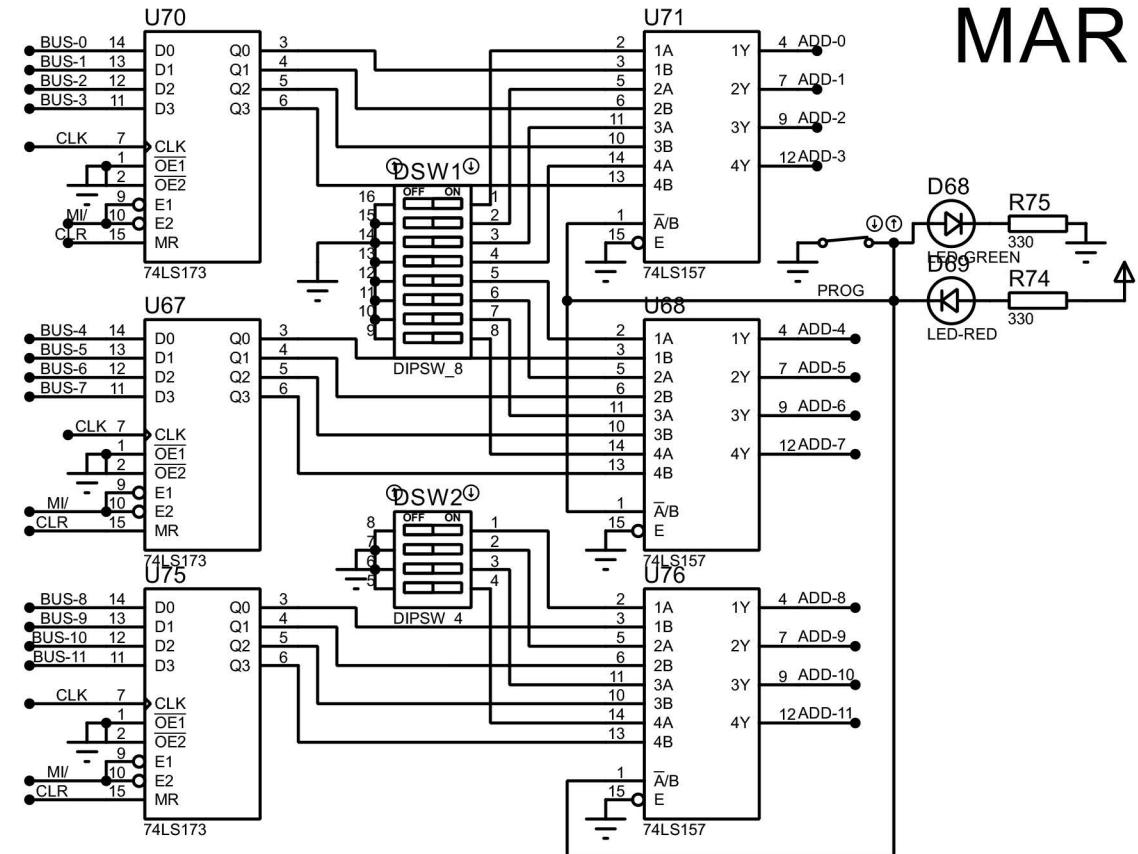


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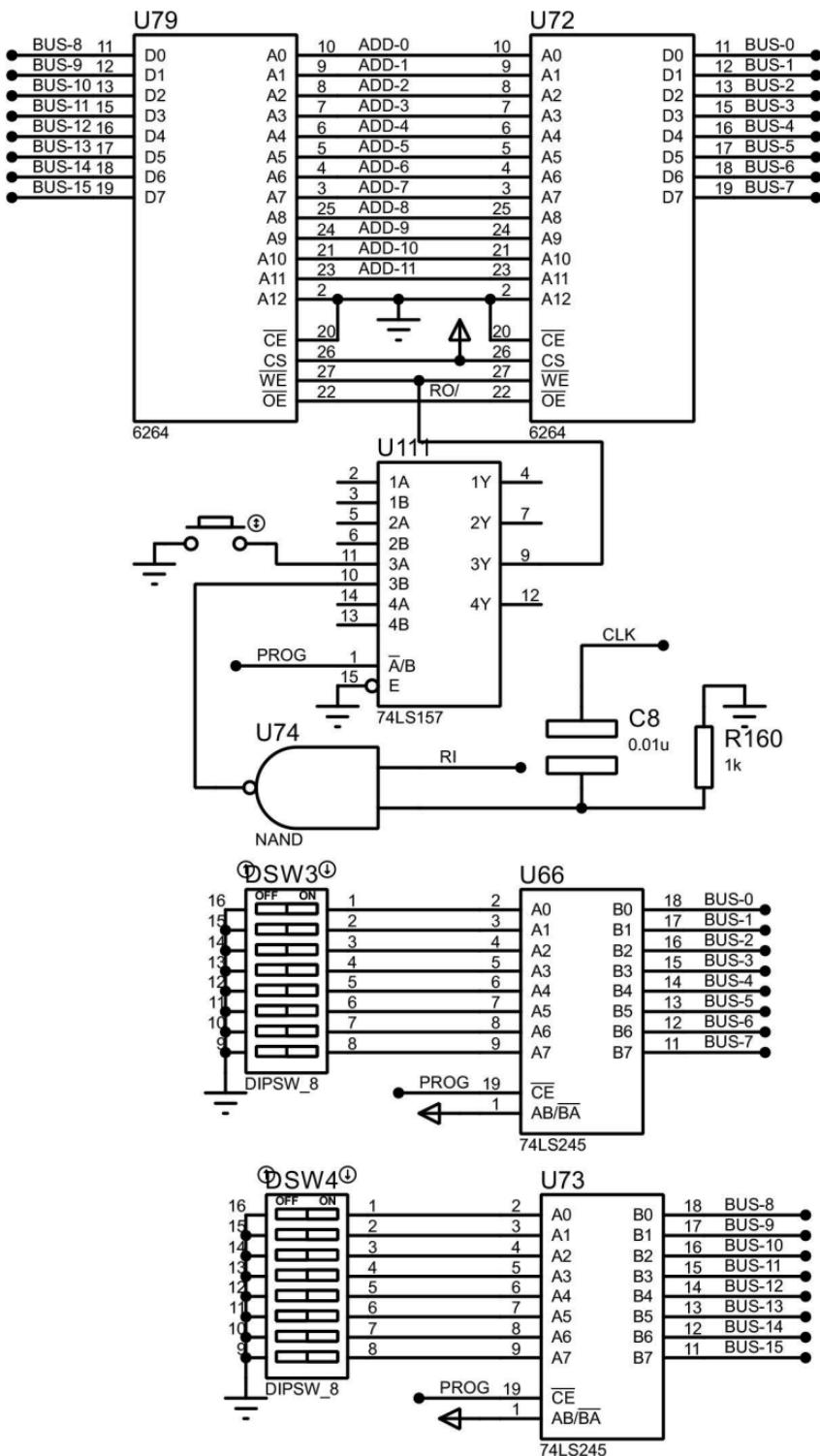


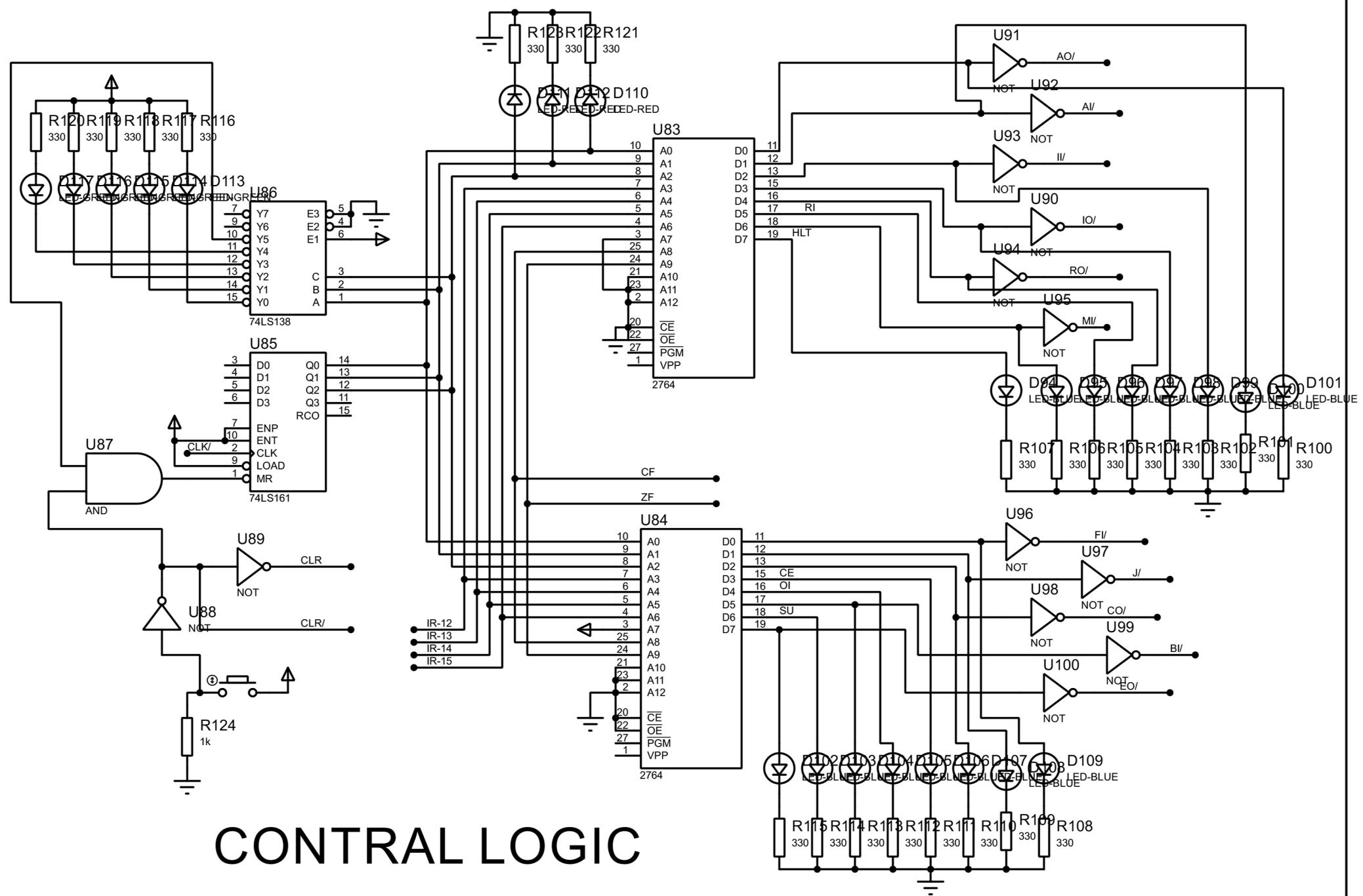


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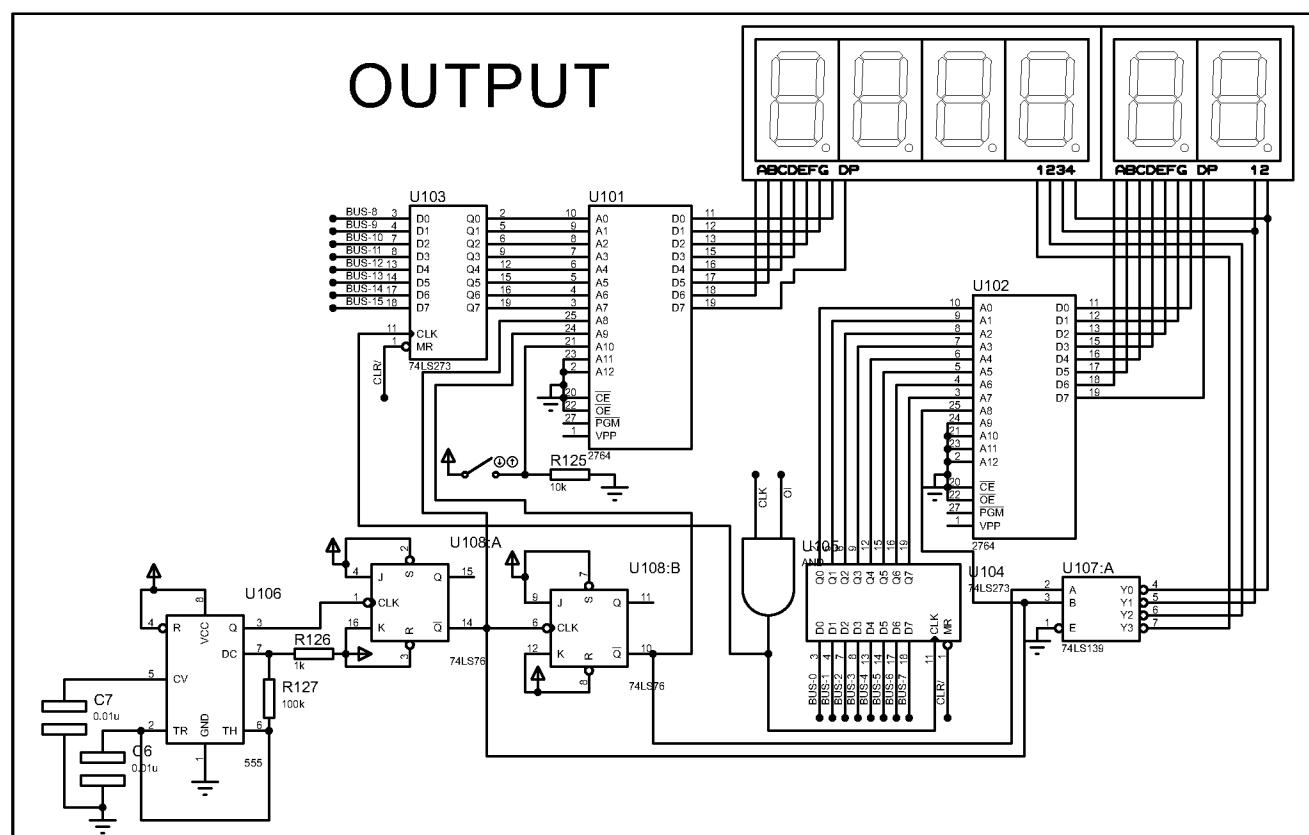


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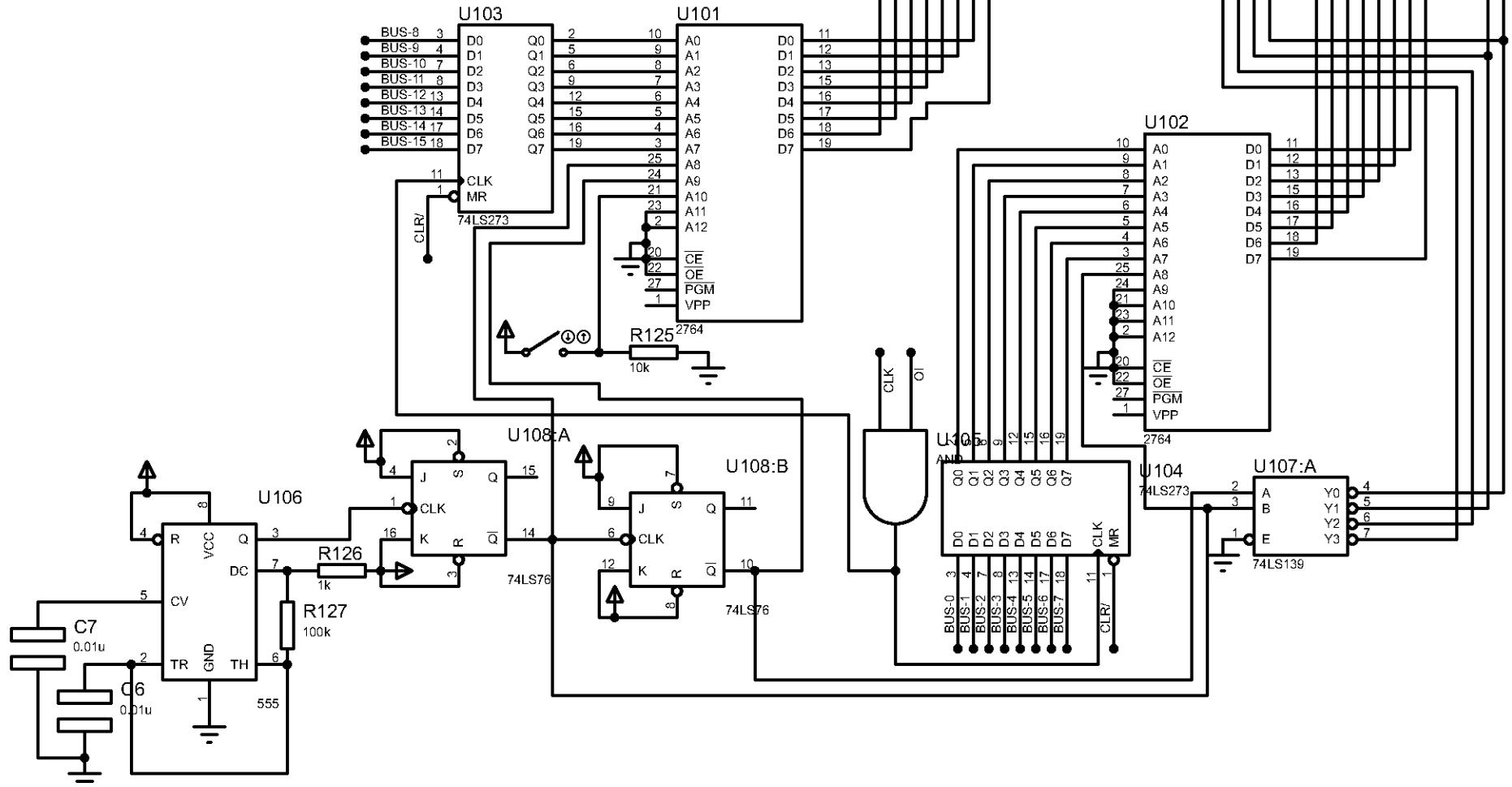


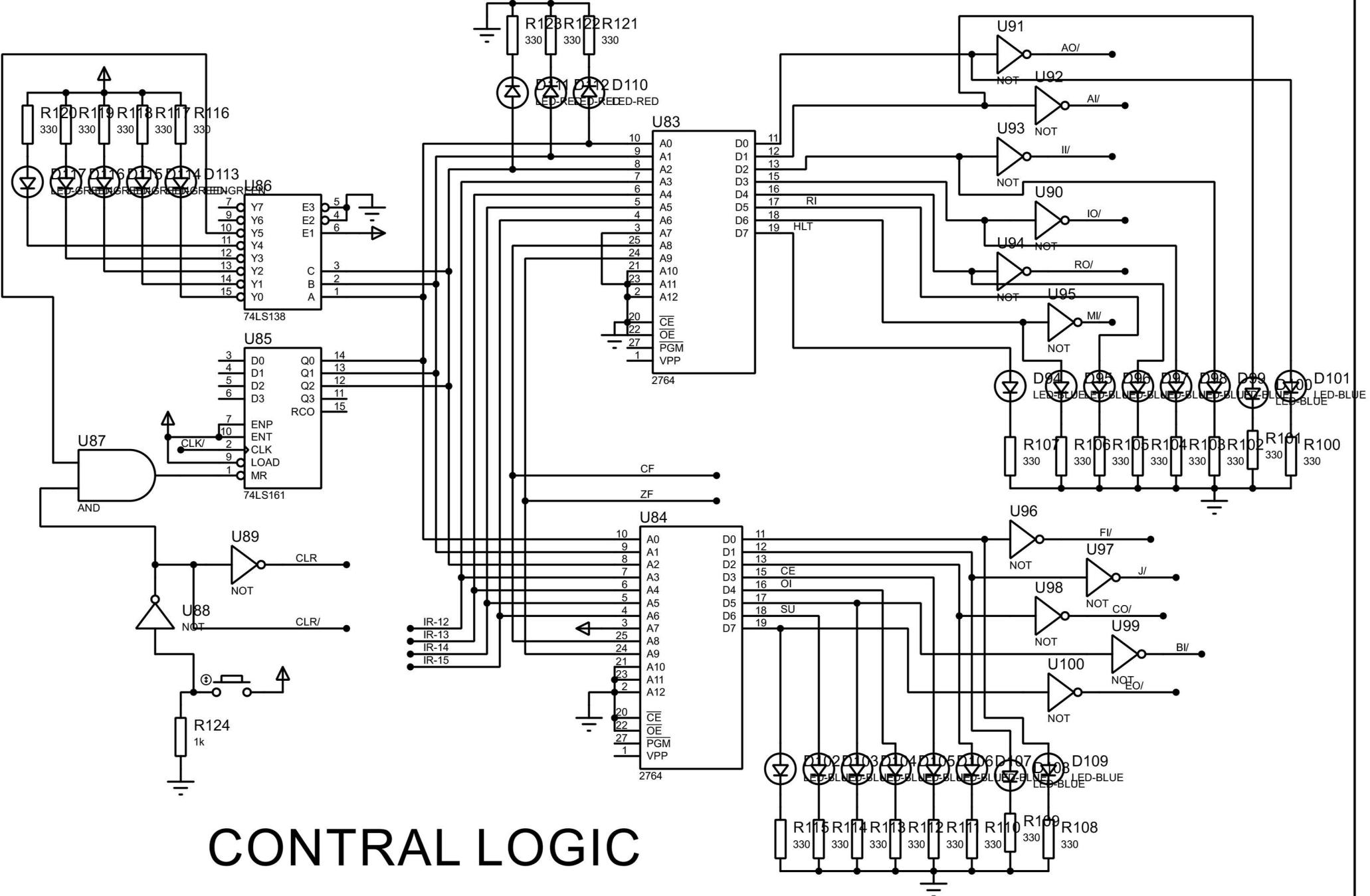


CONTRAL LOGIC



OUTPUT





BUS

