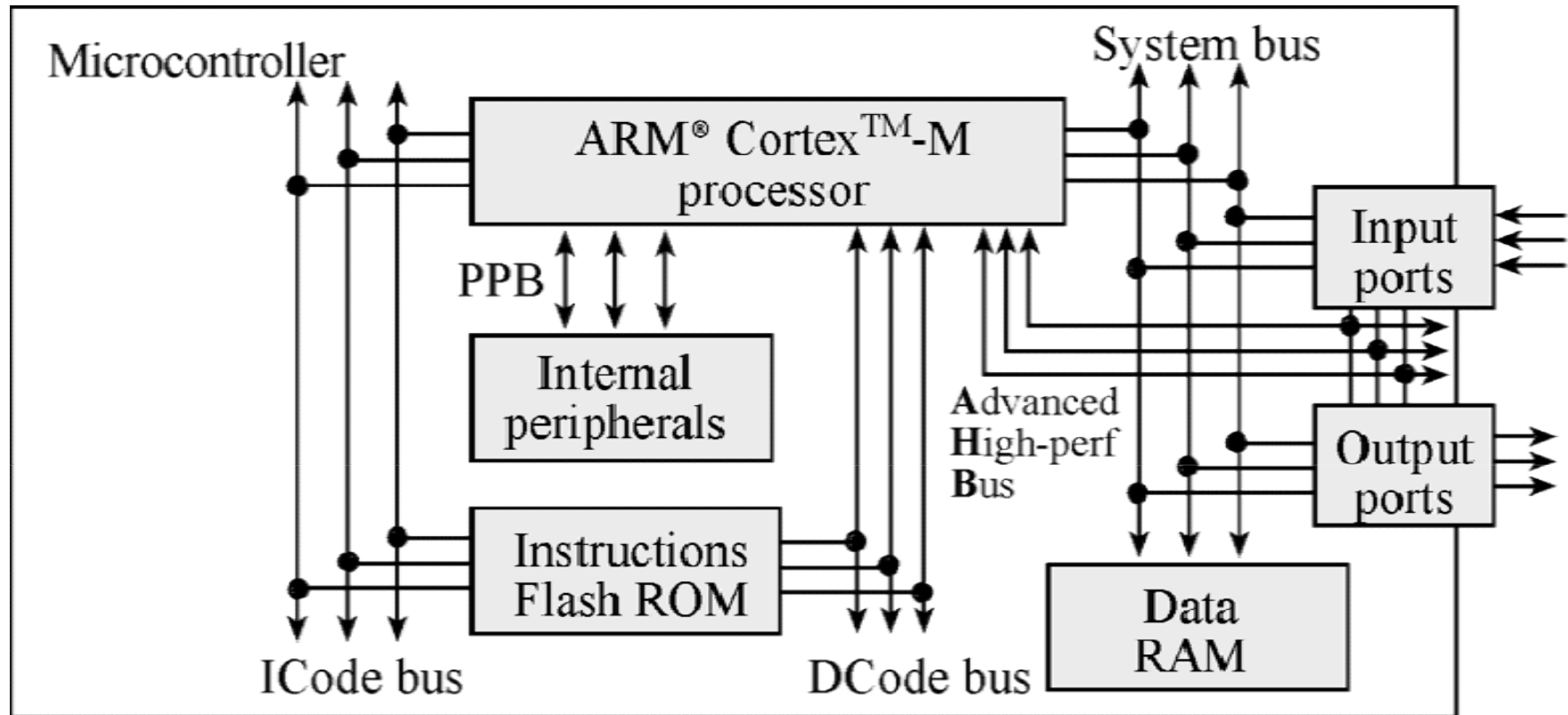
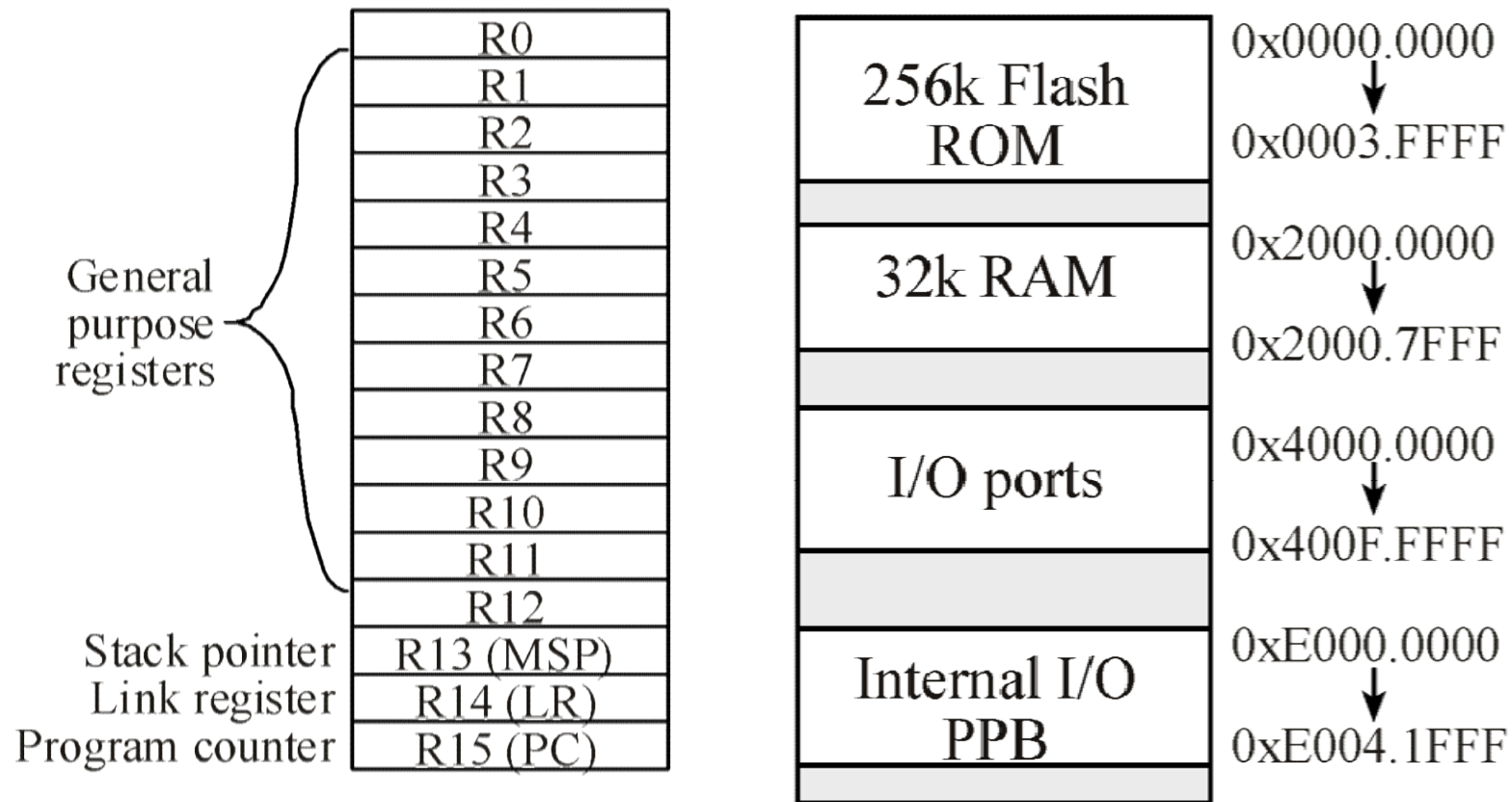


# ARM Cortex M4-based System



- ❑ ARM Cortex-M4 processor
- ❑ *Harvard* architecture
  - ❖ Different busses for instructions and data

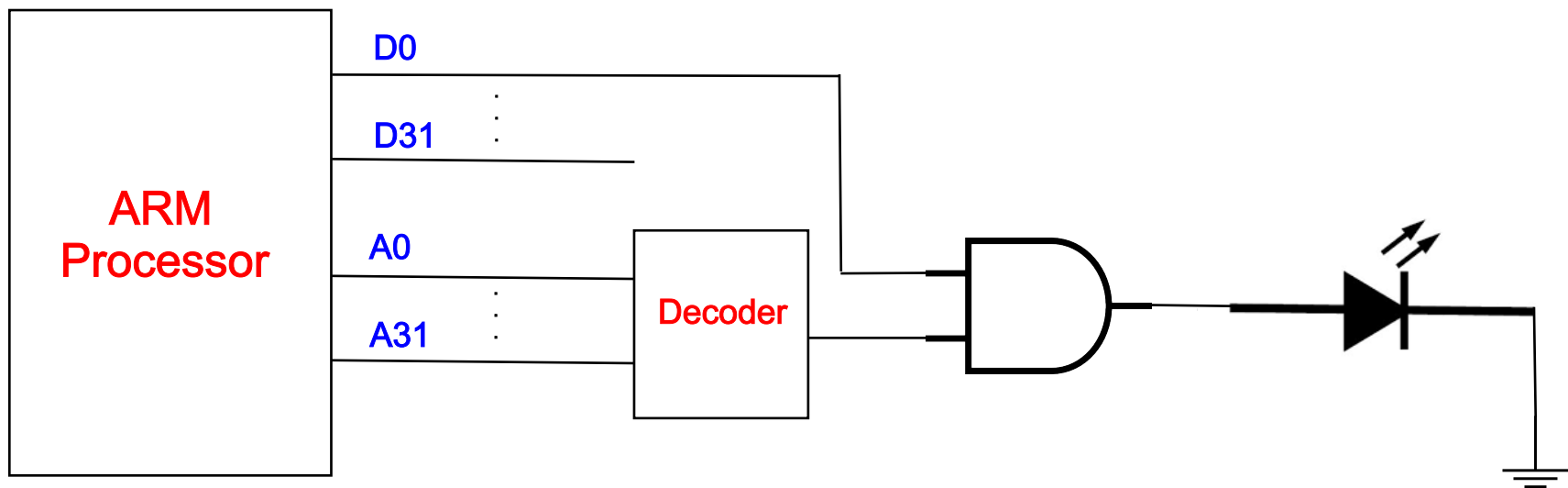
# ARM ISA: Registers, Memory-map



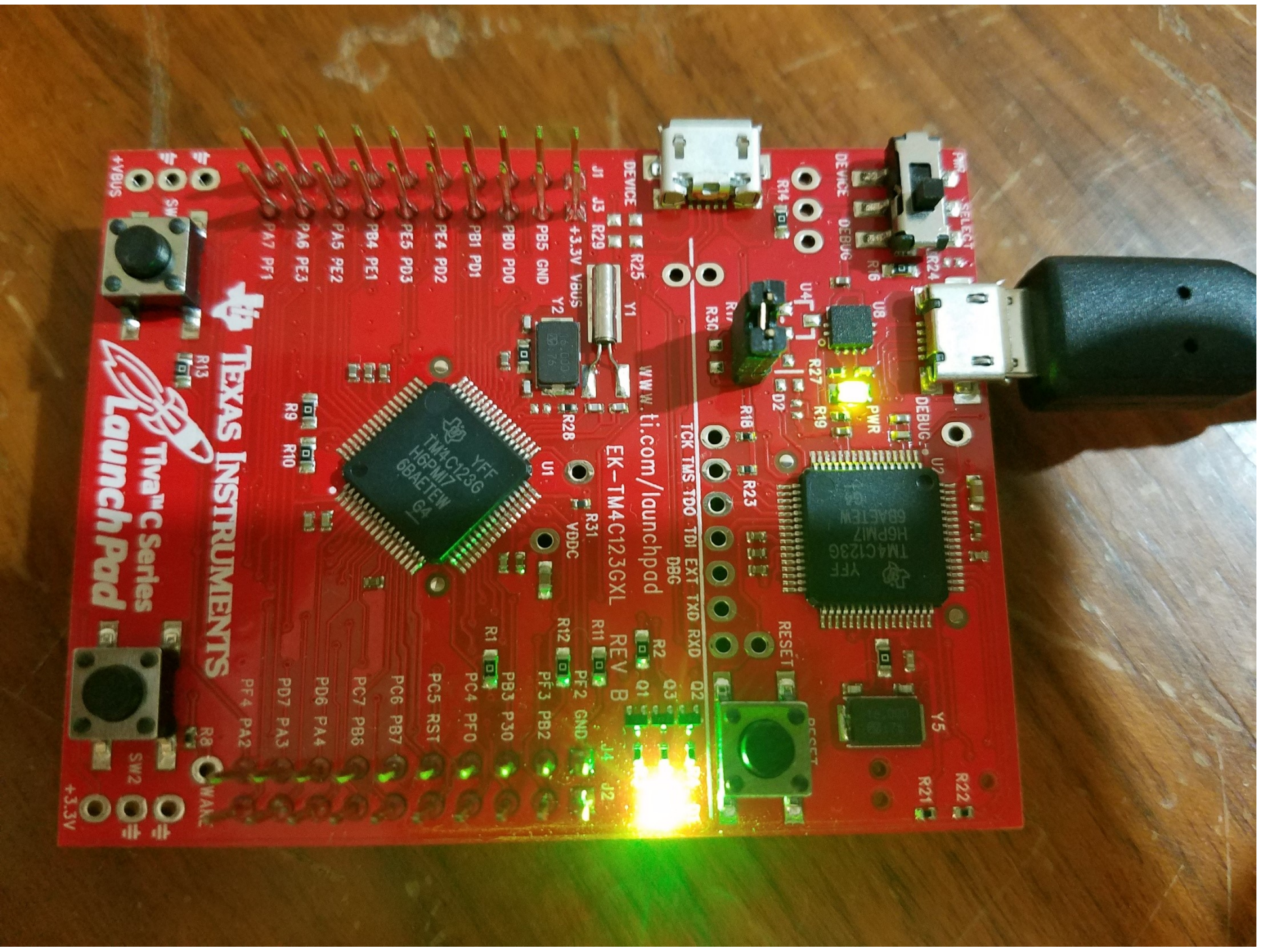
<i><b>Condition Code Bits</b></i>	<i><b>Indicates</b></i>
N	negative
Z	zero
V	overflow
C	carry

Result is negative  
 Result is zero  
 Signed overflow  
 Unsigned overflow

**TI TM4C123  
 Microcontroller**

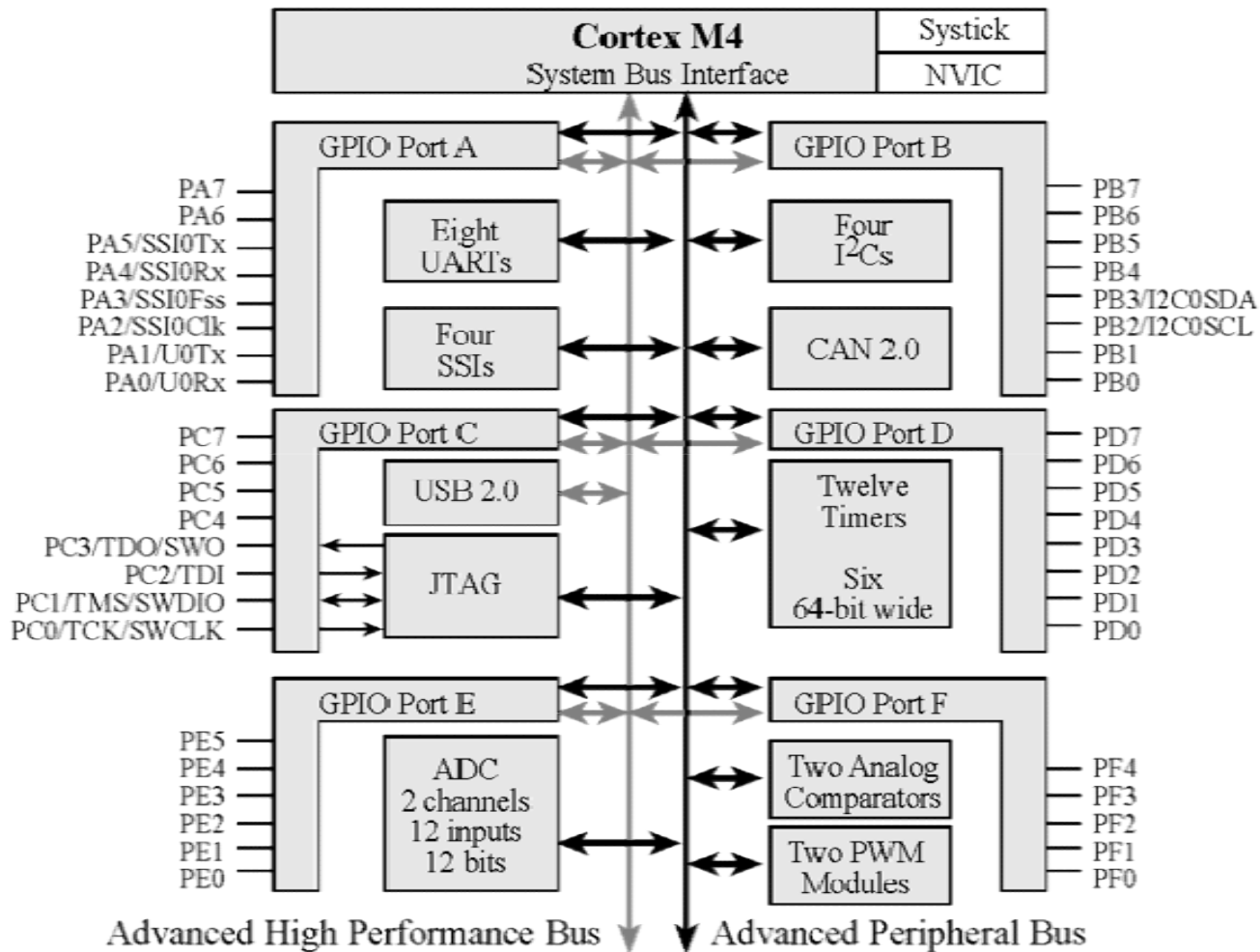








# Texas Instruments TM4C123



**ARM Cortex-M4**  
**+ 256K Flash**  
**+ 32K RAM**  
**+ JTAG**  
**+ 43 Ports**  
**+ SysTick**  
**+ ADC**  
**+ UART**