# AIN SHAMS UNIVERSITY FACULTY OF ENGINEERING

**ICHEP; MCT-CESS** 

Spring 2022



June 9<sup>th</sup>,2022 Course Code: CSE411-345-347 Time: 2 Hours

Real-Time & Embedded Systems Design

The Exam Consists of 5 Questions in 5 Pages

Total Marks: 40 Marks

تنبيه هام جدا: يجب على كل طالب الحل فقط هنا في ورق الاسئلة ولن يلتفت لأي إجابة في الكراسة المرفقة والتي تحتوي على البار كود الخاص بكل طالب

## **Important Rules:**

- Having a (mobile -Smart Watch- earphones) inside the examination hall is forbidden and is considered as a cheating behavior.
- It is forbidden to have any references, notes, books, or any other materials even if it is not related to the exam content with you in the examination hall.
- This is an answer sheet.
- Assume missing data if any Read it all well, at first.

#### <u>نعليمات هامة</u>

- حيازة (المحمول- الساعات الذكية سماعة الأذن) داخل لجنة الامتحان يعتبر حالة غش تستوجب العقاب.
- لايسمح بدخول أي كتب أو ملازم أو أوراق داخل اللجنة والمخالفة
   تعتبر حالة غش.
- هذه ورقة إجابة أيضا أقرأها أولا جيدا أفترض الناقص إن وجد

# Question 1: (3 Marks)

For FreeRTOS based embedded hypothetical application, "heap2" Heap memory algorithm is used. Figure A shows heap memory state where T1, T2 and T3 are allocated tasks. F1, F2, and F3 are free spaces of 100 bytes, 400 bytes, and 900 bytes respectively. Show in the following figures, heap memory states for the cases:

- B: Create T4 of 600 bytes (Stack and TCB)
- C: Create T5 of 350 bytes (Stack and TCB)
- D: Create T6 of 36 bytes (Stack and TCB)

A	T1	F1	Т2	F2	Т3	F3
В	T1		T2		Т3	
C	T1		T2		Т3	
D	T1		Т2		Т3	

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## Question 2: (10 Marks)

Assume the following snippet of code/application that already had all necessary declarations, inclusions, and prototypes. In the given table, order the first 10 break points to be hit, when GO is pressed.

1 <sup>st</sup> Break Point Hit	2 <sup>nd</sup> Break Point Hit	3 <sup>rd</sup> Break Point Hit	4 <sup>th</sup> Break Point Hit	5 <sup>th</sup> Break Point Hit

6 <sup>th</sup> Break Point Hit	7 <sup>th</sup> Break Point Hit	8 <sup>th</sup> Break Point Hit	9 <sup>th</sup> Break Point Hit	10 <sup>th</sup> Break Point Hit

```
82 int main ( void )
    83 - {
            vSemaphoreCreateBinary( xBinarySemaphore );
    84
    85
            prvSetupSoftwareInterrupt();
            xTaskCreate( vPeriodicTask, "Periodic1", 240, NULL, 1, NULL );
    86
             xTaskCreate( vPeriodicTask, "Periodic2", 240, NULL, 3, NULL );
    87
             xTaskCreate( vHandlerTask, "Handler", 240, NULL, 2, NULL );
    88
1
            vTaskStartScheduler();
    89
    90 -}
       static void vHandlerTask( void *pvParameters )
    91
    92 - {
    93
            xSemaphoreTake( xBinarySemaphore, 0 );
    94
             for( ;; )
    95
    96
                 xSemaphoreTake( xBinarySemaphore, portMAX DELAY );
    97
                 vPrintString( "Handler task - Processing event.\n" );
    98
             }
    99 L}
        static void vPeriodicTask( void *pvParameters )
   100
   101 - {
   102
            for( ;; )
   103
   104
                 vTaskDelay( 500 / portTICK RATE MS );
   105
                 vPrintString( "Periodic task - About to generate an interrupt.\n" );
                 mainTRIGGER INTERRUPT();
   106
   107
                 vPrintString( "Periodic task - Interrupt generated.\n\n" );
   108
   109 -}
   110
        void vSoftwareInterruptHandler( void )
   111 = {
   112
        portBASE TYPE xHigherPriorityTaskWoken = pdFALSE;
             xSemaphoreGiveFromISR( xBinarySemaphore, &xHigherPriorityTaskWoken );
   113
   114
            mainCLEAR INTERRUPT();
             portEND SWITCHING ISR( xHigherPriorityTaskWoken );
   115
   116 -}
```

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## Question 3: (10 Marks)

Assume the following snippet of code/application that already had all necessary declarations, inclusions, and prototypes. In the given table, order the first 10 break points to be hit, when GO is pressed.

1st Break Point Hit	2 <sup>nd</sup> Break Point Hit	3 <sup>rd</sup> Break Point Hit	4 <sup>th</sup> Break Point Hit	5 <sup>th</sup> Break Point Hit

6 <sup>th</sup> Break Point Hit	7 <sup>th</sup> Break Point Hit	8 <sup>th</sup> Break Point Hit	9 <sup>th</sup> Break Point Hit	10 <sup>th</sup> Break Point Hit

```
54 - int main ( void ) {
        xMutex = xSemaphoreCreateMutex();
56
        xTaskCreate( prvPrintTaskl, "Printl", 240, "Task 1 ********** \n", 3, NULL );
        xTaskCreate( prvPrintTask2, "Print2", 240, "Task 2 -----\n", 1, NULL );
57
        xTaskCreate( vPeriodicTask, "Print3", 240, NULL, 2, NULL);
58
59
        vTaskStartScheduler();
60 L}
61
   static void prvPrintTaskl( void *pvParameters )
62 - {
63
   char *pcStringToPrint;
64
   pcStringToPrint = ( char * ) pvParameters;
65
      for(;;)
66 🖨
     -{
67
        xSemaphoreTake( xMutex, portMAX_DELAY );
   /* Print out the string using the newly defined function. */
69
        xSemaphoreGive(xMutex);
70
        vTaskDelay( 100);
71
72 -}
73
   static void prvPrintTask2( void *pvParameters )
74 □{ int i,j;
    char *pcStringToPrint;
   pcStringToPrint = ( char * ) pvParameters;
77
      for(;;)
78
        xSemaphoreTake( xMutex, portMAX DELAY );
    /* Print out the string using the newly defined function. */
80
81 🚊
        for (i=0;i<1000000;i++) {
82
        j++;}
83
        xSemaphoreGive( xMutex );
84
85 L}
86 static void vPeriodicTask( void *pvParameters )
87 - {
88
      for(;;)
89 🗀
     -{
90
        vTaskDelay(110);
91
92 -}
```

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# Question 4: (9 Marks)

Assume the following snippet of code/application that already had all necessary declarations, inclusions, and prototypes. In the given table, order the first 8 break points (PB) to be hit, when GO is pressed. At each PB, Define the states (READY, RUNNING or BLOCK) of all tasks. (Hint: "Receiver 2" will be the first to execute once Scheduler starts).

#### WHAT IS THE BEST YOU CAN CODE IN LINE 61, instead of "???", TO COMPILE?

Break Point at Line?	State of Sender	State of Receiver 1	State of Receiver 2

```
59 int main ( void )
    60 - {
×
    61
          xQueue = xQueueCreate( ???, sizeof( long ) );
    62
             xTaskCreate( vSenderTask, "Sender", 240, ( void * ) 100, 2, NULL );
             xTaskCreate( vReceiverTask, "Receiver1", 240, NULL, 3, NULL );
    63
             xTaskCreate( vReceiverTask, "Receiver2", 240, NULL, 3, NULL);
    64
4
    65
             vTaskStartScheduler();
    66
           for( ;; );
    67 -}
    68
        static void vSenderTask( void *pvParameters )
    69 - {
    70
        long lValueToSend;
    71
           portBASE TYPE xStatus;
    72
           1ValueToSend = ( long ) pvParameters;
    73
           for( ;; )
    74
             xStatus = xQueueSendToBack( xQueue, &1ValueToSend, 0 );
    75
    76
          }
    77 -}
    78
        static void vReceiverTask( void *pvParameters )
    79 - {
    80
        long lReceivedValue;
        portBASE TYPE xStatus;
    81
    82
         const portTickType xTicksToWait = 100 / portTick RATE MS;
           for( ;; )
    83
    84 -
             xStatus = xQueueReceive( xQueue, &lReceivedValue, xTicksToWait );
    85
    86
             vPrintStringAndNumber( "Received = ", lReceivedValue );
    87
    88 -}
```

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#### Question 5: (8 Marks)

For the following FreeRTOS based application snippet, **Sketch tasks timing diagram starting from 0ms and ending at 400ms.**Vertical axes should be graded down-up from low- to high-priority.

```
73 - int main( void ){
 74
         xPrintQueue = xQueueCreate( 5, sizeof( char * ) );
 75
         xTaskCreate( prvPrintTaskl, "Print1", 240, ( void * ) 0, 1, NULL );
         xTaskCreate( prvPrintTask2, "Print2", 240, ( void * ) 1, 2, NULL );
 76
         xTaskCreate( prvStdioGatekeeperTask, "Gatekeeper", 240, NULL, 3, NULL);
 77
 78
         vTaskStartScheduler();}
 79
     static void prvStdioGatekeeperTask( void *pvParameters )
 80 □ {
 81
     char *pcMessageToPrint;
 82 E
       for(;;){
 83
         xQueueReceive(xPrintQueue, &pcMessageToPrint, portMAX_DELAY);
 84
         printf( pcMessageToPrint );
 85
 86 -}
 87 - void vApplicationTickHook( void ) {
 88
     static int iCount = 0;
     portBASE TYPE xHigherPriorityTaskWoken = pdFALSE;
 89
 90
       iCount++;
 91
       if( iCount >= 200 ){
         xQueueSendToFrontFromISR(xPrintQueue, &(pcStringsToPrint[2]), &xHigherPriorityTaskWoken);
 92
 93
         iCount = 0;
 94
 95 L}
 96 static void prvPrintTaskl( void *pvParameters ) {
 97
     int iIndexToString;
 98
       iIndexToString = ( int ) pvParameters;
 99 😑
       for(;;){
100
         xQueueSendToBack( xPrintQueue, &( pcStringsToPrint[ iIndexToString ] ), 0 );
101
         vTaskDelay( 100 / portTICK RATE MS );
102
103 -}
104 static void prvPrintTask2( void *pvParameters ) {
105
     int iIndexToString;
106
       iIndexToString = ( int ) pvParameters;
107
       for(;;){
108
         xQueueSendToBack( xPrintQueue, &( pcStringsToPrint[ iIndexToString ] ), 0 );
109
         vTaskDelay( 300 / portTICK RATE MS );
110
111 L}
```