

بِسْمِ اللَّهِ الرَّحْمَنِ الرَّحِيمِ

CSE211s:

# Introduction to Embedded Systems

## Introduction

﴿يَرْفَعُ اللَّهُ الَّذِينَ آمَنُوا مِنْكُمْ وَالَّذِينَ أُوتُوا الْعِلْمَ دَرَجَاتٍ﴾

Ahmed Juba

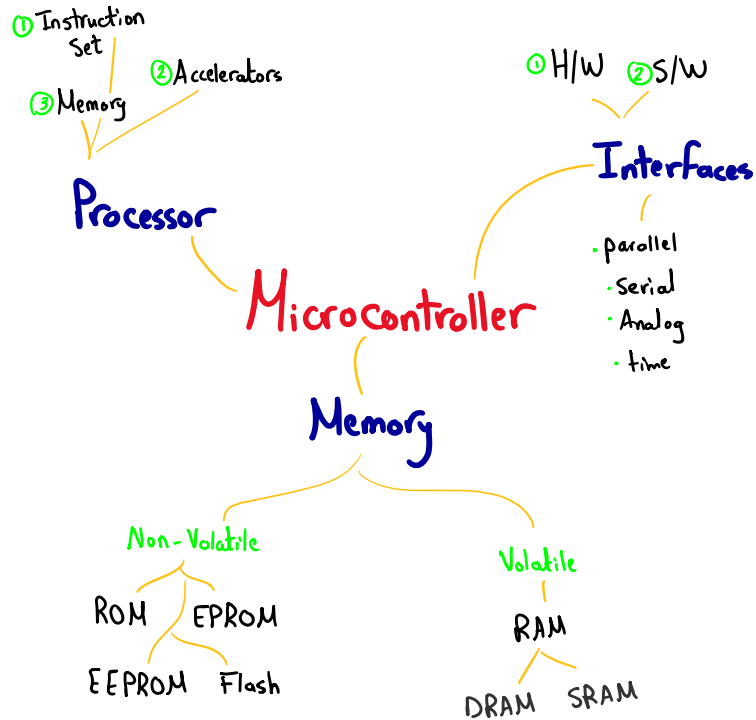




# \* Embedded System:

→ it's any device that includes a **programmable Computer** but it's not itself intended to be general-purpose Computer

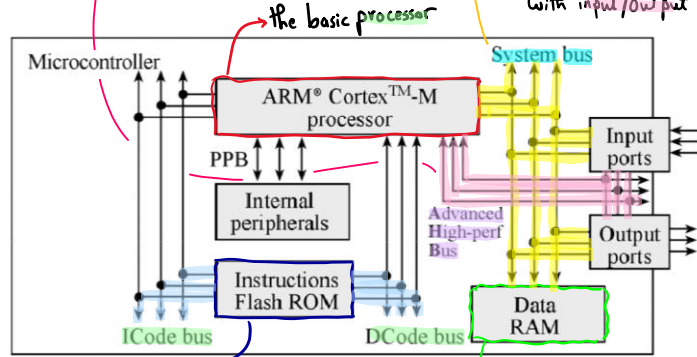
→ Thus, a PC is not itself an embedded computing system, although PCs are often used to build embedded computing systems, But a fax machine or a clock built from a microprocessor is an embedded computing system



## \* ARM Cortex M4-based System:

the advanced high-performance bus connects the processor with I/O ports

the system bus which connects the processor with Data RAM & with input/output ports



\* Contains the instructions to be executed

\* Connected to two buses  
ICode bus  
DCode bus

Contains the data that will temporarily stored

\* Flash ROM remembers data after power is turned off

\* This microcontroller-system has a **harvard architecture**  
→ meaning that we have **two separate buses** one for data & one for instructions

\* More connections on the bus means slower data rate

Condition Code Bits	Indicates
N negative	Result is negative
Z zero	Result is zero
V overflow	Signed overflow
C carry	Unsigned overflow

## \* ARM ISA:

### ① Registers:

→ it has 16 Registers each of 32-bit

13 of them are general-purpose registers (R0-R12)

3 of them are special-purpose registers

R13: Stack pointer (MSP)

R14: Link Register (LR)

R15: Program Counter (PC)

### ② Memory-Map:

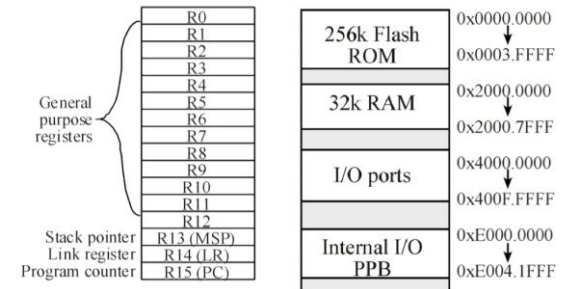
① Flash-ROM → it size is 256kB

Contains instructions

② The RAM → it size is 32kB

③ I/O ports → it's a memory-mapped ports

④ Internal I/O PPB → some memory locations are allocated for internal modules such as timers



\* Test yourself → **QUIZ**