AIN SHAMS UNIVERSITY FACULTY OF ENGINEERING

ICHEP; MCT-CESS

Spring 2022



June 9th,2022 Course Code: CSE411-345-347 Time: 2 Hours

Real-Time & Embedded Systems Design

The Exam Consists of **5 Questions in 5 Pages**

Total Marks: 40 Marks

تنبيه هام جدا: يجب على كل طالب الحل فقط هنا في ورق الاسئلة ولن يلتفت لأي إجابة في الكراسة المرفقة والتي تحتوي على البار كود الخاص بكل طالب

Important Rules:

- Having a (mobile -Smart Watch- earphones) inside the examination hall is forbidden and is considered as a cheating behavior.
- It is forbidden to have any references, notes, books, or any other materials even if it is not related to the exam content with you in the examination hall.
- This is an answer sheet.
- Assume missing data if any Read it all well, at first.

<u>علیمات هامه</u>

- حيازة (المحمول- الساعات الذكية سماعة الأذن) داخل لجنة الامتحان يعتبر حالة غش تستوجب العقاب.
- لايسمح بدخول أي كتب أو ملازم أو أوراق داخل اللجنة والمخالفة
 تعتبر حالة غش.
- هذه ورقة إجابة أيضا أقرأها أولا جيدا أفترض الناقص إن وجد

Question 1: (3 Marks)

For FreeRTOS based embedded hypothetical application, "heap2" Heap memory algorithm is used. Figure A shows heap memory state where T1, T2 and T3 are allocated tasks. F1, F2, and F3 are free spaces of 100 bytes, 400 bytes, and 900 bytes respectively. Show in the following figures, heap memory states for the cases:

- B: Create T4 of 600 bytes (Stack and TCB)
- C: Create T5 of 350 bytes (Stack and TCB)
- D: Create T6 of 36 bytes (Stack and TCB)

A	T1	F1	T2	F2	Т3	F3
В	T1		T2		Т3	
C	T1), i	T2		Т3	
D	T1		Т2		Т3	

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Question 2: (10 Marks)

Example 12

Assume the following snippet of code/application that already had all necessary declarations, inclusions, and prototypes. In the given table, order the first 10 break points to be hit, when GO is pressed.

1st Break Point Hit	2 nd Break Point Hit	3 rd Break Point Hit	4 th Break Point Hit	5 th Break Point Hit
104	96	104	115	107

6 th Break Point Hit	7 th Break Point Hit	8 th Break Point Hit	9 th Break Point Hit	10 th Break Point Hit
104	97	96	115	97

```
82 int main ( void )
    83 - {
    84
            vSemaphoreCreateBinary( xBinarySemaphore );
    85
            prvSetupSoftwareInterrupt();
86
            xTaskCreate( vPeriodicTask, "Periodic1", 240, NULL, 1, NULL );
    87
             xTaskCreate( vPeriodicTask, "Periodic2", 240, NULL, 3, NULL );
             xTaskCreate( vHandlerTask, "Handler", 240, NULL, 2, NULL );
1
    88
            vTaskStartScheduler();
    89
    90 -}
    91
        static void vHandlerTask( void *pvParameters )
    92 □ {
    93
            xSemaphoreTake( xBinarySemaphore, 0 );
    94
            for( ;; )
    95
                 xSemaphoreTake( xBinarySemaphore, portMAX DELAY );
    96
    97
                 vPrintString( "Handler task - Processing event.\n" );
    98
            3
    99
        static void vPeriodicTask( void *pvParameters )
   100
   101 - {
   102
            for( ;; )
   103
   104
                 vTaskDelay( 500 / portTICK RATE MS );
                 vPrintString( "Periodic task - About to generate an interrupt.\n" );
   105
   106
                 mainTRIGGER INTERRUPT();
                 vPrintString( "Periodic task - Interrupt generated.\n\n" );
   107
   108
   109 -}
   110
        void vSoftwareInterruptHandler( void )
   111 - {
        portBASE TYPE xHigherPriorityTaskWoken = pdFALSE;
   112
             xSemaphoreGiveFromISR( xBinarySemaphore, &xHigherPriorityTaskWoken );
   113
            mainCLEAR INTERRUPT();
   114
   115
            portEND SWITCHING ISR( xHigherPriorityTaskWoken );
   116 -}
```

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Question 3: (10 Marks) Example 14 & 15

Assume the following snippet of code/application that already had all necessary declarations, inclusions, and prototypes. In the given table, order the first 10 break points to be hit, when GO is pressed.

1 st Break Point Hit	2 nd Break Point Hit	3 rd Break Point Hit	4 th Break Point Hit	5 th Break Point Hit
67	70	88	90	79

6 th Break Point Hit	7 th Break Point Hit	8 th Break Point Hit	9 th Break Point Hit	10 th Break Point Hit
67	83	70	90	79

```
54 mint main ( void ) {
        xMutex = xSemaphoreCreateMutex();
55
        xTaskCreate( prvPrintTaskl, "Printl", 240, "Task 1 ********** \n", 3, NULL );
56
        xTaskCreate( prvPrintTask2, "Print2", 240, "Task 2 -----\n", 1, NULL );
57
        xTaskCreate( vPeriodicTask, "Print3", 240, NULL, 2, NULL );
58
59
        vTaskStartScheduler();
60 -}
    static void prvPrintTaskl ( void *pvParameters )
61
62 □ {
63
    char *pcStringToPrint;
64
    pcStringToPrint = ( char * ) pvParameters;
65
      for( ;; )
66
67
        xSemaphoreTake( xMutex, portMAX DELAY );
    /* Print out the string using the newly defined function. */
69
        xSemaphoreGive ( xMutex );
70
        vTaskDelay( 100 );
71
72 -}
73
   static void prvPrintTask2 ( void *pvParameters )
74 ∃{ int i,j;
    char *pcStringToPrint;
   pcStringToPrint = ( char * ) pvParameters;
77
      for( ;; )
78
79
        xSemaphoreTake( xMutex, portMAX DELAY );
80
    /* Print out the string using the newly defined function. */
81
        for (i=0;i<10000000;i++) {
82
        j++;}
83
        xSemaphoreGive(xMutex);
84
85 -}
   static void vPeriodicTask( void *pvParameters )
86
87 □ {
88
      for( ;; )
89
      {
90
        vTaskDelay(110);
91
92 -}
```

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Question 4: (9 Marks)

Example 10

Assume the following snippet of code/application that already had all necessary declarations, inclusions, and prototypes. In the given table, order the first 8 break points (PB) to be hit, when GO is pressed. At each PB, Define the states (READY, RUNNING or BLOCK) of all tasks. (Hint: "Receiver 2" will be the first to execute once Scheduler starts).

WHAT IS THE BEST YOU CAN CODE IN LINE 61, instead of "???", TO COMPILE? length of queue will be 1

Break Point at Line?	State of Sender	State of Receiver 1	State of Receiver 2
85	Ready	Ready	Running
85	Ready	Running	Blocked
75	Running	Blocked	Blocked
86	Ready	Blocked	Running
85	Ready	Blocked	Running
75	Running	Blocked	Blocked
86	Ready	Running	Blocked
85	Ready	Running	Blocked

```
59 int main ( void )
                                 length of queue
    60 ⊟ {
    61
           xQueue = xQueueCreate( ??? sizeof( long ) );
×
    62
             xTaskCreate( vSenderTask, "Sender", 240, ( void * ) 100, 2, NULL );
             xTaskCreate ( vReceiverTask, "Receiver1", 240, NULL, 3, NULL );
    63
als.
             xTaskCreate( vReceiverTask, "Receiver2", 240, NULL, 3, NULL );
    64
30
             vTaskStartScheduler();
    65
    66
           for( ;; );
    67 -}
    68 static void vSenderTask( void *pvParameters )
    69 - {
    70
        long lValueToSend;
    71
           portBASE TYPE xStatus;
    72
           1ValueToSend = ( long ) pvParameters;
          for( ;; )
    73
    74
    75
             xStatus = xQueueSendToBack( xQueue, &lValueToSend, 0 );
    76 -
    77 -}
    78 static void vReceiverTask( void *pvParameters )
    79 - (
        long lReceivedValue;
    80
         portBASE TYPE xStatus;
    81
    82
         const portTickType xTicksToWait = 100 / portTiCK RATE MS;
           for( ;; )
    83
    84 🖹
    85
             xStatus = xQueueReceive( xQueue, &lReceivedValue, xTicksToWait );
    86
             vPrintStringAndNumber( "Received = ", lReceivedValue );
    87
    88 -}
```

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Question 5: (8 Marks)

For the following FreeRTOS based application snippet, **Sketch tasks timing diagram starting from 0ms and ending at 400ms.** Vertical axes should be graded down-up from low- to high-priority.

```
73 = int main( void ) {
 74
          xPrintQueue = xQueueCreate(5, sizeof(char *));
 75
          xTaskCreate( prvPrintTaskl, "Print1", 240, ( void * ) 0, 1, NULL );
          xTaskCreate( prvPrintTask2, "Print2", 240, ( void * ) 1, 2, NULL );
 76
          xTaskCreate( prvStdioGatekeeperTask, "Gatekeeper", 240, NULL, 3, NULL);
 77
 78
          vTaskStartScheduler();}
 79
     static void prvStdioGatekeeperTask( void *pvParameters )
 80 □ {
 81
      char *pcMessageToPrint;
 82
       for( ;; ){
 83
          xQueueReceive( xPrintQueue, &pcMessageToPrint, portMAX DELAY );
 84
          printf( pcMessageToPrint );
 85
    L}
 86
 87 - void vApplicationTickHook( void ) {
 88
      static int iCount = 0;
 89
     portBASE TYPE xHigherPriorityTaskWoken = pdFALSE;
 90
        iCount++;
 91
       if ( iCount >= 200 ) {
 92
          xQueueSendToFrontFromISR( xPrintQueue, &( pcStringsToPrint[ 2 ] ), &xHigherPriorityTaskWoken );
 93
          iCount = 0;
 94
 95 L}
 96 static void prvPrintTaskl ( void *pvParameters ) {
 97
      int iIndexToString;
 98
        iIndexToString = ( int ) pvParameters;
 99
        for( ;; ){
100
          xQueueSendToBack( xPrintQueue, &( pcStringsToPrint[ iIndexToString ] ), 0 );
101
          vTaskDelay( 100 / portTICK RATE MS );
102
103 -}
104 = static void prvPrintTask2 ( void *pvParameters ) {
105
      int iIndexToString;
106
        iIndexToString = ( int ) pvParameters;
107
        for( ;; ){
108
          xQueueSendToBack( xPrintQueue, &( pcStringsToPrint[ iIndexToString ] ), 0 );
109
          vTaskDelay( 300 / portTICK RATE MS );
110
111 -}
                     print Task
                                                              3
                                                   0
                             OTale Task
                                 Duris it -> App Tick Hook will happen loo times
                                    Count at the end = 100
                                 - Duris it -> APP Tick Hook will happen loo Times
                                    Count = 200 then =0
                                 Duris it - App Tick Hook will happen loo Time
                                    Court at the end = 100
                            @ Ible Task
                                 Dyrig it -> APR
```