```
(*((volatile unsigned long *)0x400253FC))
#define GPIO PORTF DATA R
#define GPIO PORTF DIR R
                              (*((volatile unsigned long *)0x40025400))
                              (*((volatile unsigned long *)0x40025420))
#define GPIO PORTF AFSEL R
                              (*((volatile unsigned long *)0x40025510))
#define GPIO PORTF PUR R
#define GPIO PORTF DEN R
                              (*((volatile unsigned long *)0x4002551C))
                              (*((volatile unsigned long *)0x40025520))
#define GPIO PORTF LOCK R
                              (*((volatile unsigned long *)0x40025524))
#define GPIO PORTF CR R
#define GPIO PORTF AMSEL R
                              (*((volatile unsigned long *)0x40025528))
                              (*((volatile unsigned long *)0x4002552C))
#define GPIO PORTF PCTL R
                              (*((volatile unsigned long *)0x40025040))
#define PF4
#define PF3
                              (*((volatile unsigned long *)0x40025020))
                              (*((volatile unsigned long *)0x40025010))
#define PF2
                              (*((volatile unsigned long *)0x40025008))
#define PF1
#define PF0
                              (*((volatile unsigned long *)0x40025004))
#define GPIO LOCK KEY
                              0x4C4F434B // Unlocks the GPIO CR register
#define SYSCTL RCGCGPIO R
                              (*((volatile unsigned long *)0x400FE608))
#define SYSCTL PRGPIO R
                              (*((volatile unsigned long *)0x400FEA08))
                0x02
#define RED
#define BLUE 0x04
#define GREEN 0x08
                             0x0E
#define PF123 mask
                             0x11
#define PF04 mask
#define PF mask
                             0x20
```

```
RGB_LED_Init
              LDR R1, =SYSCTL_RCGCGPIO_R
                                                                               ;activating the clock of PORT F
                               LDR R0, [R1]
                               ORR R0, R0, #0x20
                               STR R0, [R1]
                               NOP
                                                                                               ;waiting for activation to complete
                               NOP
                               LDR R1, =GPIO_PORTF_LOCK_R
                                                                                               ;unlocking PORT F
                               LDR R0, [R1]
                               LDR R0, =0x4C4F434B
                               STR R0, [R1]
                                                                                               ;unlocking PORT F pins no. 1 2 3
                               LDR R1, =GPIO_PORTF_CR_R
                               LDR R0, [R1]
                               ORR
                                       R0, R0, #0x0E
                               STR R0, [R1]
                               LDR R1, =GPIO_PORTF_DIR_R
                                                                                               ;setting pins 1,2,3 of port F to be output
                               LDR R0, [R1]
                               ORR
                                       R0, R0, #0x0E
                               STR R0, [R1]
                               LDR R1, =GPIO_PORTF_DEN_R
                                                                                               ;setting pins 1,2,3 of port F to be digital
                               LDR R0, [R1]
                               ORR
                                       R0, R0, #0x0E
                               STR R0, [R1]
```

```
LDR R1, =GPIO_PORTF_AMSEL_R
                                                                ;disable pins 1,2,3 of port F to be analog
LDR R0, [R1]
       R0, R0, #0x0E
BIC
STR R0, [R1]
LDR R1, =GPIO PORTF AFSEL R
                                                               ;disable pins 1,2,3 of port F to be alternative
LDR R0, [R1]
BIC
       R0, R0, #0x0E
STR R0, [R1]
LDR R1, =GPIO_PORTF_PCTL_R
                                                               ;disable pins 1,2,3 of port F to be analog
LDR R0, [R1]
LDR R2, =0xFFF0
       R0, R0, R2
BIC
STR R0, [R1]
LDR R1, =GPIO PORTF DATA R
                                                               ;initializing the pins with zero values
LDR R0, [R1]
BIC R0, R0, #0x0E
```

BX LR

```
//Q1. Write a C function that initializes port F pins 1, 2, and 3 as Digital Output with initial zero values
#include "IO.h"
void RGBLED Init(void)
                SYSCTL_RCGCGPIO_R
                                        = 0x20;
                                                                                 //PORT F Clock enable
                while ( (SYSCTL PRGPIO R&0x20) == 0) {}
                GPIO PORTF LOCK R |= 0x4C4F434B;
                                                                                 //Unlock PORT F
                GPIO PORTF CR R |= 0 \times 0 E;
                                                                                 //Allow changes to pins 1,2,3
                GPIO PORTF DIR R |= 0x0E;
                                                                                 //SET pins 1,2,3 to be output
                GPIO_PORTF_DEN_R |= 0x0E;
                                                                                 //SET pins 1,2,3 to be digital
                GPIO_PORTF_AMSEL_R &= ~0x0E;
                                                                                 //Disable Analog function
                GPIO PORTF AFSEL R &= ~0x0E;
                                                                                 //No alternative function
                GPIO PORTF PCTL R &= ~0xFFF0;
                                                                                 //GPIO clear bit PCTL
                GPIO PORTF DATA R &= ~0x0E;
                                                                                 //Initialize LEDs to be off
```

```
SW1_Init
                               LDR R1, =SYSCTL_RCGCGPIO_R
                               LDR R0, [R1]
                               ORR R0, R0, #0x20
                               STR R0, [R1]
                               NOP
                                NOP
                               LDR R1, =GPIO_PORTF_LOCK_R
                               LDR R0, =0x4C4F434B
                               STR R0, [R1]
                               LDR R1, =GPIO_PORTF_CR_R
                               LDR R0, [R1]
                               ORR R0, R0, #0x10
                               STR R0, [R1]
                               LDR R1, =GPIO_PORTF_DIR_R
                                       R0, [R1]
                               LDR
                               BIC R0, R0, #0x10
                               STR R0, [R1]
                               LDR R1, =GPIO_PORTF_DEN_R
                               LDR R0, [R1]
                               ORR R0, R0, #0x10
                               STR R0, [R1]
                               LDR R1, =GPIO_PORTF_AMSEL_R
                               LDR R0, [R1]
                               BIC R0, R0, #0x10
                               STR R0, [R1]
```

```
LDR R1, =GPIO PORTF AFSEL R
LDR RØ, [R1]
BIC RO, RO, #0x10
STR RØ, [R1]
LDR R1, =GPIO_PORTF_PCTL_R
LDR R1, [R0]
LDR R2, =0xF0000
```

LDR R1, =GPIO_PORTF_PUR_R

BIC RØ, RØ, R2 STR R0, [R1]

LDR R0, [R1]

STR R0, [R1]

ORR R0, R0, #0x10

BX LR

```
//Q2. Write a C function that initializes port F pin 4 as Digital Input that will be connected to a switch.
void SW1_Init (void)
               SYSCTL_RCGCGPIO_R
                                       = 0x20;
               while ( (SYSCTL_PRGPIO_R&0x20) == 0) {}
               GPIO_PORTF_LOCK_R |= 0x4C4F434B;
               GPIO_PORTF_CR_R |= 0x10;
               GPIO_PORTF_DIR_R &= ~0x10;
               GPIO_PORTF_DEN_R |= 0x10;
               GPIO_PORTF_AMSEL_R &= ~0x10;
               GPIO_PORTF_AFSEL_R &= ~0x10;
               GPIO_PORTF_PCTL_R &= ~0xF000;
               GPIO_PORTF_PUR_R |= 0x10;
```

```
;Q3. Write an assembly function that reads PORTF pin4.
```

SW1_Input

```
LDR R0, [R1]
AND R0, R0, #0x10
STR R0, [R1]
```

BX LR

LDR R1, =GPIO PORTF DATA R

;imports all contents of port f
;reads content of pin 4 only

```
//Q3. Write a C function that reads PORTF pin4.

unsigned char SW1_Input (void)
{
    return GPIO_PORTF_DATA_R & 0x10;
```

```
;Q4. Write an assembly function that clears pin1, pin2, and pin3 then update the mentioned pins with new values of data in PORTF.
                               LDR R3, =data
RGB output
                               LDR RØ, [R3]
                               LDR R1, =GPIO PORTF DATA R
                               LDR R2, [R1]
                               BIC R2, R2, #0x0E
                                                                                                ;clearing pins 1,2,3
                               ORR R2, R2, R0
                                                                                                ;updating the new values of data
                               STR R2, [R1]
                               BX LR
```

```
//Q4. Write a C function that clears pin1, pin2, and pin3 then update the mentioned pins with new values of data in PORTF.

void RGB_Output (unsigned char data)
{
```

GPIO_PORTF_DATA_R &= ~0x0E; GPIO PORTF DATA R |= data; ;Q5. In Tiva C, PF4 is connected to a push button and PF1, PF2, and PF3 are connected to an RGB LED. ;PF1 is red, PF2 is blue, and PF3 is green. Write assembly application that uses the init functions developed in ;previous questions that reads input from the switch and when it is pressed for the first time the red LED ;should be turned on then when pressed a second time turn off the red LED and turn on the blue LED then ;when pressed a third time turn off the blue LED and turn on the green LED then when pressed a fourth time ;turn off the green LED and turn on again the red LED and then repeat the cycle.

BL RGB_LED_Init BL SW1 Init

__main MOV R3, #0x02 SuperLoop CMP R3, #0x10

BNE read_SW1

MOV R3, #0x02

read SW1 BL SW1 Input

CMP R0, #0x10

BEQ end_if

MOV RØ, R3

BL RGB_Output

LSL R3, R3, #1

end_if B SuperLoop

```
//Q5. In Tiva C, PF4 is connected to a push button and PF1, PF2, and PF3 are connected to an RGB LED.
//PF1 is red, PF2 is blue, and PF3 is green. Write C application that uses the init functions developed in
//previous questions that reads input from the switch and when it is pressed for the first time the red LED
//should be turned on then when pressed a second time turn off the red LED and turn on the blue LED then
//when pressed a third time turn off the blue LED and turn on the green LED then when pressed a fourth time
//turn off the green LED and turn on again the red LED and then repeat the cycle.
unsigned char LED OUT = 0x02;
                                         //initalize LED TO RED 0000 0010
unsigned char Button In;
void main(void)
        RGB_LED_INIT();
        SW1_init;
                while(1){
                 if ( LED OUT == 0x10 ){
                                                    // 0x10 --> 0001 0000 Which means that All LEDs are OFF and SW1 OFF
                 LED OUT = 0x02;
                                               // Reset LED OUT TO RED AFTER GREEN
                 Button In = SW1 Input();
                                                 // CHECK ON STATUS OF SW1 IF PRESSED (0) OR NOT (1)
                if (Button In != 0x10)
                         // SW1 is pressed
                RGB Output(LED OUT);
```

//Shift Left to next color in pin

LED OUT = LED OUT << 1;

Q7. What is a direction register? Why does the microcontroller have direction registers?

It determines whether the pins operate as inputs or Outputs "GPIO_PORTF_DIR_R"

& Using the Concept of direction registers to make the micro controller more
marketable.

- Q8. What is the alternative function register?
- Individual port pins can be general purpose I/O "GPIO" or have an alternative function. We will set bits in the alternative function register "GPIO_PORTF_AFSEL_R" when we wish to activate the alternative function listed in tables