

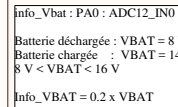
B



D

OPTION
PB6: TIM4_CH1 : USART1_TX
PB7: TIM4_CH2 : USART1_RX
PC13 : USER BUTTON

PB4: TIM3_CH1/PB4
PB5: TIM3_CH2
PA2: USART2_TX/ADC12_IN2/TIM2_CH3
PA3: USART2_RX/ADC12_IN3/TIM2_CH4
PB2 :



```
info_Vbat : PA0 : ADC12_IN0
Batterie déchargée : VBAT = 8
Batterie chargée : VBAT = 14
8 V < VBAT < 16 V
Info_VBAT = 0.2 x VBAT
```

SPI

5 SPI2_SCK
4 SPI2_MOSI
3 SPI2_MISO
2 SPI2_NSS
1 GND

SPI2_SCK : PB13 : SPI2_SCK
SPI2_MOSI : PB14 : SPI2_MISO
SPI2_MISO : PB15 : SPI2_MOSI
SPI2_NSS : PB12 : SPI2_NSS

LIASON TORSADE CAN

1
2
3
4
5
6
7
8
9
10

SubD M

8
7
6
5

RS
CANH
CANL
VREF
SN65HVD230
U2

1 D
2 GND
3 VCC
4 CANRX

3.3V
C7
470nF

RCAN
120

CANRX : PA11
CANTX : PA12

STM32nucleo -F103RB

