h550 project - Security assessment of an AP

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Contents

1 Abstract

Closed source firmware cannot be trusted. Therefore, we will show how to hack an access point and search for exploitable vulnerabilities from the hardware to the software. Doing, so we will take a look at how U-boot and BusyBox shells can be used and leveraged to our advantage. Afterwards, we will showcase a level 10[1] known vulnerability (which is in fact a backdoor placed by the constructor) that allows remote code execution and how it was found.

2 Introduction

Nowadays most IOT devices contain some flavor of a vulnerability, backdoor or critical bug. However, those problems in some cases are never fixed as IoT devices are dependent on their vendors for firmware update due their code being proprietary (in most cases). Furthermore, some users might forget (or not know) that their device's software is not updated and by extension, vulnerable. This paper takes the approach of a guide on how to perform basic hardware hacking on everyday embedded devices. What should be taken away from this work is the process which aims to be as generic as possible and not the results as the chosen target is now discontinued and irrelevant. Therefore, we will start by setting the scope of our analysis then, do reconnaissance under those constraints followed by the exploitation of our findings.

3 Scope

The device is an access point from Cisco released in October 2008, aimed at small businesses. This products has been discontinued in 2019 and its sales ended back in 2014.

(a) Product picture (b) Bottom (c) Inside

Figure 1: Pictures of of the WAP4410N

The device can be powered up using a barrel adapter or through PoE which hints at its purpose being ease of batch deployment. On the network side,

[1] https://nvd.nist.gov/vuln/detail/CVE-2014-0659

it support 802.11n, 802.11g and 802.11b. With the time period in mind, for 802.11b and g, the only supported encryption protocol was WEP (64/128 bits in this case). Also, firmware updates were handled through the web porta : there is no I/O on the device. On figure 1c, we can see a cable going outside of the device, this is the UART access which was enabled by default.

3.1 Lab setup

The testing setup is made of the WAP4410N, a router and a computer : for simplicity sake, the router is out of scope from this analysis and all its security features are disabled. The computer has a wireless connection to the AP.

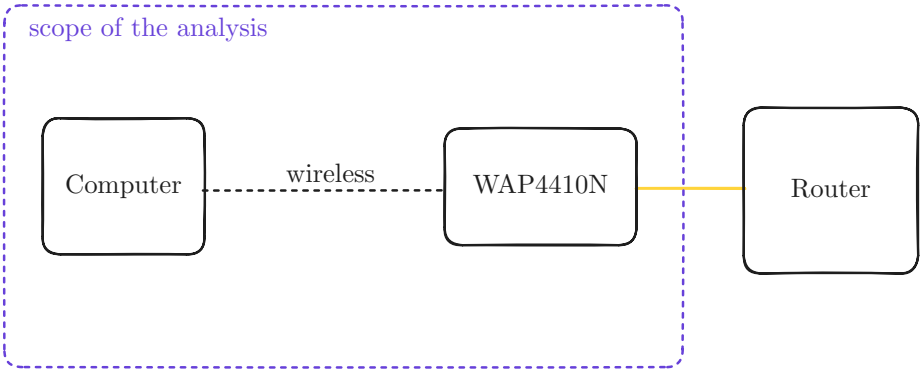

Figure 2: Lab setup

3.1.1 Equipment

The following table contains all the software and hardware required for basic hardware hacking. The only barriers of entry are the TTL and multimeter (for safety) which can both be obtained for cheap or borrowed.

software	hardware
Wireshark, nmap GNU Screen, xxd, dd, binwalk squashfs-utils radare2, ghidra	FT232 board USB-A UART adapter Multimeter Dupont Cables

Figure 3: List of used software/hardware

At first, we intended to use a SPI programmer (CH341a) to dump the firmware more easily using flashrom. However, as we will see in a later section, it was not feasible in this context without specialized hardware tools. On the software side, there are no real limitations as most of the state of the art tools are free and open source (with the exception of IDA).

4 Reconnaissance

In this section, we will discuss the reconnaissance of our device. This part was inspired by the first step in the cyber kill chain (which will be vaguely followed throughout the following sections). We will proceed in a bottom up approach in regards to the level of abstraction : from hardware to software

4.1 Physical

Teardown The device is held together by four 8-points screws under rubber feets which indicates that it wasn't meant to be opened (nor repaired . . .). Once inside, most of the device is empty and everything is available without further need to dismantle it.

IC reconnaissance At this point we can start looking for interesting ICs like flash memory or processors :

- reference : MX29LV640DBTC-90G
- format : 48 TSOP
- manufacturer : Macronix
- size : 8MB

- family : Atheros 9100
- reference : Atheros9132
- manufacturer : Qualcomm
- architecture : MIPS 24K V7.4 32bits
- endianness : big endian
- bogomips : 265.21

The MIPS architecture is very common on embedded devices and very easy to reverse engineer its assembly. Regarding the NAND flash, it would as hinted before, it would require to de-solder the IC using a special SPI programmer (or an adapter for the ch341a).

Finding the UART There are 4 header pin on the board, we will try them using a multimeter : the UART has a very specific behavior.
Before taking measures, we must find the ground, to do so, can check continuity between a metal part (which should be grounded) and the ground pin from a known IC (in this case the flash). After making sure that a metal part is grounded we can start taking measurements,
The voltage goes from 0V to the VCC, at startup because of the boot log outputs a lot of data. The Rx has a voltage of 0 as its used to receive data and doesn't

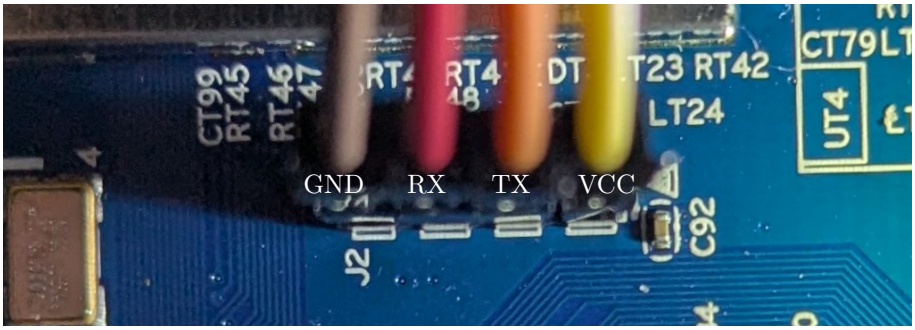

Figure 4: UART connected through Dupont cables with labeled ports

pin	R_{VSS}	V	info
1	$8.6k\Omega$	$\approx 3.3V$	VCC
2	$\infty k\Omega$	$\approx 0V - VCC$	TX
3	$\infty k\Omega$	$\approx 0V$	RX
4	0 k Ω	$0V$	GND

Figure 5: Multimeter measurements

send any. The next step is to try connecting the UART to the computer using a FT232,

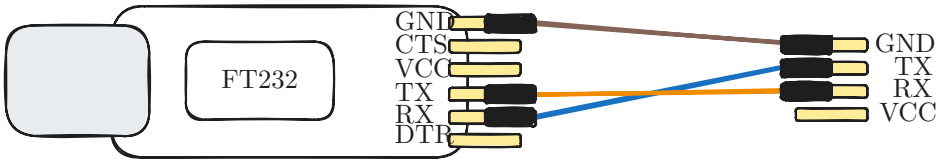

Figure 6: UART to USB-A

Regarding the baudrate, we can try 115200 as its the most common,

```
screen /dev/ttyUSB0 115200
```

This is indeed the correct baudrate however, if that wasn't the case we would have had to find it by trial and error or by using a logic analyzer.

4.2 Bootloader

4.2.1 Boot log

First, we will take a look at the boot log that is displayed before getting access to any console as analyzing it provides a lot of information about the hardware, software, ... One such information is the bootloader : U-Boot 1.1.4 which is a

very commnon and open source bootloader. We can even tell that the bootloader was compiled the 19 of August 2009 at 14:53:37 which makes it very old

```
arg 1: console=ttyS0,115200
arg 2: root=31:02
arg 3: rootfstype=jffs2
arg 4: init=/sbin/init
arg 5: mtdparts=ar9100-nor0:256k(u-boot),
        64k(u-boot-env),
        6464k(rootfs),
        1280k(uImage),
        64k(nvram),
        64k(calibration)
arg 6: mem=32M
```

From the capture snippet above we see that the rootfs uses jffs2 however, this is not the case.

```
Kernel command line: console=ttyS0,
        115200 root=31:02 rootfstype=squashfs init=/sbin/
            init mtdparts=ar9100-nor0:256k(u-boot),
        64k(u-boot-env),
        6464k(rootfs),
        1280k(uImage),
        64k(nvram),
        64k(calibration)
        mem=32M
```

This snippet comes after the previous ones and tells us that rootfs uses squashfs. This information can be confirmed later using the console and/or by analyzing the dumped firmware. There is no further information on the use of jffs2 on the device : in normal use cases, a device would have two filesystems one for persitency of configuration (read write) and the other containing the software (read only). How the device handles persistency remains a mistery in our case.

4.2.2 U-boot

At some point we are prompted to interrupt the auto boot : by doing so, we get the U-boot console which allows us to get more information on the device,

```
ar7100> bdinfo
...
flashstart   = 0xBF000000
flashsize    = 0x00800000
flashoffset  = 0x0002F690
...
baudrate     = 115200 bps
```

We now know the memory addresses of the start and of the flash storage : this will come handy during the firmware extraction in 5.1.1. There are other useful commands available to us such as tftpboot which allows us to boot over the network by serving an image on our computer. Assuming that the console is protected, this could allow us to get an unrestricted console by providing our own patched image (or using an open source one). Other than that, there are custom commands for testing purposes.

4.3 Console

If we leave the autoboot alone, we get into a root Linux ash shell. There are device specific CLI commands and a reduced busybox. There is a fwversion command which tell us that the firmware version is 2.0.4.2. As we are already root, we can't really do any privilege escalation. Furthermore, as the device only has a squashfs3 file system we can't get any persistency.

4.4 Network

We can run a port scan on the device using nmap,

```
Nmap scan report for wap86eb04 (192.168.1.3)
Host is up (0.012s latency).
Not shown: 65532 closed tcp ports (reset)
PORT      STATE     SERVICE
80/tcp    open      http
443/tcp   open      https
32764/tcp open    unknown
MAC Address: CC:EF:48:86:EB:04 (Cisco Systems)
```

The http(s) ports are used for the web administrative portals : when using the http web portal the credential are sent through base64. However, this isn't really a problem as the http port will most likely be disabled by any competent IT staff in real use cases

5 Exploitation

In this section we will see how to exploit the device

5.1 Dumping the firmware

5.1.1 Through the bootloader

This is the most "classical" technique used to dump firmware because of its simplicity and lack of technical and material requirements. To perform it, we will leverage the memory display command using the addresse from 4.2.2 and capturing the output. The size is written in decimal and divided by 4 as it reads by blocks of dwords. When the read is done, the output has to be cleaned into

a binary format (there is a script in the repo). However, this technique didn't work in our case. The reason, is unclear as the output is not entirely unreadable when using binwalk : some possible reasons might be,

- glitchy cable
- this version of U-Boot has a problem with memory display
- out of bound area on the NAND flash
- endianness problem

5.1.2 Through the console

To do so, we have to update the version of busybox : we will download precompiled busybox binary and serve it to our device through an ftp server. We download it in the /tmp directory as it the only location in a read only filesystem where we can write a file. From there, we use netcat to upload the mtd partitions : concatenate the partitions together to make the whole firmware image :

```
                /home/aaaaaa/aaaaaaaaa/aaa/aaaaaaaaaaaa/dump/firmware2.0.4.2.bin
--------------------------------------------------------------------------------------------------
DECIMAL                         HEXADECIMAL                     DESCRIPTION
--------------------------------------------------------------------------------------------------
158992                          0x26D10                         CRC32 polynomial table, big
                                                                endian
327680                          0x50000                         SquashFS file system, big
                                                                endian, version: 3.0,
                                                                compression: unknown, inode
                                                                count: 794, block size: 65536,
                                                                image size: 4761817 bytes,
                                                                created: 2011-05-13 10:54:02
6946816                         0x6A0000                        uImage firmware image, header
                                                                size: 64 bytes, data size:
                                                                875547 bytes, compression:
                                                                gzip, CPU: MIPS32, OS: Linux,
                                                                image type: OS Kernel Image,
                                                                load address: 0x80002000,
                                                                entry point: 0x801C2000,
                                                                creation time: 2011-05-13
                                                                10:51:49, image name: "Linux
                                                                Kernel Image"
8269351                         0x7E2E27                        PEM private key
8270238                         0x7E319E                        PEM certificate
--------------------------------------------------------------------------------------------------
```

with this we can do some reverse engineering however, as the rootfs uses squashfs3 we can't patch binaries on the fly : we need to upload the entire firmware therefore we didn't path any binary.

5.2 CVE-2014-0659

This CVE is a backdoor planted by SerComm. To try it, we ping the 32764 using netcat : this opens a prompt in which after entering some random data and press enter will answer with ScMM, when analyzing the traffic we can notice the data that we sent and as the answer we get, 53 63 4d 4d ff ff ff ff 00 00 00 00 which translates to ScMM. Based on the work of Eloi Vanderbken : you can use this exploit to get remote code execution on the device. Going into some reverse engineering of the vulnerable binary.

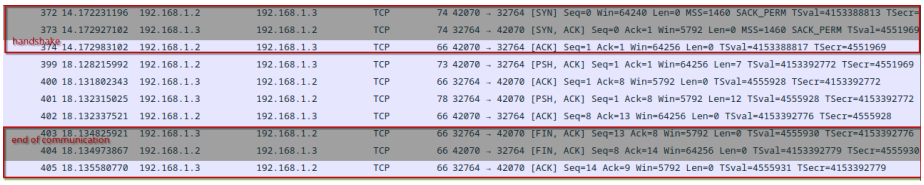

Figure 7: Capture of the exploit traffic

5.3 Reverse Engineering

The vulnerability is located int he **scfgmgr** file : we know this because it mas mentioned by the discoverer of the vulnerability. However, to find it naturally, one can look at the process list and analyze all the programs that sound like they don't belong there. The backdoor opens a new child process for each new connection to its socket and starts an alarm of 10s during which you have to send the correct character sequence :

1. Checks if the response starts with the correct code *(ScMM)* and is followed by and identifier (we will call it **id**) and optionally an argument
2. The program matches the **id** and, if it matches with a known **id** it performs the corresponding action *eg. there is an id for restarting the device*
3. The child process terminates

One of those *id*, opens a pipe that calls for **popen** with the user input as argument and returns the output through the socket. In the snippet above,

```
read_ret = (char *)malloc(1);
*read_ret = '\0';
*output = read_ret;
__stream = popen(command,"r");
```

Figure 8: Snippet of vulnerable code

command is a user input and, the resulting stream from the **popen** command is read into the **read_ret** which is pointed by **output** : a function argument. After, returning from the pipe, **output** is written to the socket.