

|   |  |   |   |   |   |   |
|---|--|---|---|---|---|---|
|   | 1  | 2 | 3 | 4 | 5 | 6 |
| A | <div>Sheet: UpperPCB</div> <div>Upper Board<br/>CPU,<br/>Display Connect<br/>EEPROM<br/>ETH<br/>CAN<br/>RS485<br/>Switches<br/>Battery</div> <div>File: OP.sch</div>   |   |   |   |   | A |
| B |  |   |   |   |   | B |
| C | <div>Sheet: LowerPCB</div> <div>not used till now</div> <div>File: UP.sch</div>  |   |   |   |   | C |
| D | <div>Modifications: M8<br/>Experimental Study<br/>CPU Board, CAN, ETH, RS485, EEPROM, ModeSW, Reset; Bat, Display, Switches<br/>CC BY-NC-SA<br/><b>C.Niesen, Koenigswinter, Germany</b></div> <div>Sheet: /<br/>File: HomeTeensy41.sch</div> <div><b>Title: myT41-Controller</b></div> <div>Size: A4Date: 2023-08-18Rev: V2.0</div> <div>KiCad E.D.A. kicad (5.1.7)-1Id: 1/3</div> |   |   |   |   | D |
|   | 1  | 2 | 3 | 4 | 5 | 6 |



|   |   |   |   |   |   |   |   |   |
|---|---|---|---|---|---|---|---|---|
|   | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |
| A |   |   |   |   |   |   |   |   |
| B |   |   |   |   |   |   |   |   |
| C |   |   |   |   |   |   |   |   |
| D |   |   |   |   |   |   |   |   |
| E |   |   |   |   |   |   |   |   |
| F |   |   |   |   |   |   |   |   |

Unterplatine UP

|  |                  |           |
|--|------------------|-----------|
| Modifications: M8<br>Experimental Study<br>CPU Board, CAN, ETH, RS485, EEPROM, ModeSW, Reset; Bat, Display, Switches<br>CC BY-NC-SA<br><b>Clemens Niesen, Koenigswinter, Germany</b> |                  |           |
| Sheet: /LowerPCB/<br>File: UP.sch  |                  |           |
| <b>Title: myT41-Controller</b>   |                  |           |
| Size: A3   | Date: 2023-08-18 | Rev: V2.0 |
| KiCad E.D.A. kicad (5.1.7)-1   |                  | Id: 3/3   |