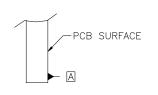


2016 AFCI



PIN	SIGNAL NAME	PIN	SIGNAL NAME						
A1	GND	B12	GND						
A2	SSTXp1	B11	SSRXp1						
АЗ	SSTXn1	B10	SSRXn1						
A4	VBUS	В9	VBUS						
A5	CC1	В8	SBU2						
A6	Dp1	В7	Dn2						
A7	Dn1	В6	Dp2						
A8	SBU1	B5	CC2						
A9	VBUS	B4	VBUS						
A10	SSRXn2	ВЗ	SSTXn2						
A11	SSRXp2	B2	SSTXp2						
A12	GND	B1	GND						

NOTES:

- 1. ELECTRICAL:
 - 1-1. CONTACT CURRENT RATING: 5.0A FOR VBUS AND GND.

(i.e., Pins A4,A9,B4,B9)

1.25A FOR Vconn AND GND.

(i.e., Pins B5,A1,A12,B1,B12) 0.25A FOR ALL THE OTHERS CONTACTS.

1-2. INSULATION RESISTANCE:

100 MEGAOHMS MIN. AT 250VDC.

- 1-3. DIELECTRIC WITHSTANDING VOLTAGE: 100VAC MIN.
- 1-4. LOW LEVEL CONTACT RESISTANCE: INITIAL: $40m\Omega$ MAX.

AFTER ENVIRONMENTAL TEST: $50m\Omega$. MAX.

2.MECHANICAL CHARACTERISTICS:

2-1. INSERTION FORCE: 5N~20N.

2-2. EXTRACTION FORCE:

8N~20N FROM 1 TO 1000 CYCLES 6~20N FROM 1001 TO 10000 CYCLES.

2-3. DURABILITY:

10,000 MATING CYCLES.

2-4. OPERATING TEMPERATURE: -55°C TO +85°C

- 3. CAPABLE TO 260°C REFLOW SOLDER FOR 10 SEC.
- 4. PRODUCT SPECITICATION: GS-12-1336
- 5. PACKAGE SPECITICATION: GS-14-2487

PART NUMBER SCHEME: 10137061-000 X X LF

CONTACT PLATING 1. GOLD FLASH

PACKAGING E AND REEL

2. 30u" Au

PDS: Rev :C 3

1.	TAPI

RECOMMENDED PCB LAYOUT TOLERANCE ±0.05

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form: A4mmXLc-2016-02-24

STATUS:Released

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