





### ROD Firmware current status

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#### Tests with Master TTC Emulator

Master FW: f2c9111 → TTC Emulator (+ ChipScope)

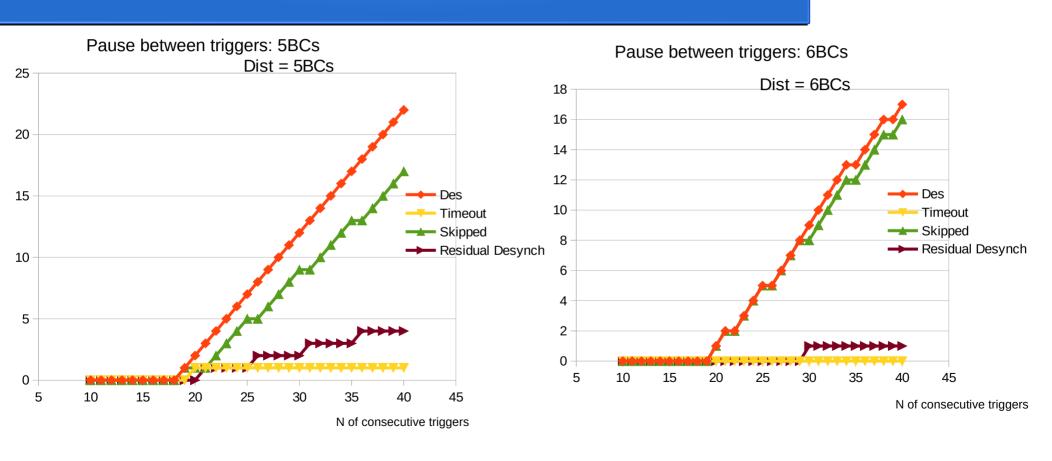
Slave FW: 64397a9 → SmartL1Desynch (always OFF) + Desynch/Timeout/Skipped counters (for monitoring)

Tested on C1\_S7 and C1\_S17 with **detector** (preamps off  $\rightarrow$  no occupancy)

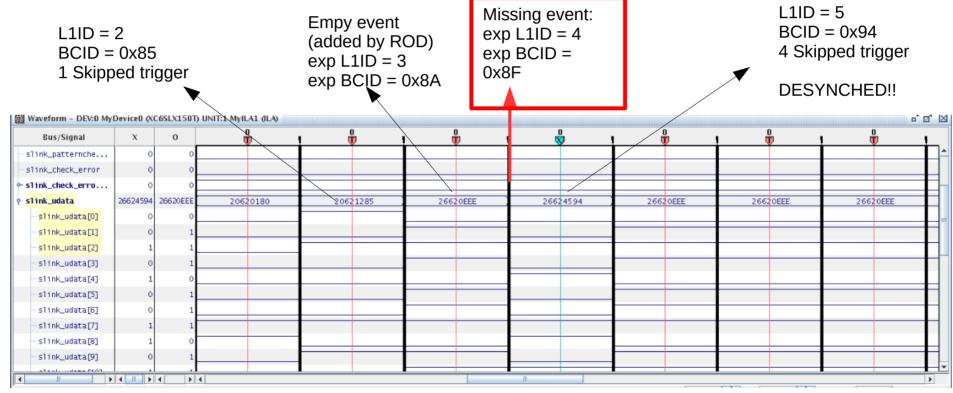
Sending N consecutive triggers with P pause between triggers (pause = 5BCs or 6 BCs)

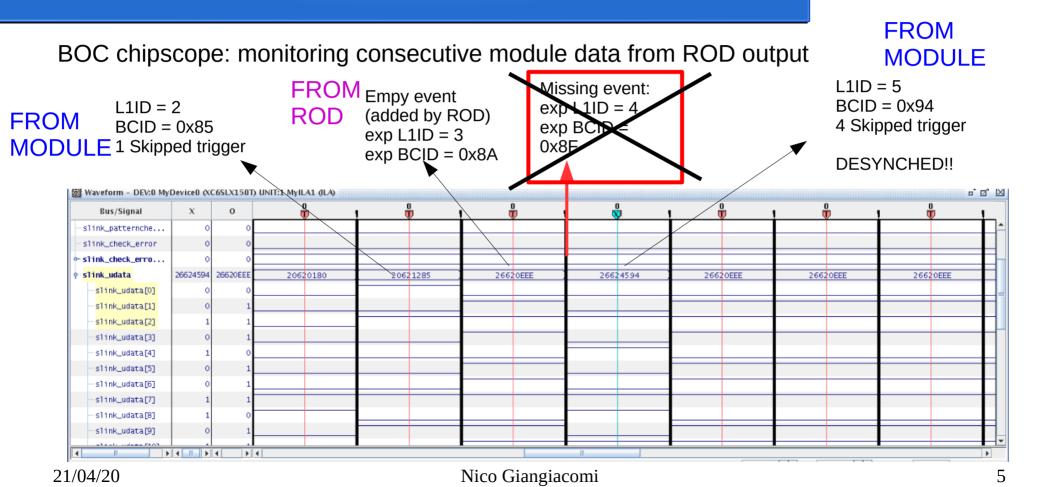
**IDENTICAL** results on C1\_S7 and C1\_S17  $\rightarrow$  (all FEI3/MCCs behave the same way??)

#### Some results



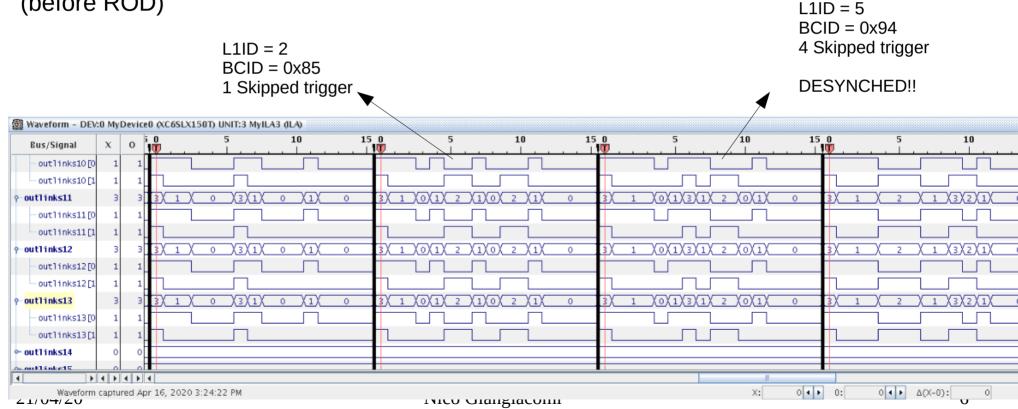
BOC chipscope: monitoring consecutive module data from ROD output

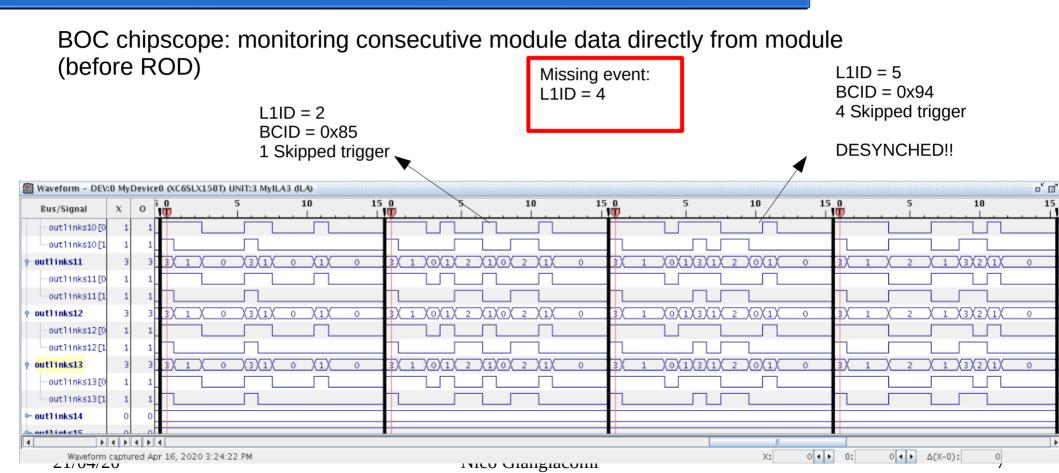




All modules behave the same way

BOC chipscope: monitoring consecutive module data directly from module (before ROD)





### **Conclusions?**

TTC emulator is correctly sending triggers to modules (module L1ID counter is increasing + XC lines checked)

Sometimes, in case of very stressful situation, 1 trigger is not sent by module (timeout  $\rightarrow$  desynch)

Situation changes if first trigger is sent at a different time

(e.g. Test 1: BCs = 105, 111, 117, 123, 129, 136, .....  $\rightarrow$  no timeout Test 2: BCs = 104, 110, 116, 122, 128, 135, ....  $\rightarrow$  timeout)

To do next: test with different firmwares, test with occupancy, ????