

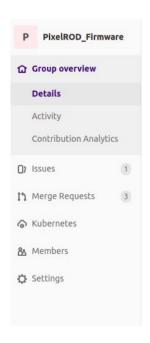
# New Rod Firmware repository, deploy system and workflow

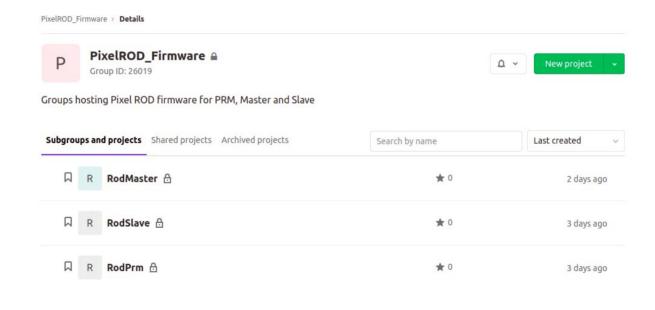
Nico Giangiacomi

# New Firmware repositories

#### New git group path:

https://gitlab.cern.ch/atlas-pixel/daq/pixelrod\_firmware





## RodPrm

#### Git repo path:

https://gitlab.cern.ch/atlas-pixel/daq/pixelrod\_firmware/rodprm

Two main branches (protected, cannot push)

- 1) master  $\rightarrow$  2018 style branch + latest fixes (minor changes). Tagged as V0.x
- 2) user/bologna/Firmware\_UBP  $\rightarrow$  latest modification by Gabriele (major changes). Tagged as V1.x (timing failure)

#### Secondary branches

• **FixTiming** (from user/bologna/firmware\_UBP) → empty, to be used to fix Firmware\_UBP timing

## RodMaster

#### Git repo path:

https://gitlab.cern.ch/atlas-pixel/daq/pixelrod\_firmware/RodMaster

Two main branches (protected, cannot push)

- 1) master  $\rightarrow$  2018 style branch + latest fixes (minor changes). Tagged as V0.x
- 2) New\_Features  $\rightarrow$  latest modification by Gabriele (major changes). Tagged as V1.x

#### Secondary branches

• **newMasterTTCEmu** (from master)→ new TTC emulator (to be properly validated yet)

### RodSlave

#### Git repo path:

https://gitlab.cern.ch/atlas-pixel/daq/pixelrod\_firmware/RodSlave

Two main branches (protected, cannot push)

- 1) master  $\rightarrow$  2018 style branch + latest fixes (minor changes). Tagged as V0.x
- 2) Firmware\_separated → calibration VS datataking + fix on terminations. Tagged as V1.x

#### Secondary branches

- gitSha (from Firmware\_separated) -> attempt to get SHA for dataTaking FW without MB
- **NewMerger** (from Firmware\_separated) → new slave merger for dataTaking
- Firmware\_SmartL1IDAlgorithm (from Firmware\_separated)→ smart L1ID algorithm for Pixel and Slave dataTaking fw
- user/ngiangia/newTimeout → very old attempt to implement new timeout mechanism (not working)

## Workflow

When adding/modifying something:

- 1) **create new branch** (from the one that needs to be modified)
- 2) work normally
- 3) **push** your branch and create **merge request**
- 4) Continuous Integration builds firmware and deploy it to cvmfs (test)
- 5) **Test** the firmware
- 6) If tests are successful, **resume Continuous Integration stages** (merge and tag) to automatically **tag** the new firmware, create **release** and **deploy to cvmfs** (official)







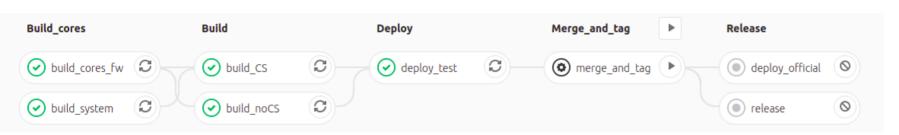
116 -o- c4c7e35c system not downloaded by do...







## CI Stages

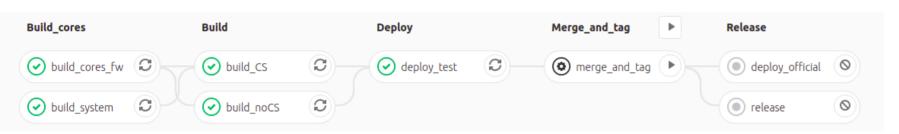


#### 5 stages:

- 1) build cores → regenerates the fw cores. Precompiled cores (associated to their SHA) are stored in eos and retireved to speed up this process
- 2) **build** → generates the fw (with CS and without CS). Fails if timing errors. CS firmwares CAN fail
- 3) **deploy** → firmwares are copied to **cvmfs** in: rod/firmware/rodXXXX/test/\$SHA/...
- 4) **Merge and tag** (to be **manually triggered** after testing fws) → automatically merges the branch (it should close the MR) and creates a new tag
- **5) Release** (only after merge\_and\_tag) → creates **new release** with MR description as changelog and with **binaries**, copy binaries to **cvmfs** in rod/firmware/rodXXXX/official/\$tag/... (the very same firmware is copied)

14/07/20

# CI Stages



5 stages:

- 1) build cores → regenerates stored in eos and retireved
- 2) **build**  $\rightarrow$  generates the fw fail

NOTE: the merged branch is not closed. Must be closed manually

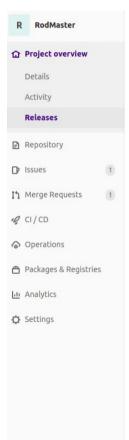
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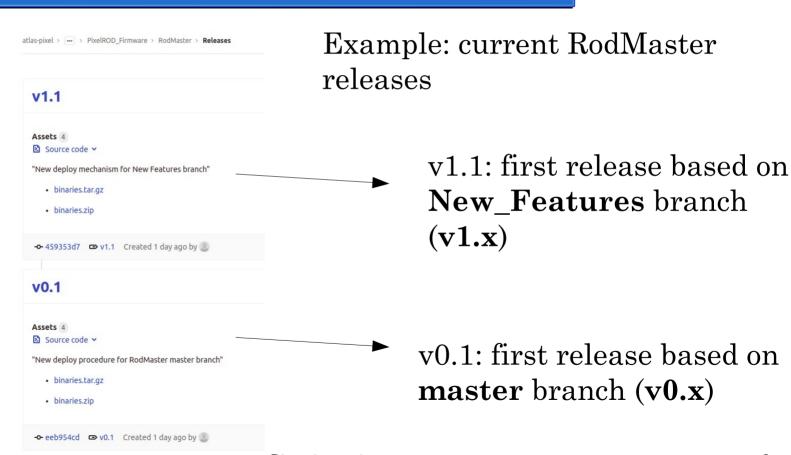
rors. CS firmwares CAN

- 3) **deploy** → firmwares are copied to **cvmfs** in: rod/firmware/rodXXXX/test/\$SHA/...
- 4) **Merge and tag** (to be **manually triggered** after testing fws) → automatically merges the branch (it should close the MR) and creates a new tag
- **5) Release** (only after merge\_and\_tag) → creates **new release** with MR description as changelog and with **binaries**, copy binaries to **cvmfs** in rod/firmware/rodXXXX/official/\$tag/... (the very same firmware is copied)

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### RELEASES





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