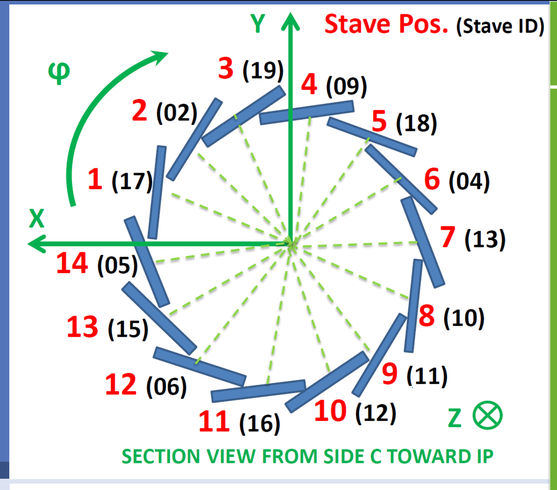
IBL multi-view mapping

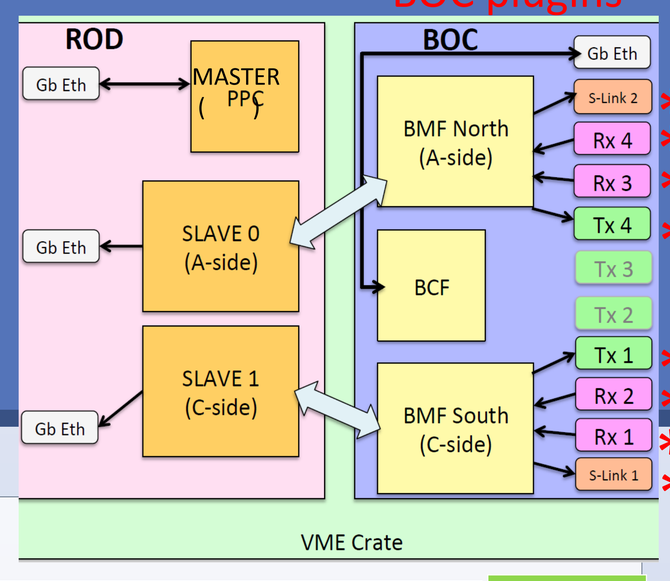
If you find any mistakes or if you would like to add/modify information that you think are relevant , please contact *Marcello.Bindi@cern.ch*

Disclaimer: this is not meant to be a Connectivity map but only an "expert- friendly" poster to be hung in the Satellite Control Room....



Stave Pos.	Centre phi (rad.)
1	0.34
2	0.79
3	1.24
4	1.69
5	2.14
6	2.58
7	3.03
8	-2.80
9	-2.35
10	-1.90
11	-1.45
12	-1.00
13	-0.56
14	-0.11

3D sensors
Planar sensors
3D+Planar sensor



IBL Detector/DCS/DAQ view

Information Type	C SIDE																A SIDE																Information Type
DCS Group (HV/LV)	M4-C				M3-C				M2-C				M1-C				M1-A				M2-A				M3-A				M4-A				DCS Group (HV/LV)
Temp. Sensor	NTC4-C				NTC3-C				NTC2-C				NTC1-C				NTC1-A				NTC2-A				NTC3-A				NTC4-A				Temp. Sensor
Pixel Sensor	C8-2	C8-1	C7-2	C7-1	C6		C5		C4		C3		C2		C1		A1		A2		A3		A4		A5		A6		A7-1	A7-2	A8-1	A8-2	Pixel Sensor
Geograph. Address	7	6	7	6	7	6	7	6	7	6	7	6	7	6	7	6	7	6	7	6	7	6	7	6	7	6	7	6	7	6	7	6	Geograph. Address
FE-ID (Module Prod.)	0	0	0	0	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	0	0	0	FE-ID (Module Prod.)
FE-Index (DAQ Console)	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	FE-Index (DAQ Console)
DAQ Module	C8		C7		C6		C5		C4		C3		C2		C1		A1		A2		A3		A4		A5		A6		A7		A8		DAQ Module
FE-I4 Chip	C8-2	C8-1	C7-2	C7-1	C6-2	C6-1	C5-2	C5-1	C4-2	C4-1	C3-2	C3-1	C2-2	C2-1	C1-2	C1-1	A1-1	A1-2	A2-1	A2-2	A3-1	A3-2	A4-1	A4-2	A5-1	A5-2	A6-1	A6-2	A7-1	A7-2	A8-1	A8-2	FE-I4 Chip
TX (PPC mask bit)	16		17		18		19		20		21		22		23		0		1		2		3		4		5		6		7		TX (PPC mask bit)
RX (PPC mask bit)	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	RX (PPC mask bit)

IBL VME Crate Layout

Information Type																		Information Type
CRATE Slot	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	CRATE Slot
IBL Stave	-	1	2	3	4	5	6	7		8	9	10	11	12	13	14	DBM	IBL Stave
ROD Master IP: 10.145.87.xxx	-	206	207	208	209	210	211	212	TIM	214	215	216	217	218	219	220	221	ROD Master IP: 10.145.87.xxx
ROD Slave 0 IP: 10.145.89.xxx (Det. Side)	-	106 (A)	107 (A)	108 (A)	109 (A)	110 (A)	111 (A)	112 (A)		114 (A)	115 (A)	116 (A)	117 (A)	118 (A)	119 (A)	120 (A)	121 (A)	ROD Slave 0 IP: 10.145.89.xxx (Det. Side)
ROD Slave 1 IP: 10.145.89.xxx (Det. Side)	-	206 (C)	207 (C)	208 (C)	209 (C)	210 (C)	211 (C)	212 (C)		214 (C)	215 (C)	216 (C)	217 (C)	218 (C)	219 (C)	220 (C)	221 (C)	ROD Slave 1 IP: 10.145.89.xxx (Det. Side)
BOC IP: 10.145.87.xxx	-	106	107	108	109	110	111	112		114	115	116	117	118	119	120	121	BOC IP: 10.145.87.xxx
ROD ID	-	0x140060	0x140070	0x140080	0x140090	0x140100	0x140110	0x140120		0x140140	0x140150	0x140160	0x140170	0x140180	0x140190	0x140200	0x140210	ROD ID

IBL STAVE/BOC/ROD/TDAQ View

Information Type	C SIDE																A SIDE																Information Type
FE-I4 Chip	C8-2	C8-1	C7-2	C7-1	C6-2	C6-1	C5-2	C5-1	C4-2	C4-1	C3-2	C3-1	C2-2	C2-1	C1-2	C1-1	A1-1	A1-2	A2-1	A2-2	A3-1	A3-2	A4-1	A4-2	A5-1	A5-2	A6-1	A6-2	A7-1	A7-2	A8-1	A8-2	FE-I4 Chip
RX	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	RX
OptoBoard VCSEL	VCSEL 1								VCSEL 2								VCSEL 1								VCSEL 2								OptoBoard VCSEL
BOC TX* Plugin	TX1																TX4																BOC TX* Plugin
BOC RX* Plugin	RX2								RX1								RX3								RX4								BOC RX* Plugin
BOC QSFP* Plugin	SLINK 1																SLINK 2																BOC QSFP* Plugin
BOC BMF	SOUTH																NORTH																BOC BMF
ROD SLAVE ID	1																0																ROD SLAVE ID
ROD Link ID	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3	ROD Link ID
ROD Formatter ID	0				1				0				1				0				1				0				1				ROD Formatter ID
ROD EFB ID	0								1								0								1								ROD EFB ID
ROD Histo Unit	0								1								0								1								ROD Histo Unit
ROD SLINK Unit	0								1								0								1								ROD SLINK Unit
ROL_ID	ROD ID + 2								ROD ID + 3								ROD ID + 0								ROD ID + 1								ROL_ID