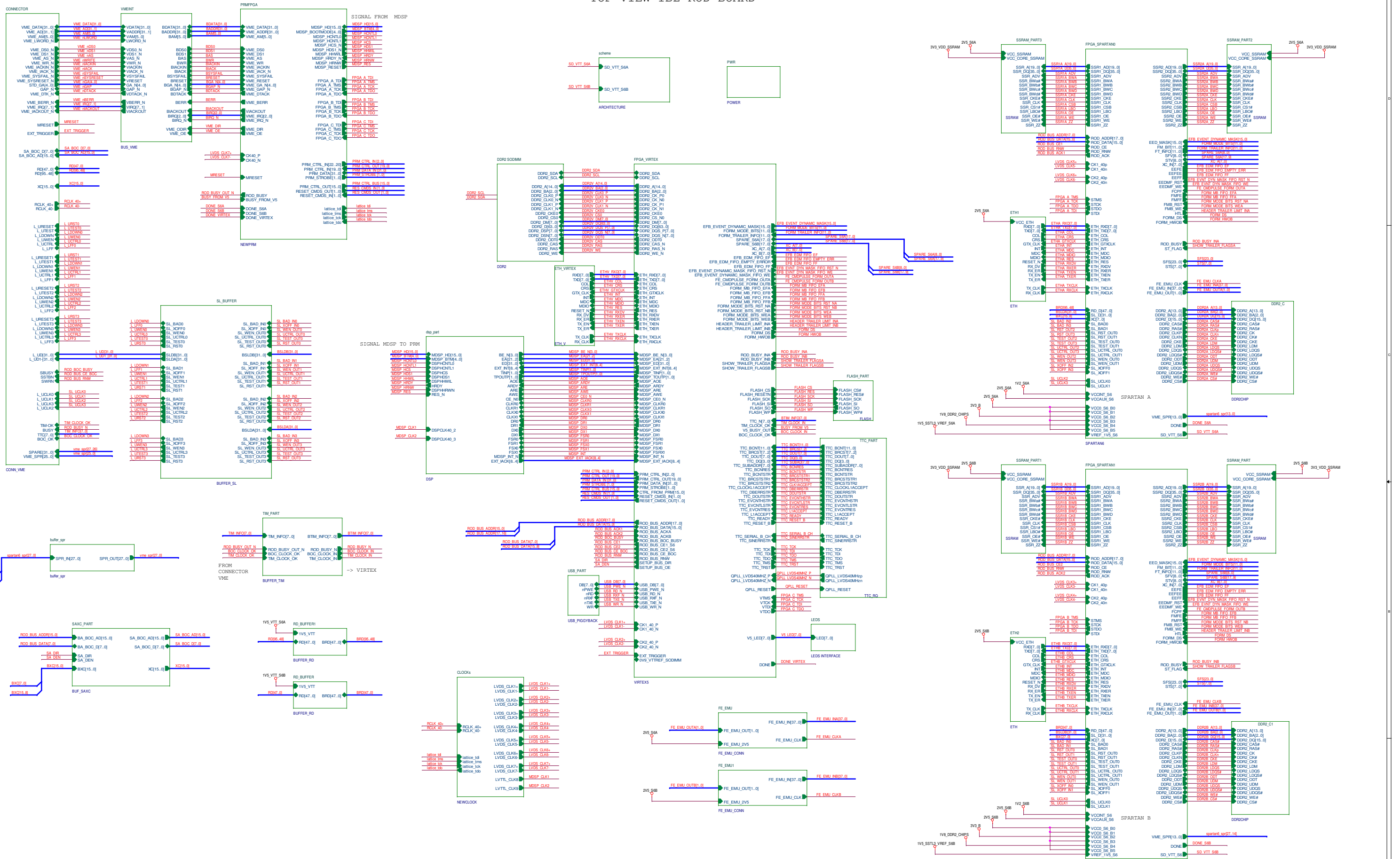
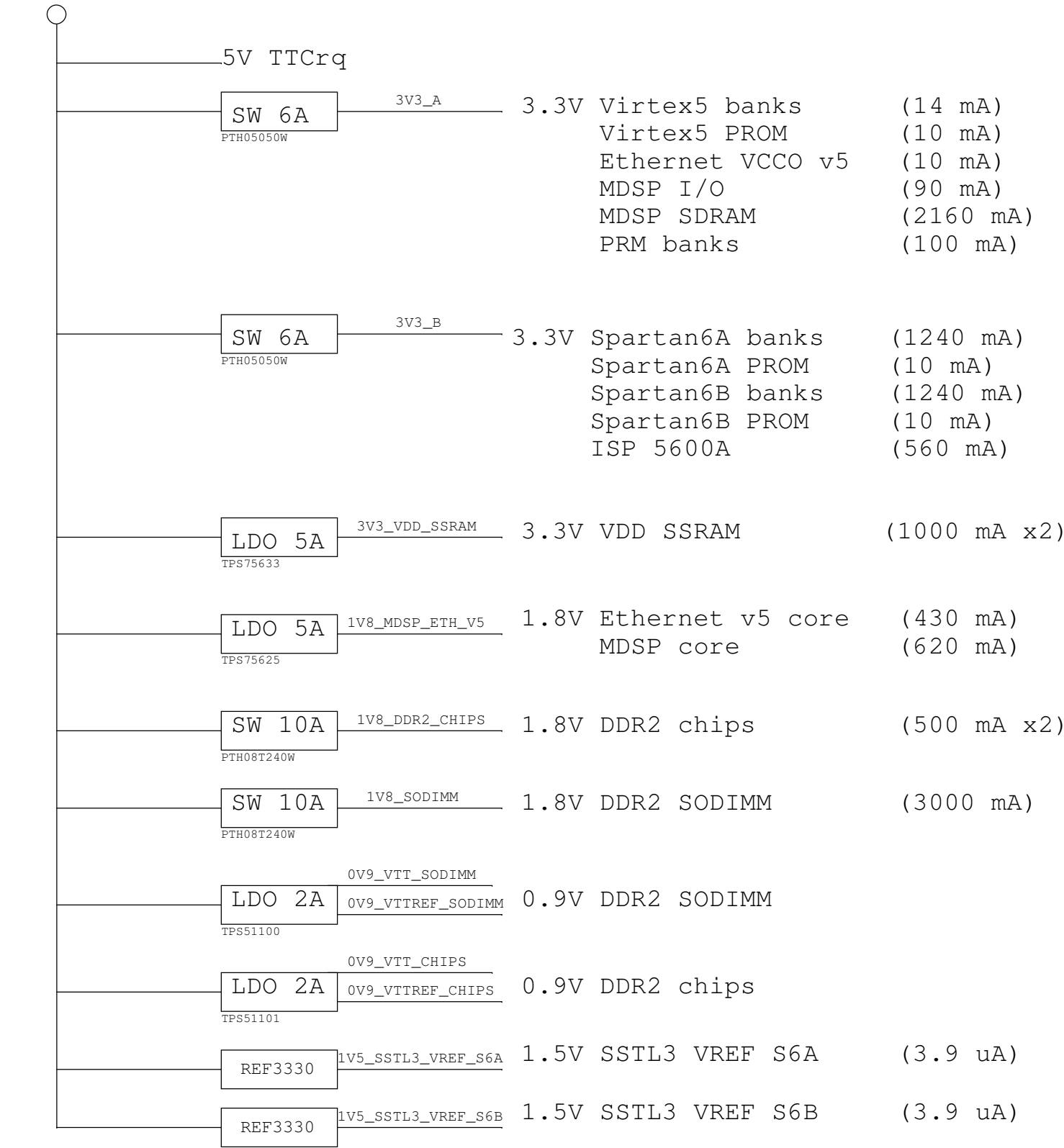


TOP VIEW IBL ROD BOARD



5V POWER SUPPLY DISTRIBUTION

5V VME



TOTAL: 10494 mA

Virtex5

Supply Summary		Total	Dynamic	Quiescent
Source	Voltage	Current (A)	Current (A)	Current (A)
Vccint	1.000	1.634	0.329	1.304
Vccaux	2.500	0.602	0.023	0.579
Vcco33	3.300	0.014	0.008	0.006
Vcco25	2.500	0.022	0.016	0.006
Vcco18	1.800	1.748	0.007	1.741
Supply Power (W)		6.388	0.468	5.920

Spartan6 (LX150)

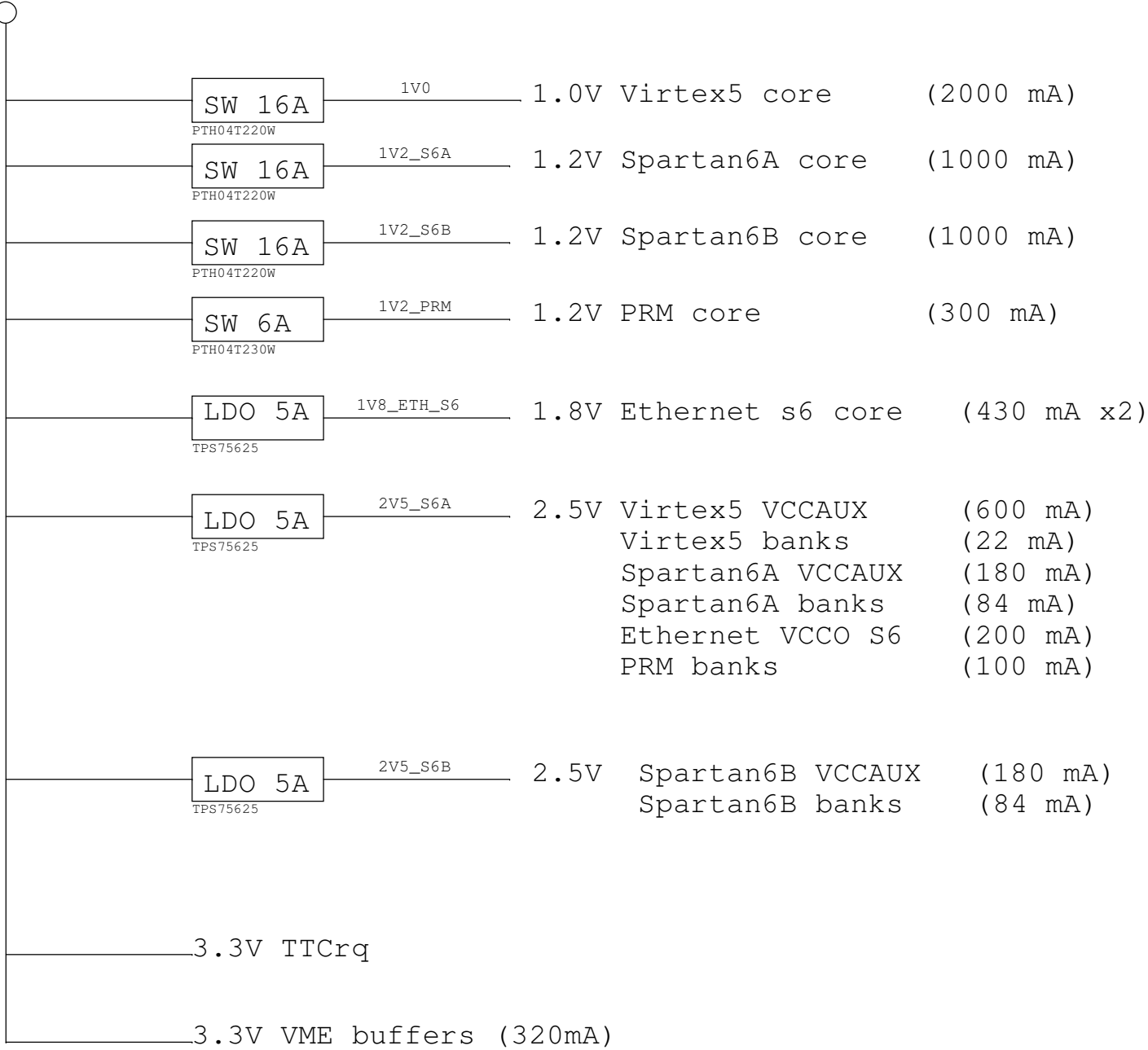
Supply Summary		Total	Dynamic	Quiescent
Source	Voltage	Current (A)	Current (A)	Current (A)
Vccint	1.200	0.338	0.228	0.110
Vccaux	2.500	0.179	0.017	0.162
Vcco33	3.300	1.244	0.004	1.239
Vcco25	2.500	0.084	0.077	0.007
Vcco18	1.800	0.307	0.009	0.299
Supply Power (W)		5.720	0.537	5.182

Spartan6 (LX45)

Supply Summary		Total	Dynamic	Quiescent
Source	Voltage	Current (A)	Current (A)	Current (A)
Vccint	1.200	0.043	0.026	0.016
Vccaux	2.500	0.011	0.000	0.010
Vcco33	3.300	0.004	0.001	0.003
Vcco25	2.500	0.007	0.004	0.003
Supply Power (W)		0.107	0.045	0.062

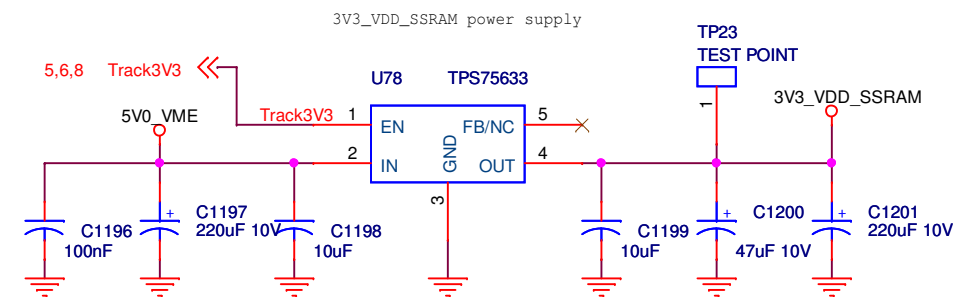
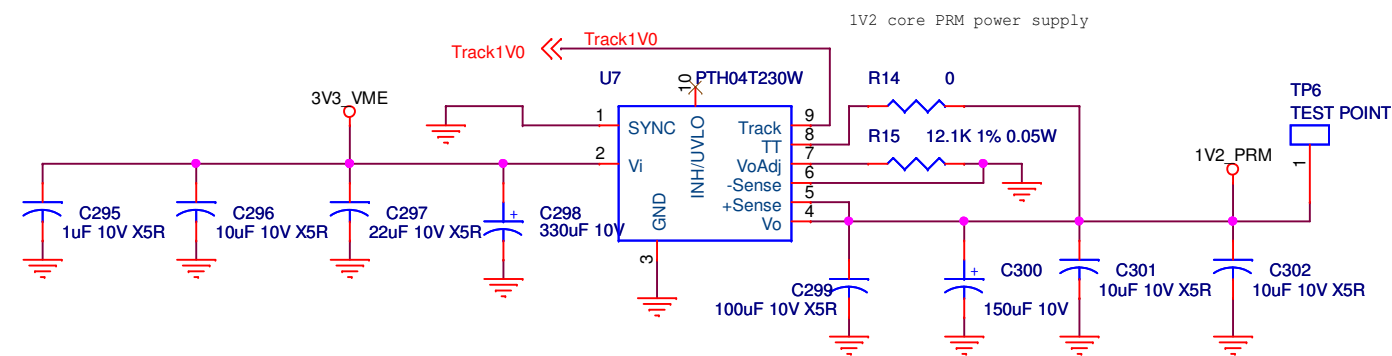
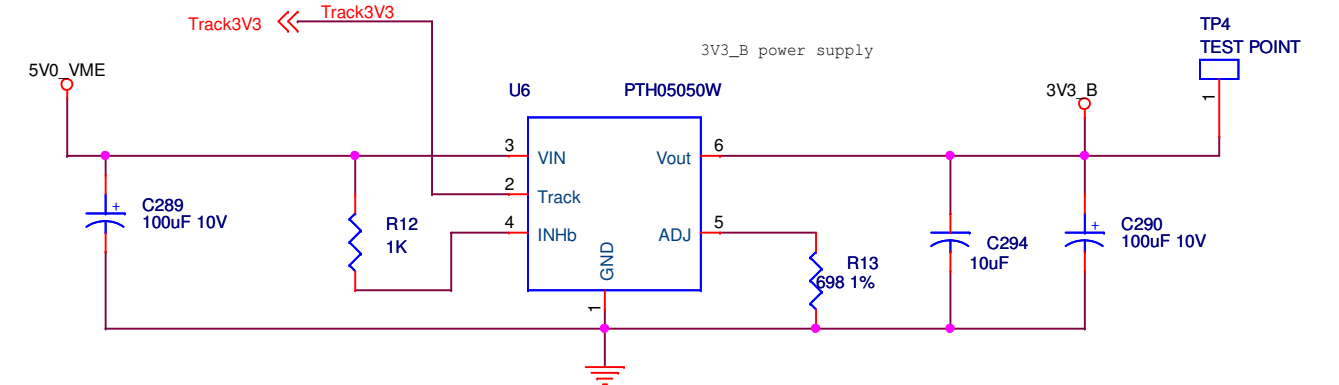
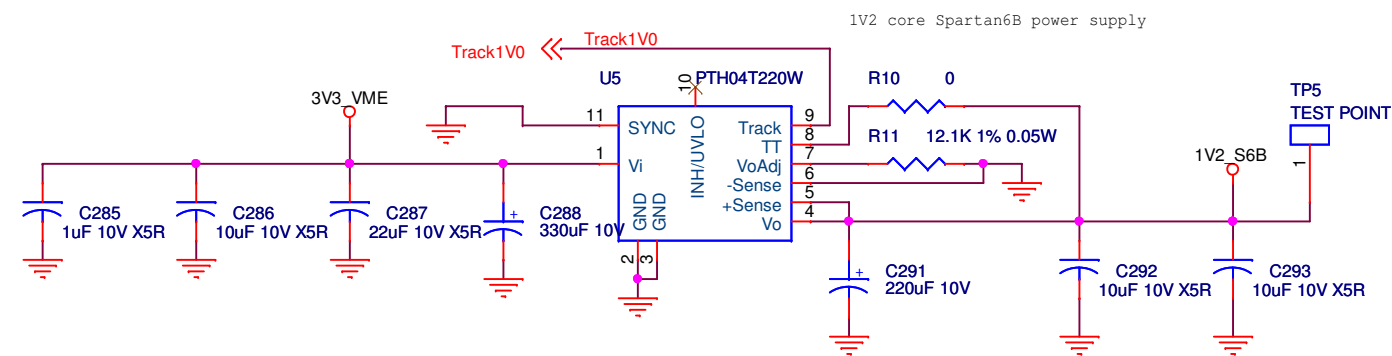
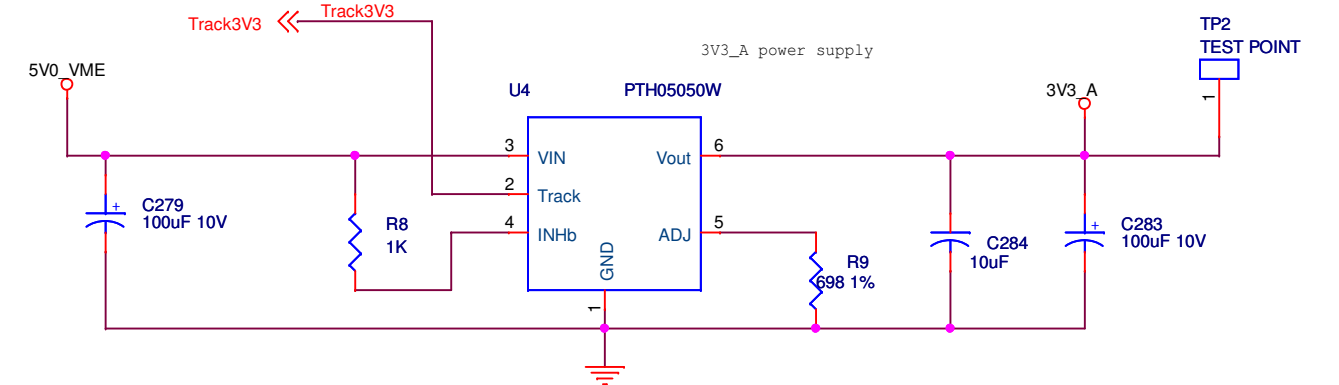
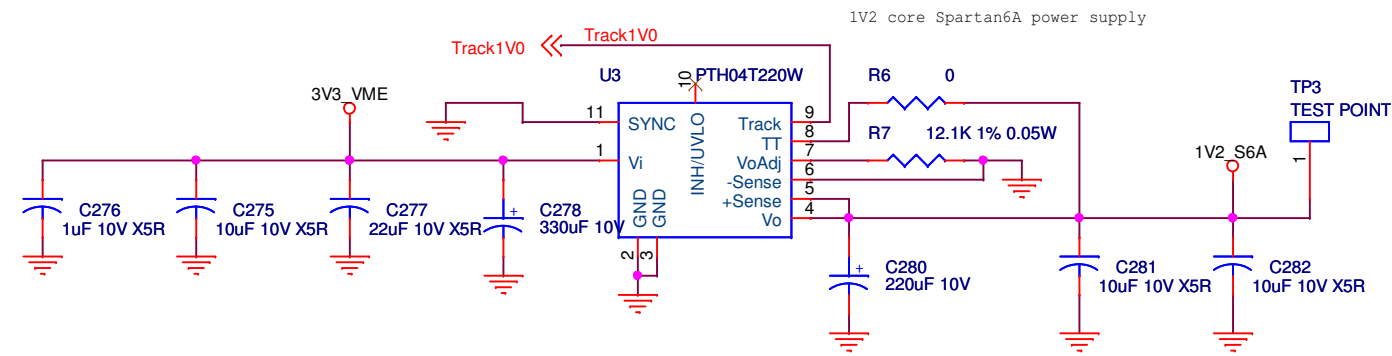
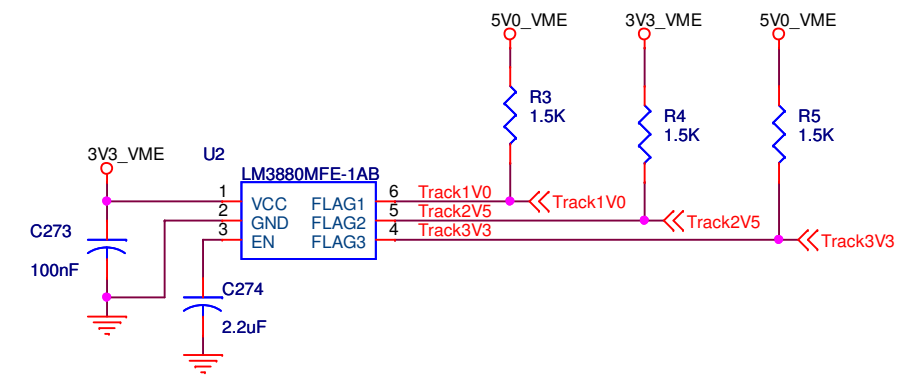
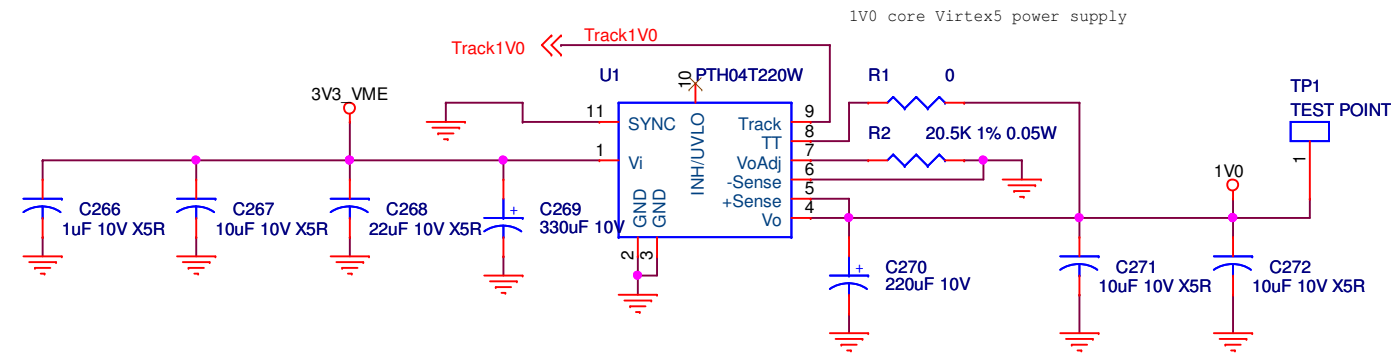
3.3V POWER SUPPLY DISTRIBUTION

3.3V VME



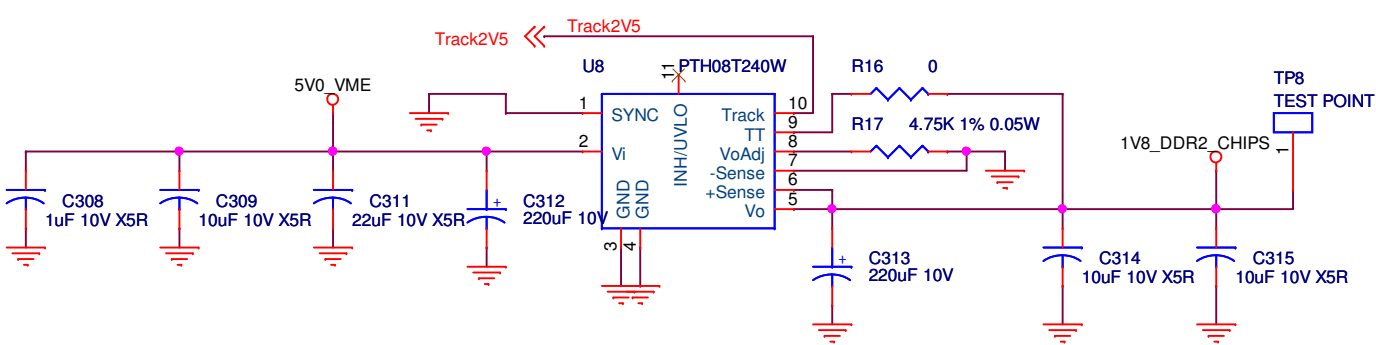
TOTAL: 8690 mA

POWER SUPPLY DISTRIBUTION 1/3

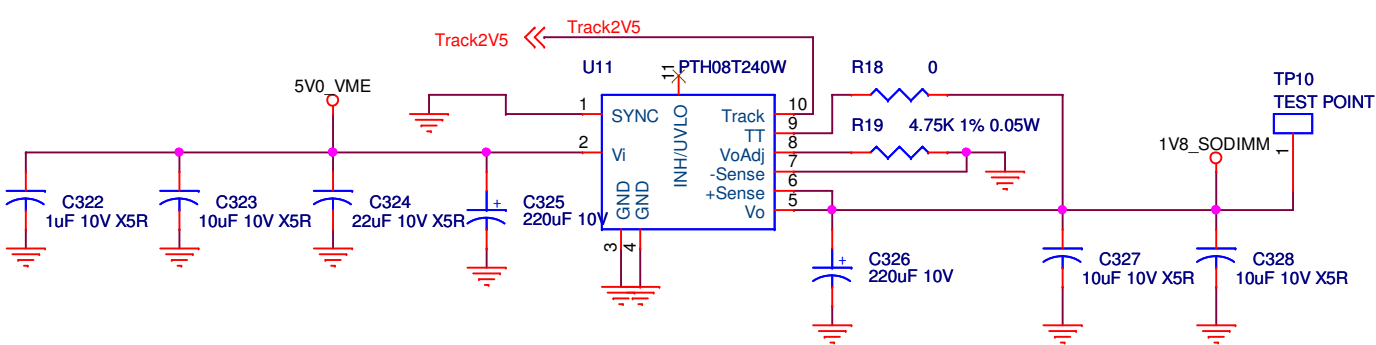


POWER SUPPLY DISTRIBUTION 2/3

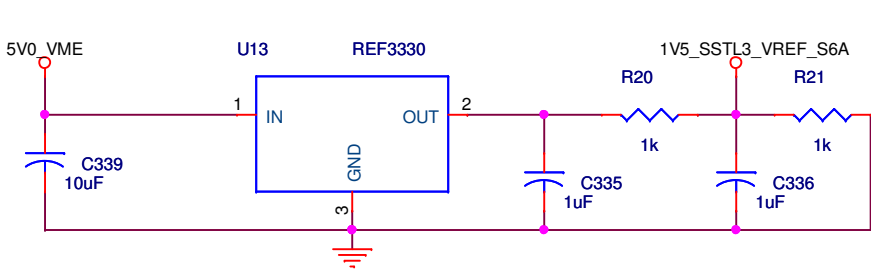
power supply 1V8_DDR2_CHIPS



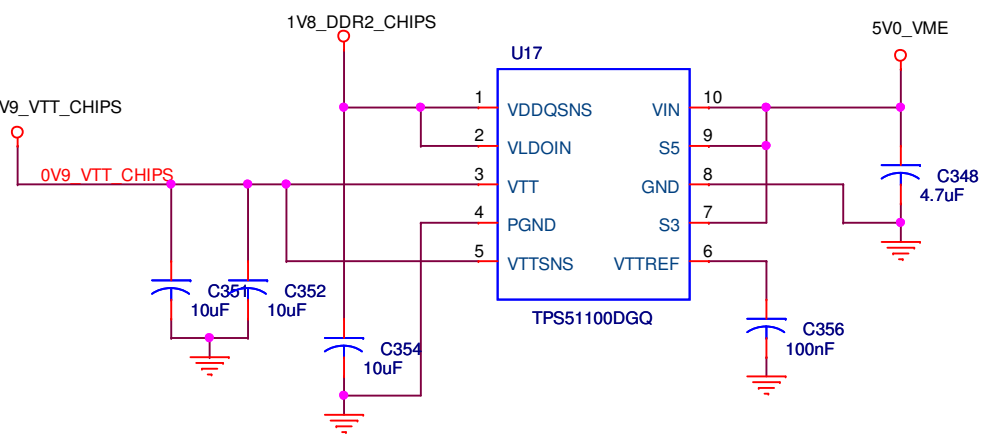
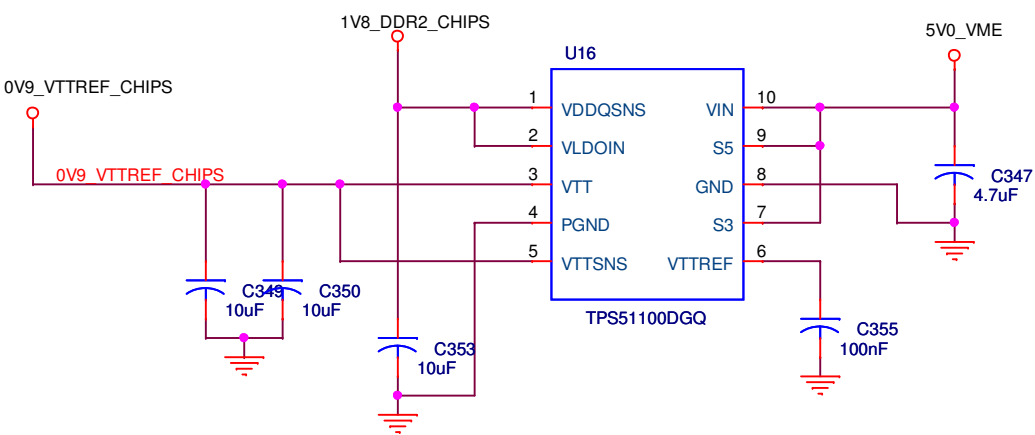
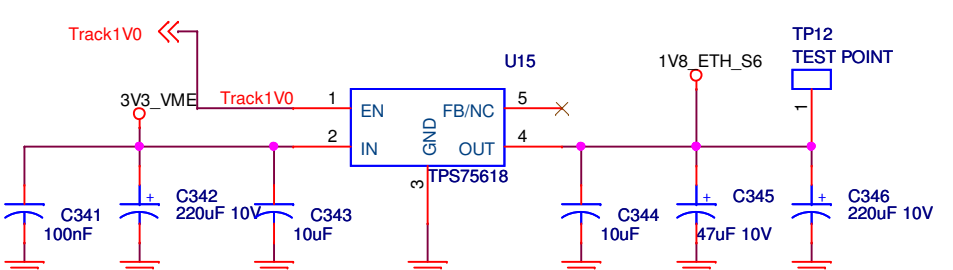
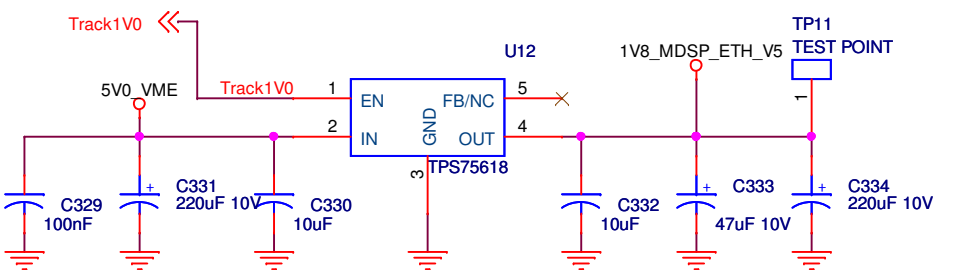
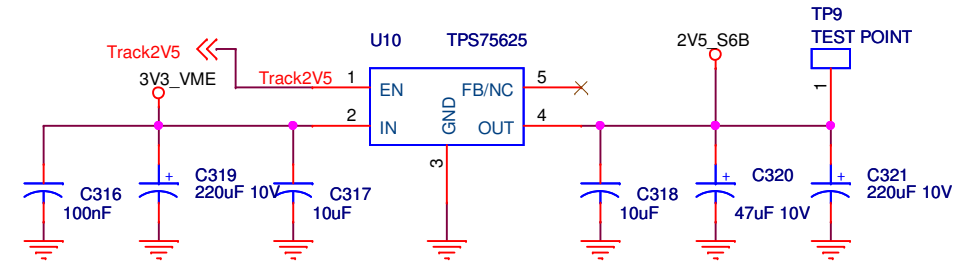
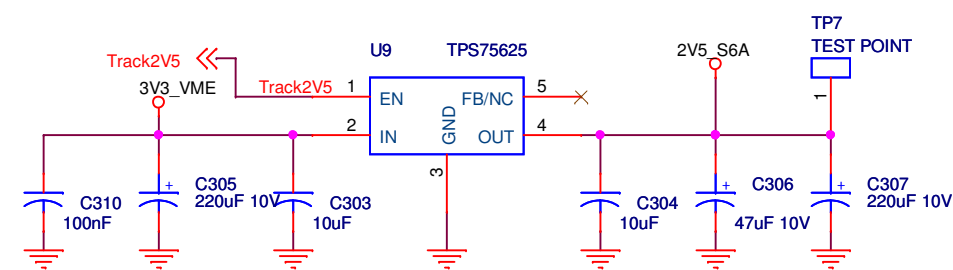
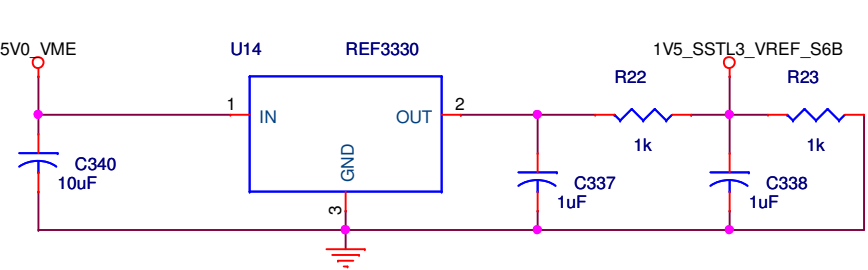
power supply 1V8_SODIMM



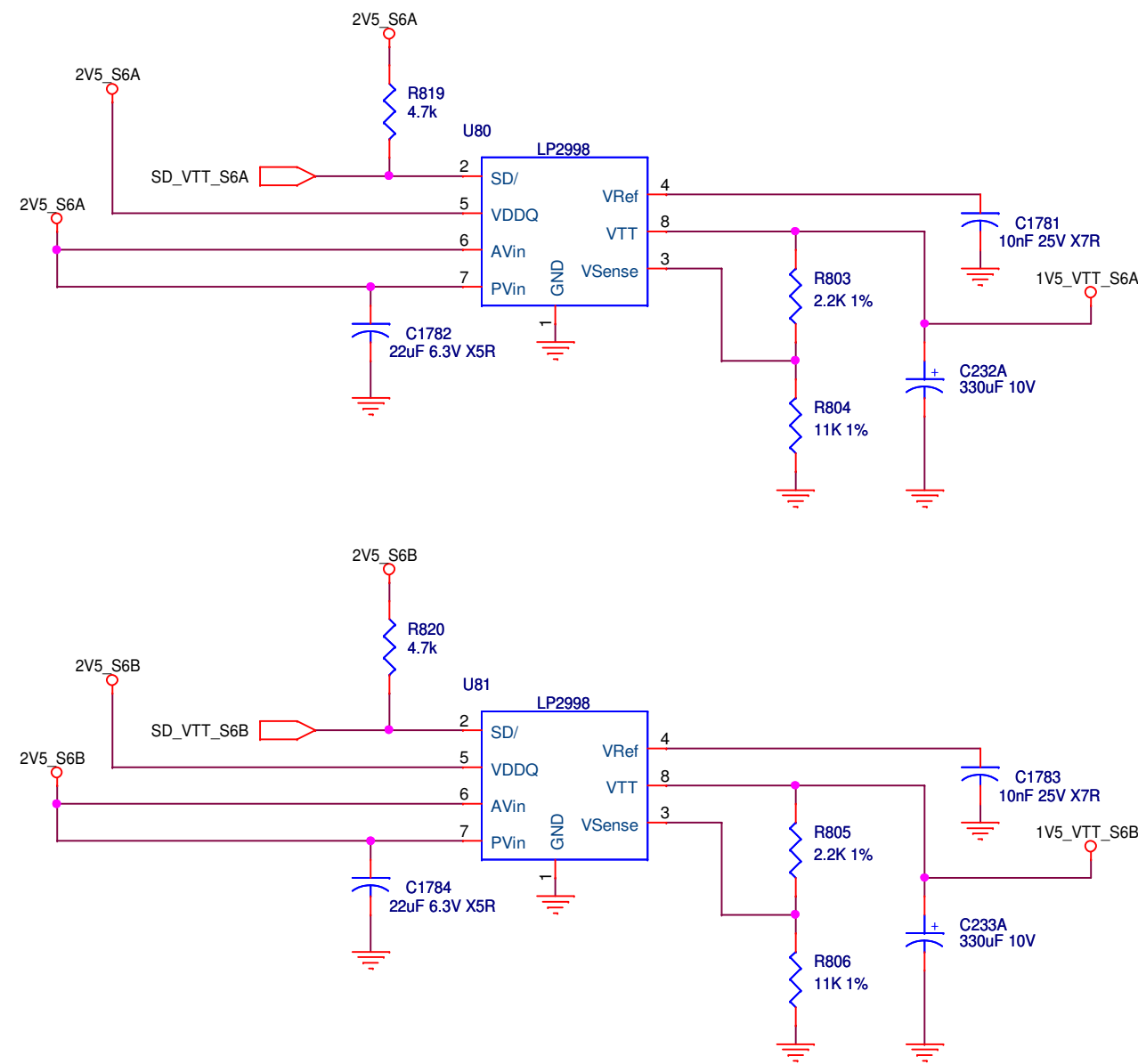
power supply 1V5_SSTL3_VREF_S6A



power supply 1V5_SSTL3_VREF_S6B

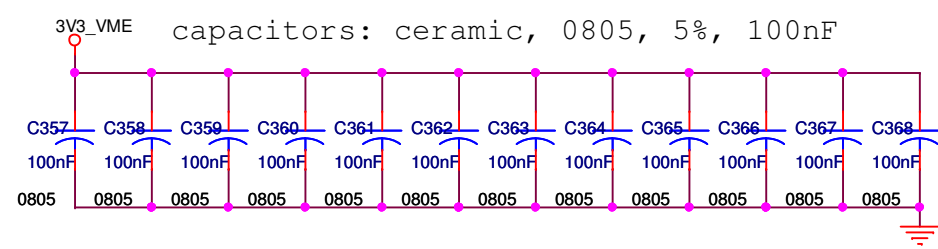
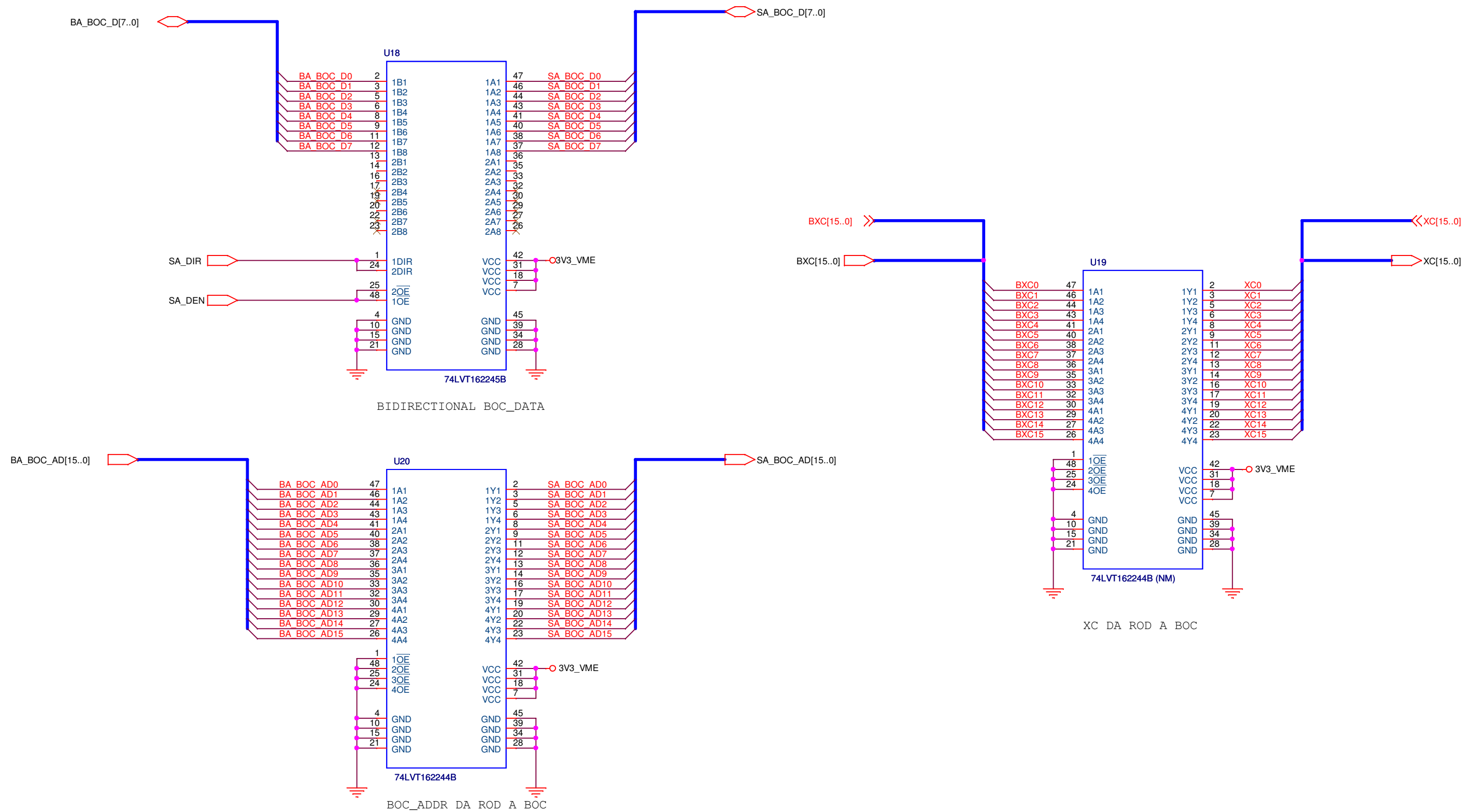


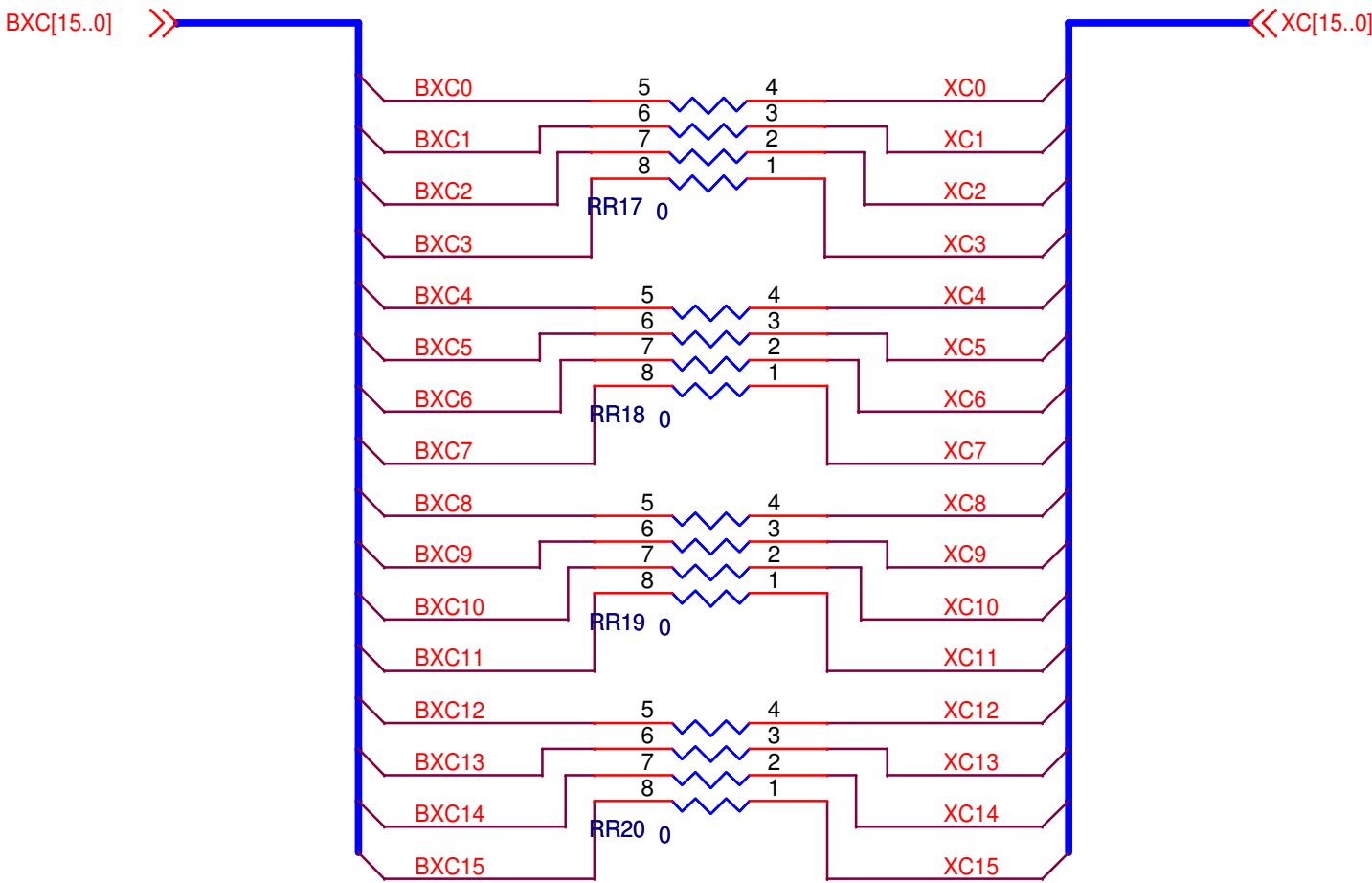
POWER SUPPLY DISTRIBUTION 3/3

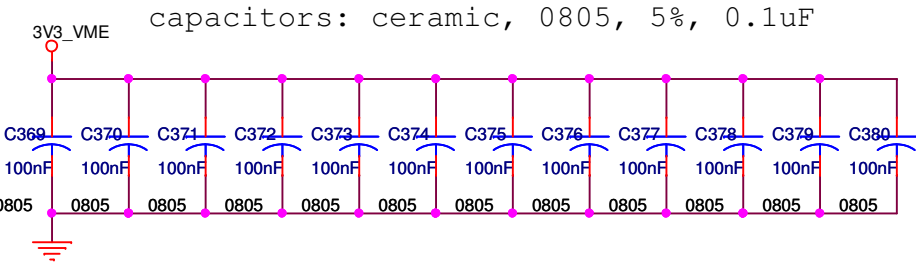
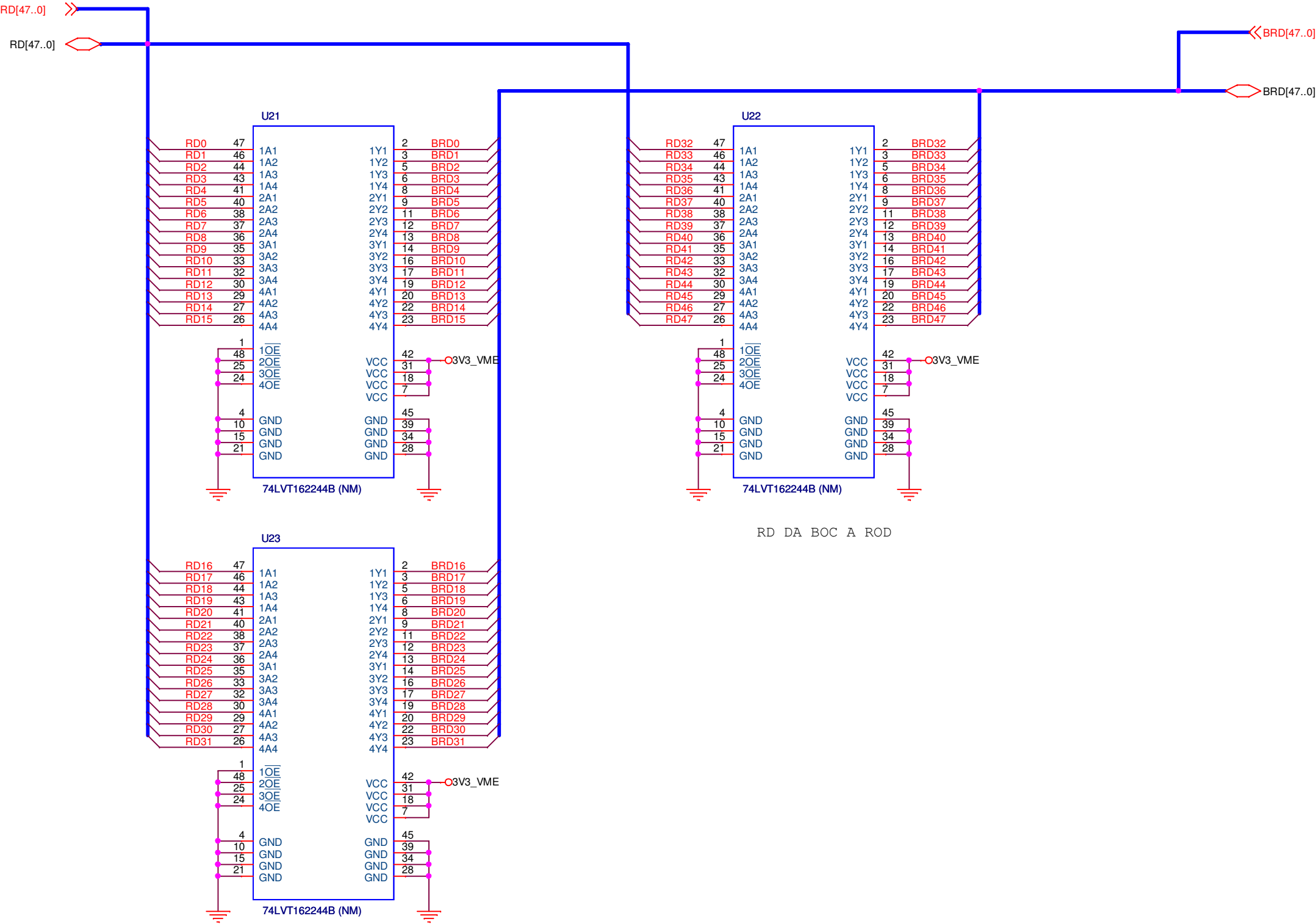


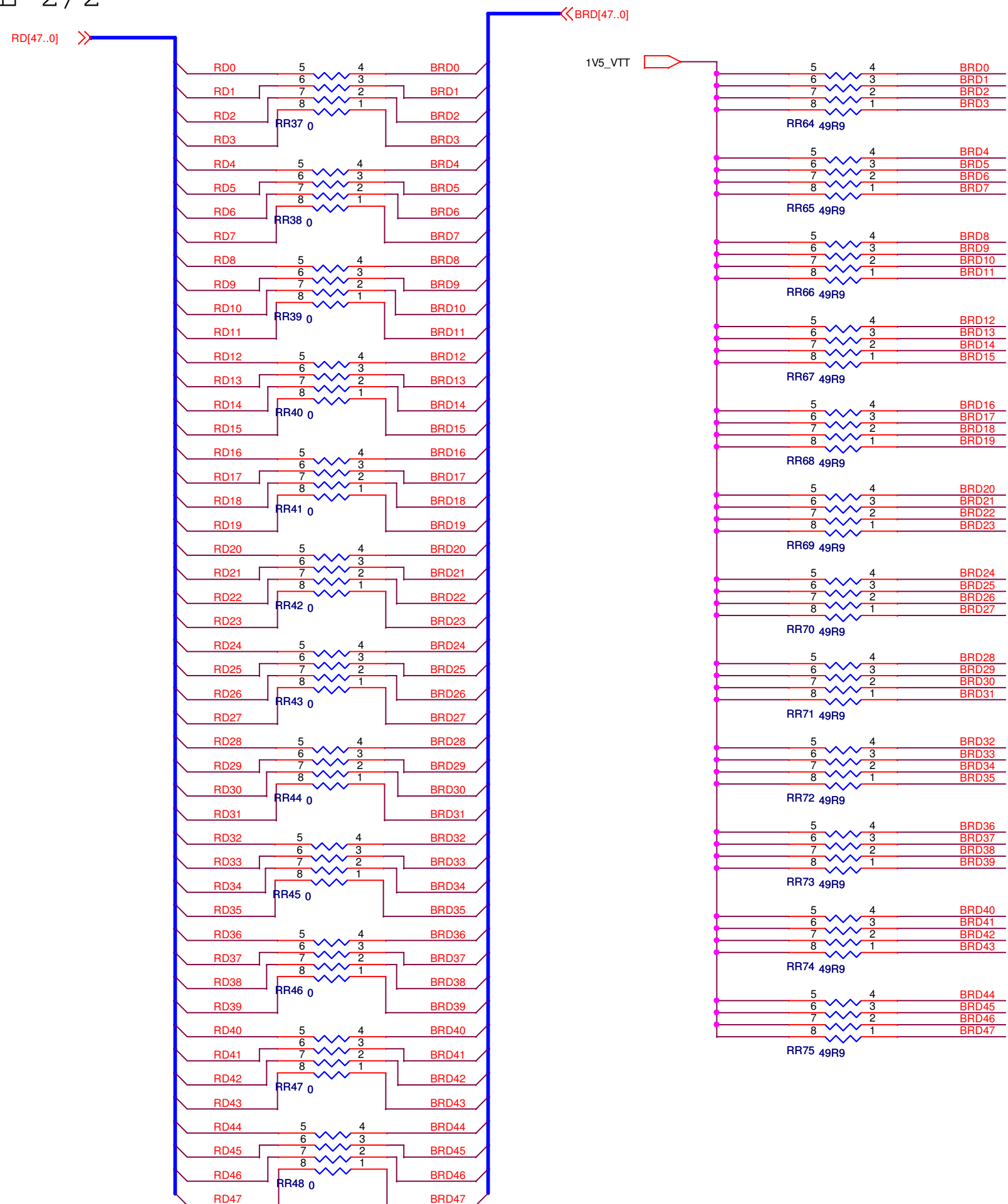
Title		
<Title>		
Size	Document Number	Rev
B	<Doc>	<RevCode>
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BOC BUS BUFFERS PAGE 1/2

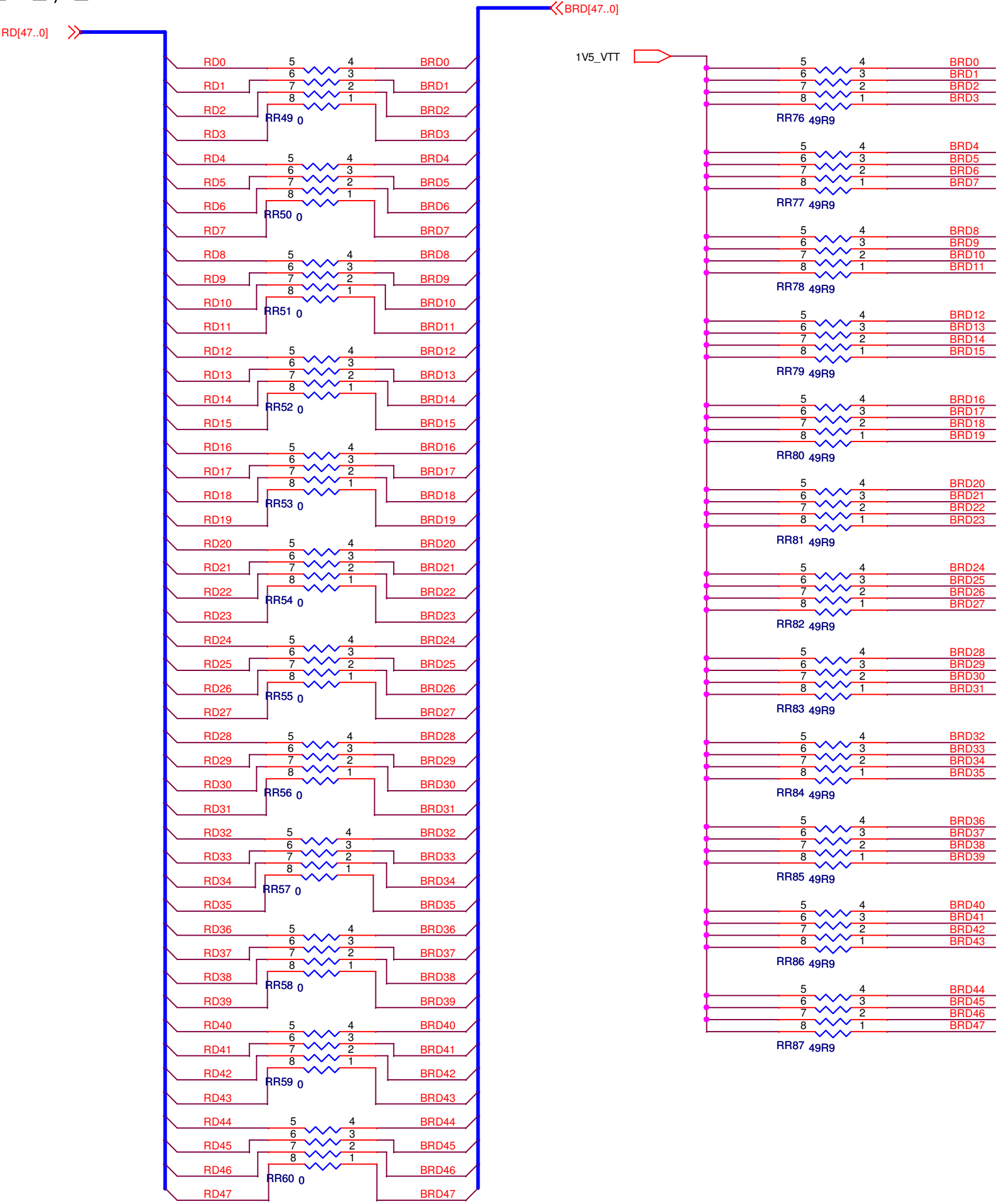


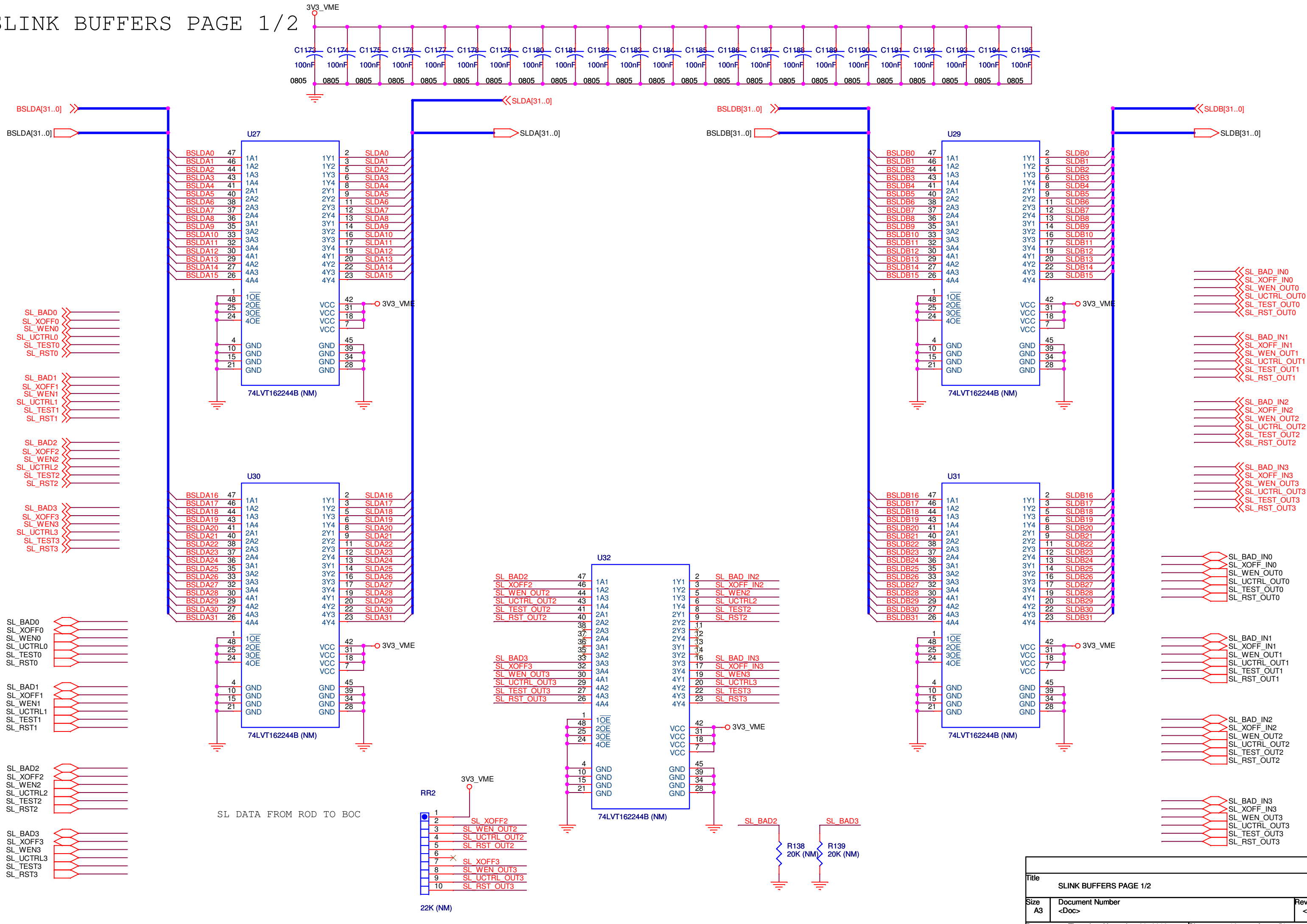


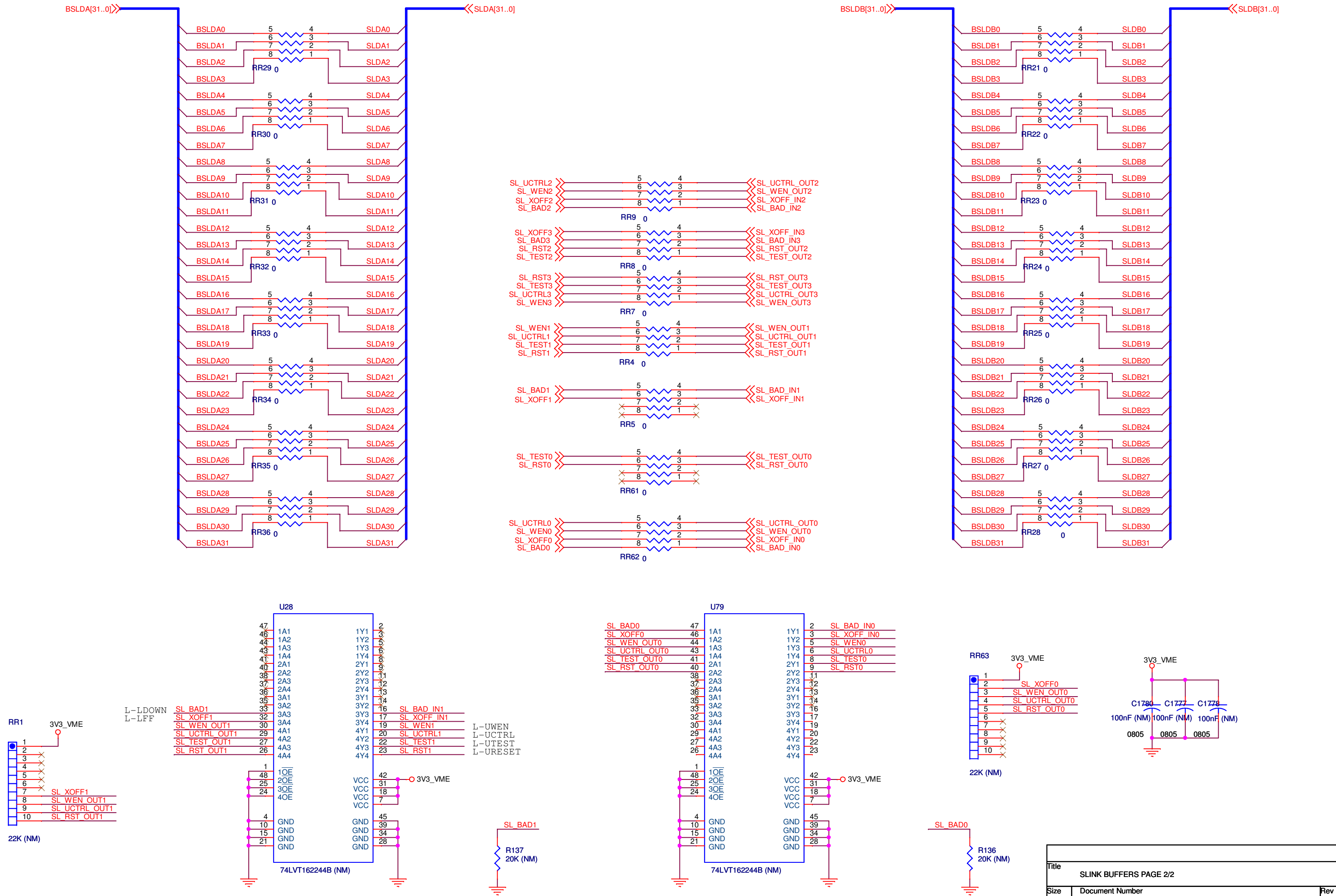




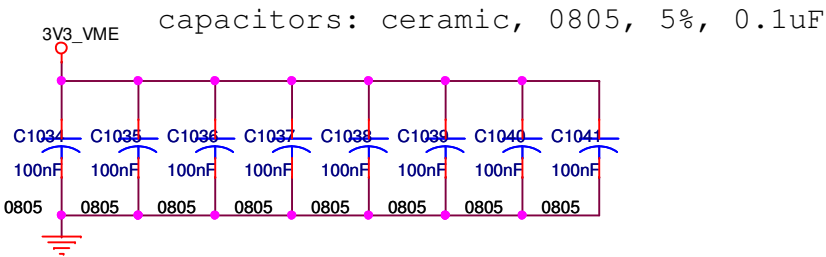
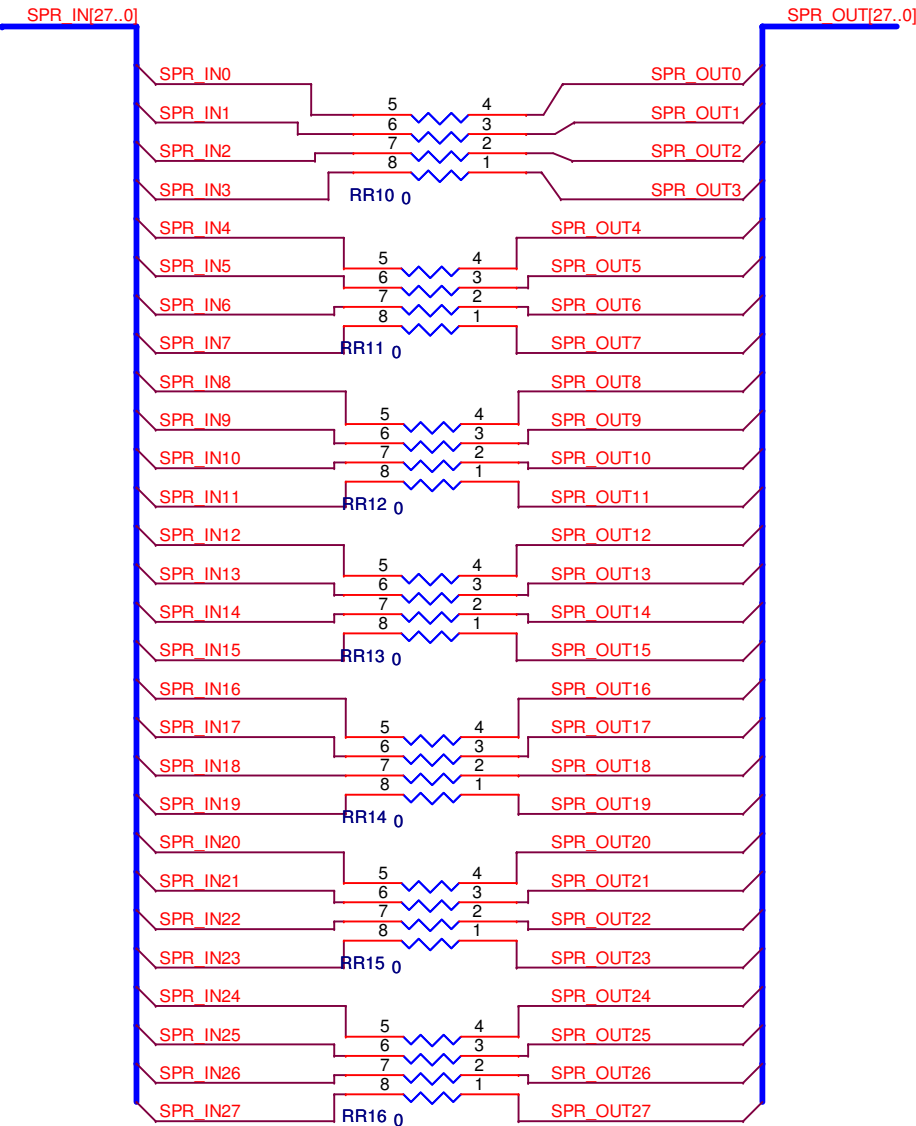
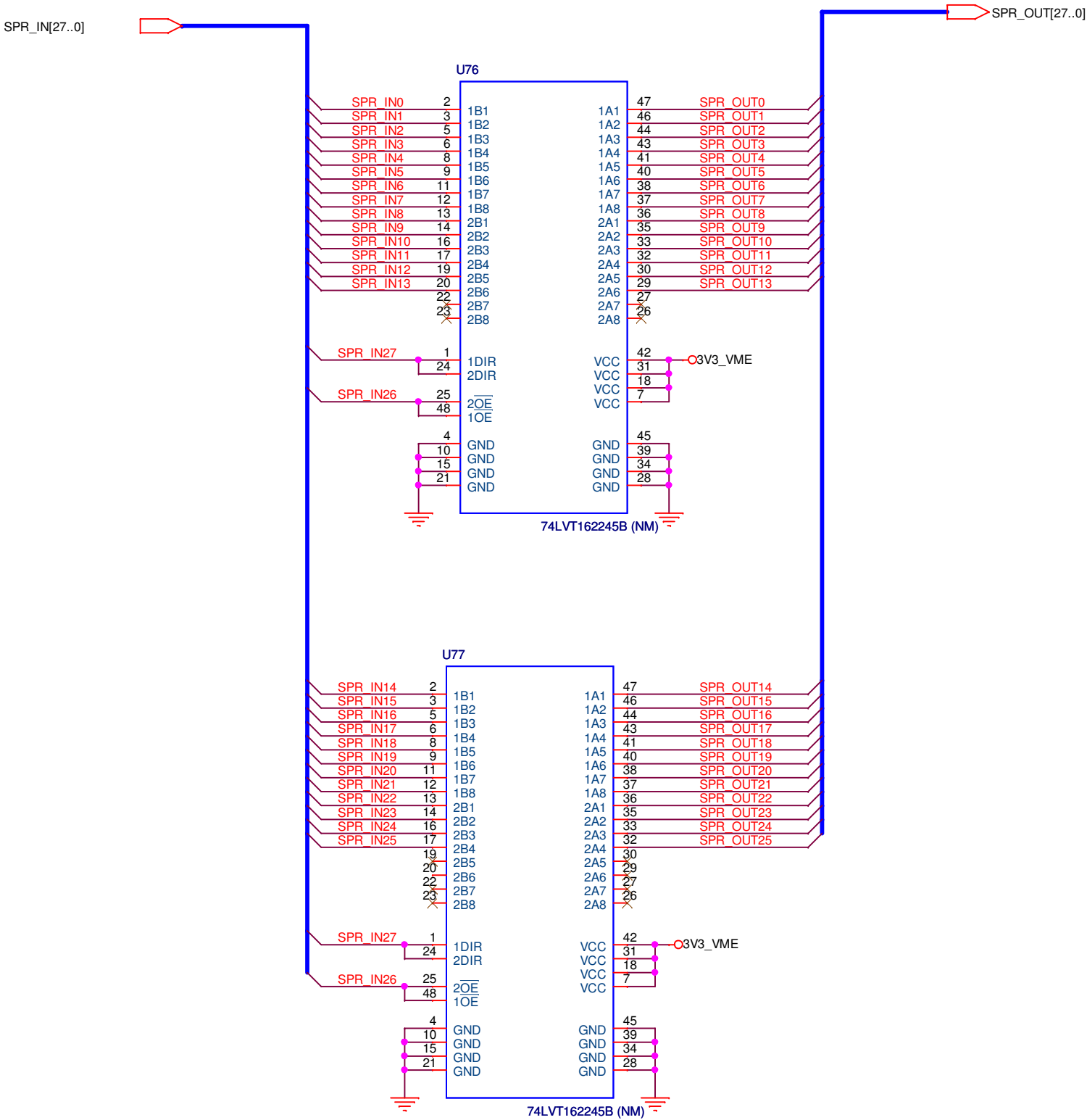
Title RD BUFFERS PAGE 2/2			
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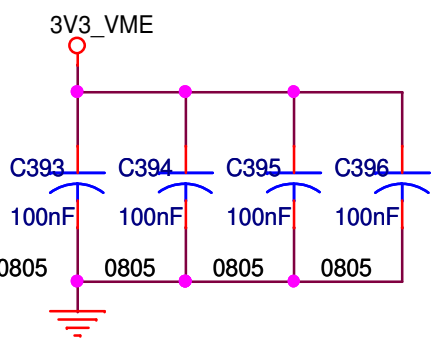
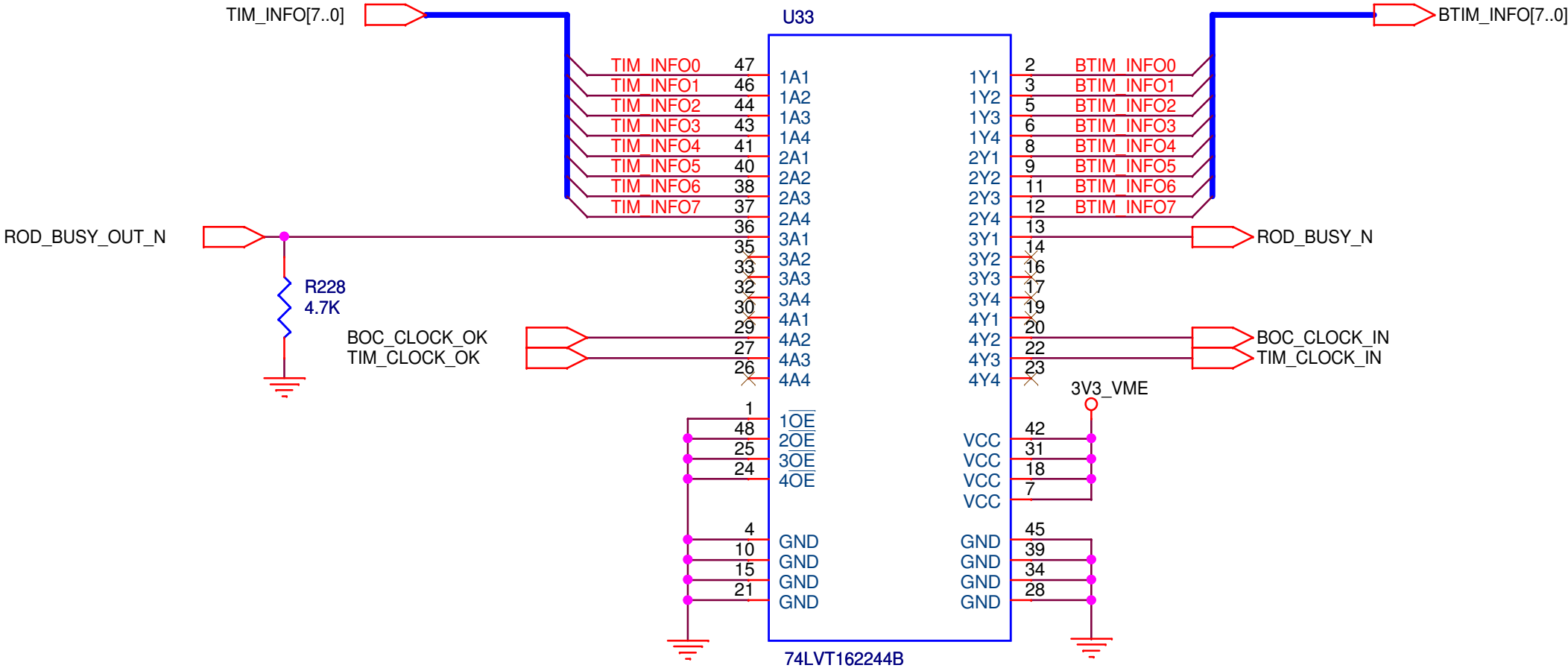


SPARE BUFFERS



Title		
SPARE BUFFERS		
Size	Document Number	Rev
A3	<Doc>	<RevC>
Date:	Monday, November 19, 2012	Sheet 15 of 59

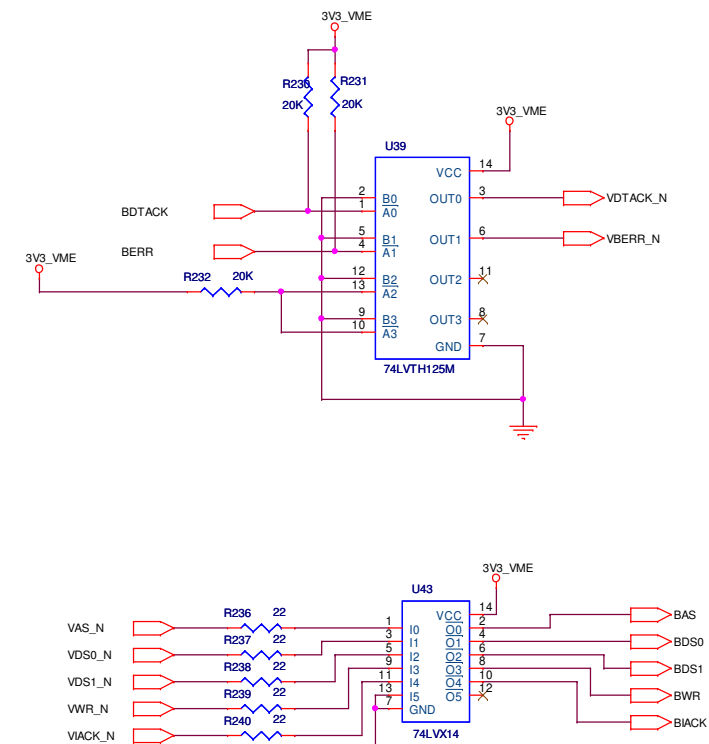
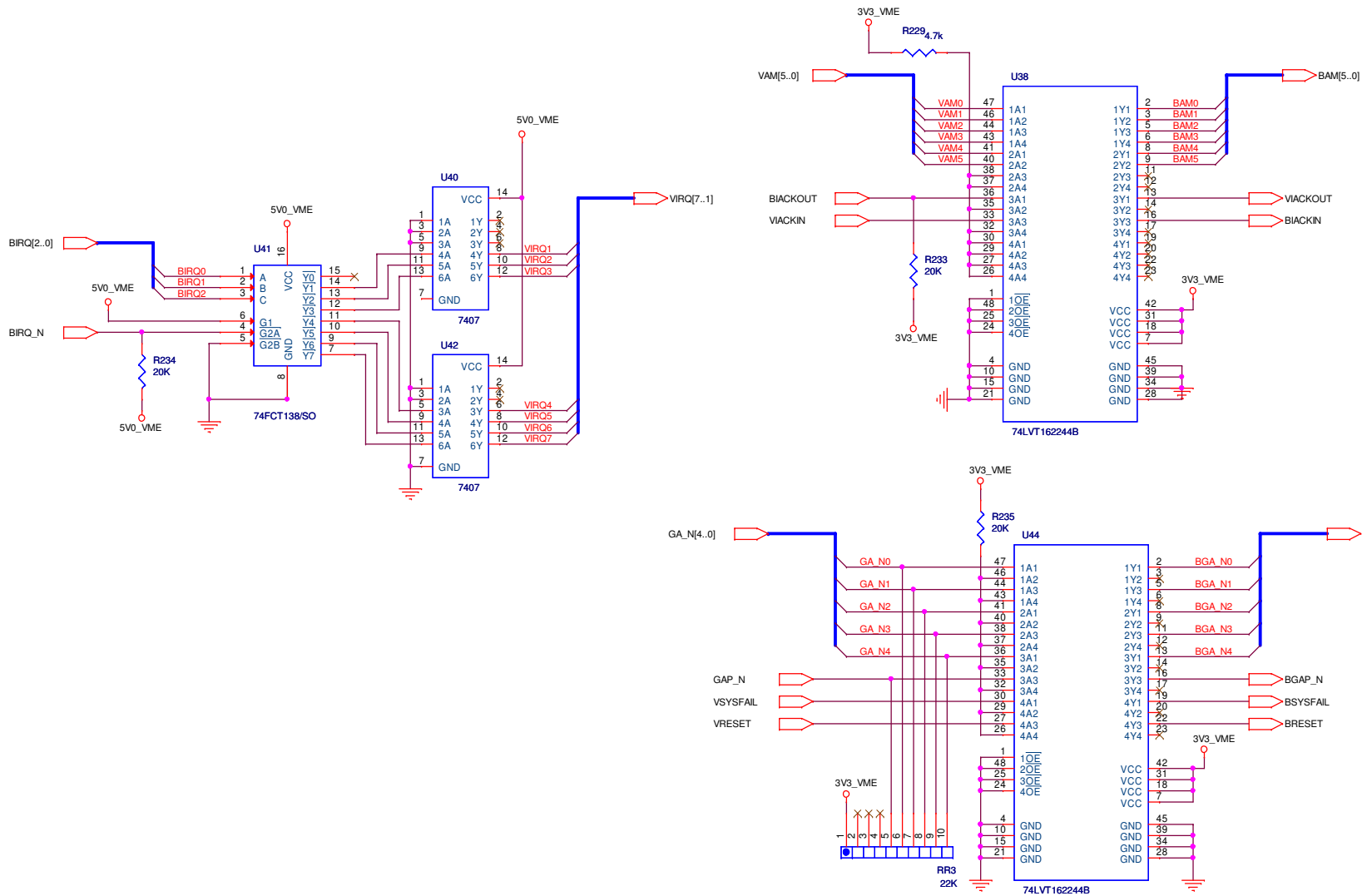
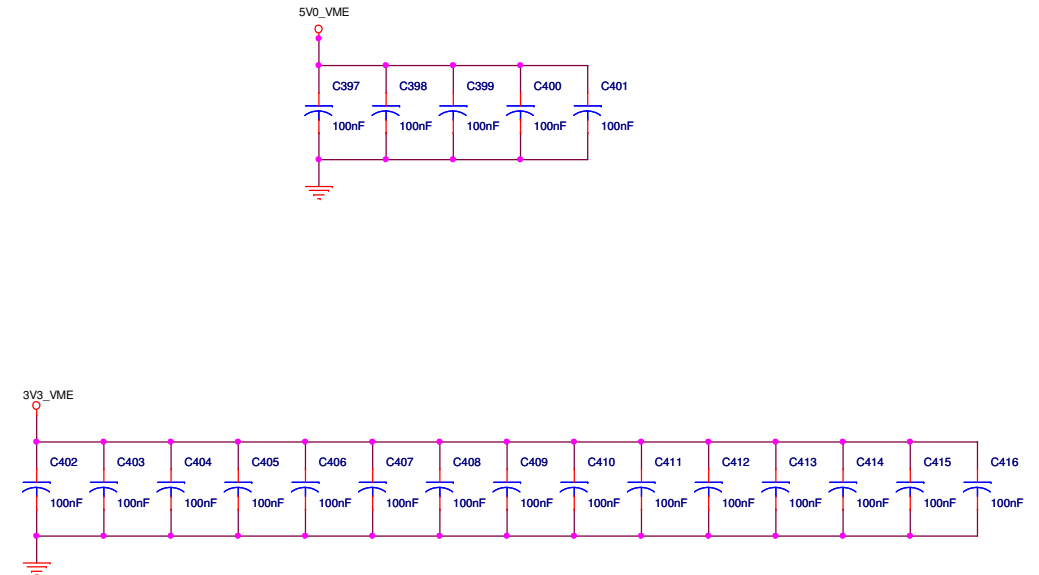
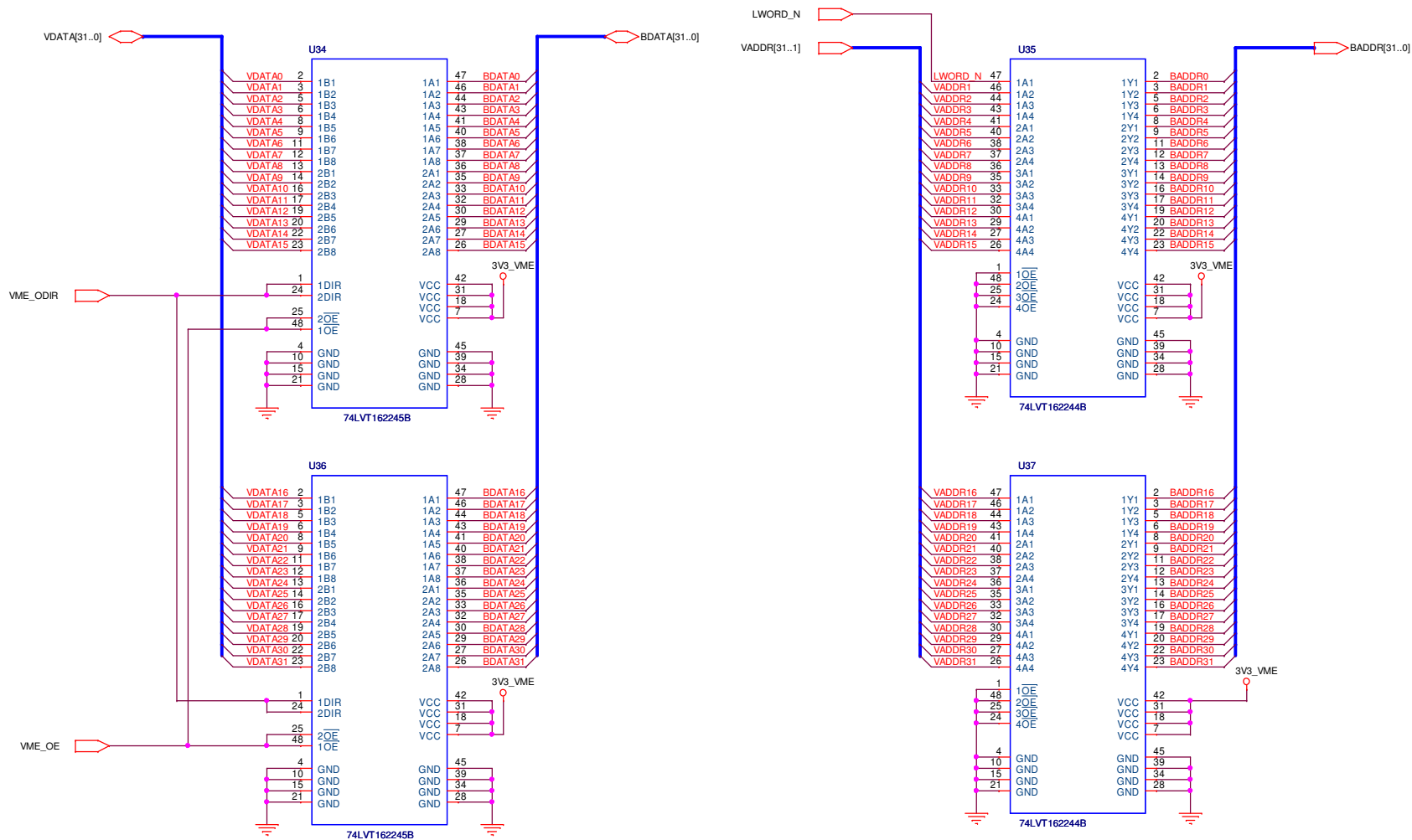
TIM BUFFER



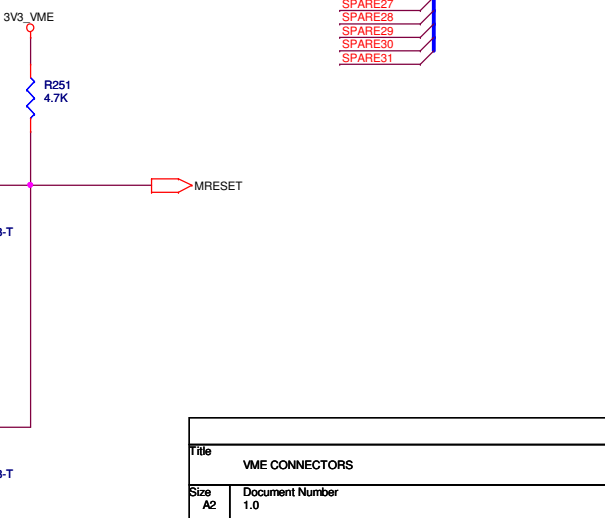
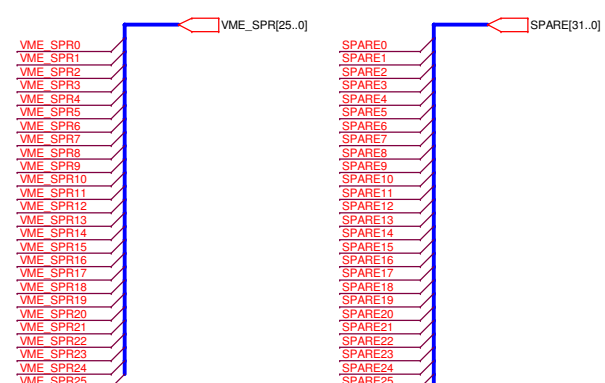
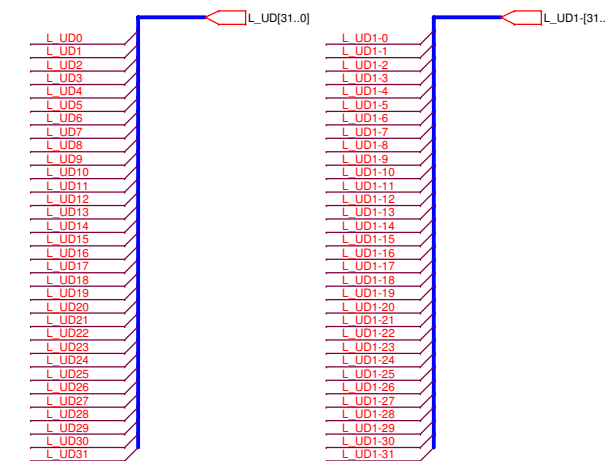
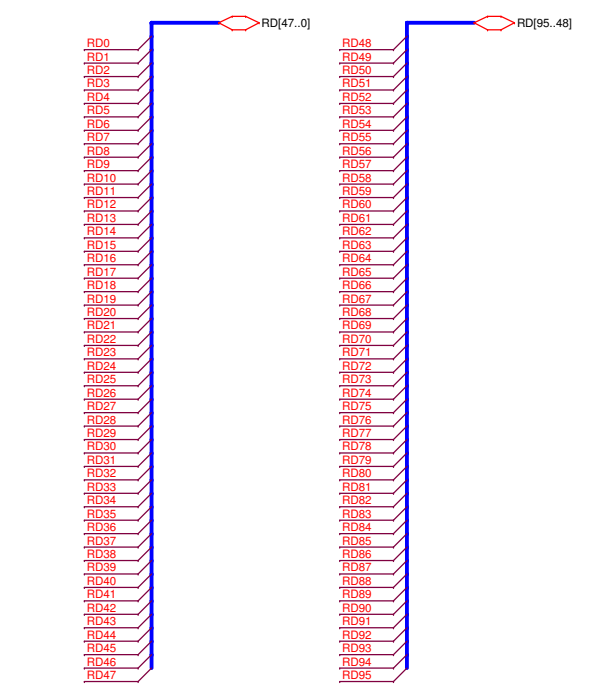
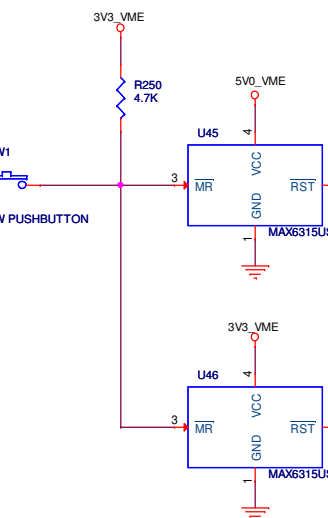
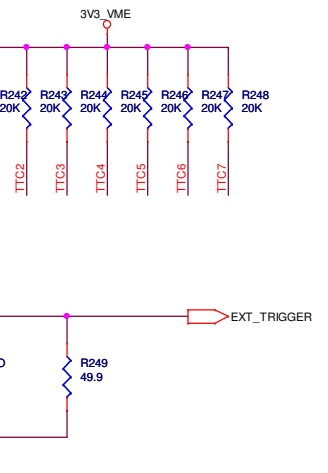
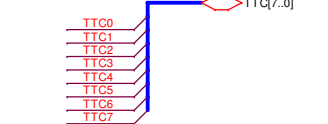
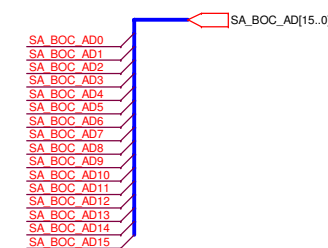
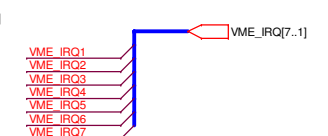
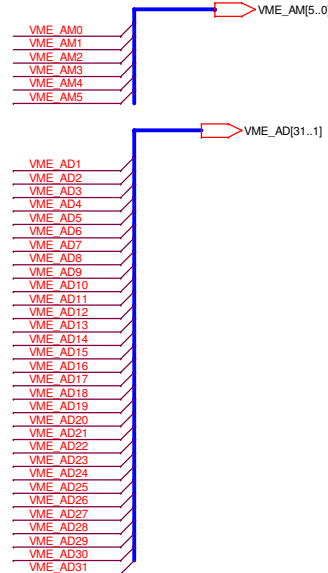
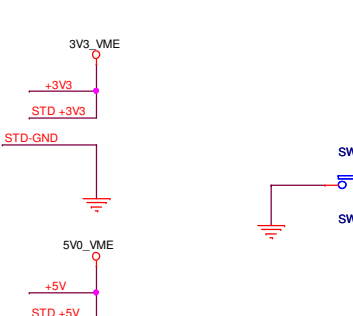
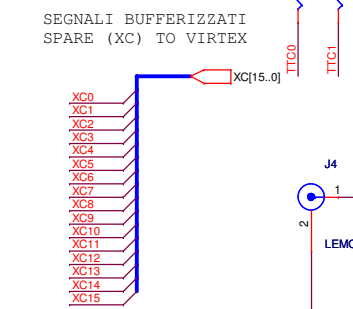
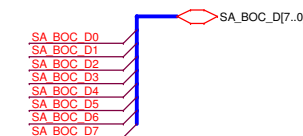
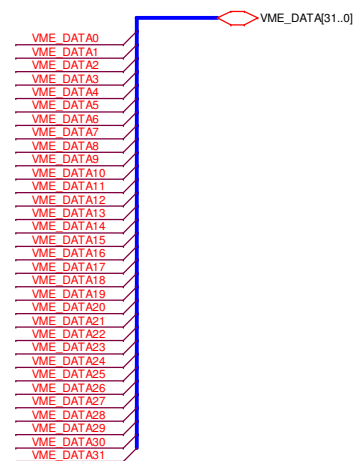
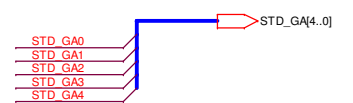
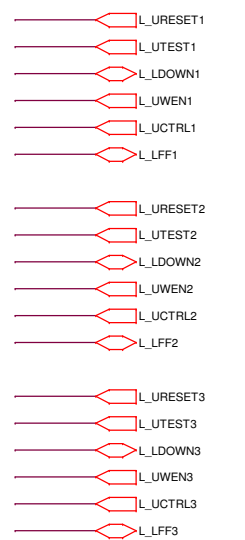
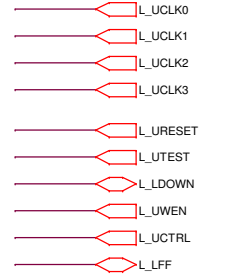
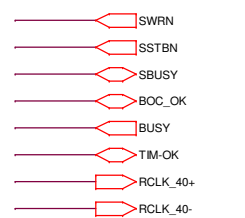
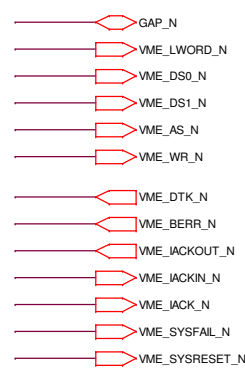
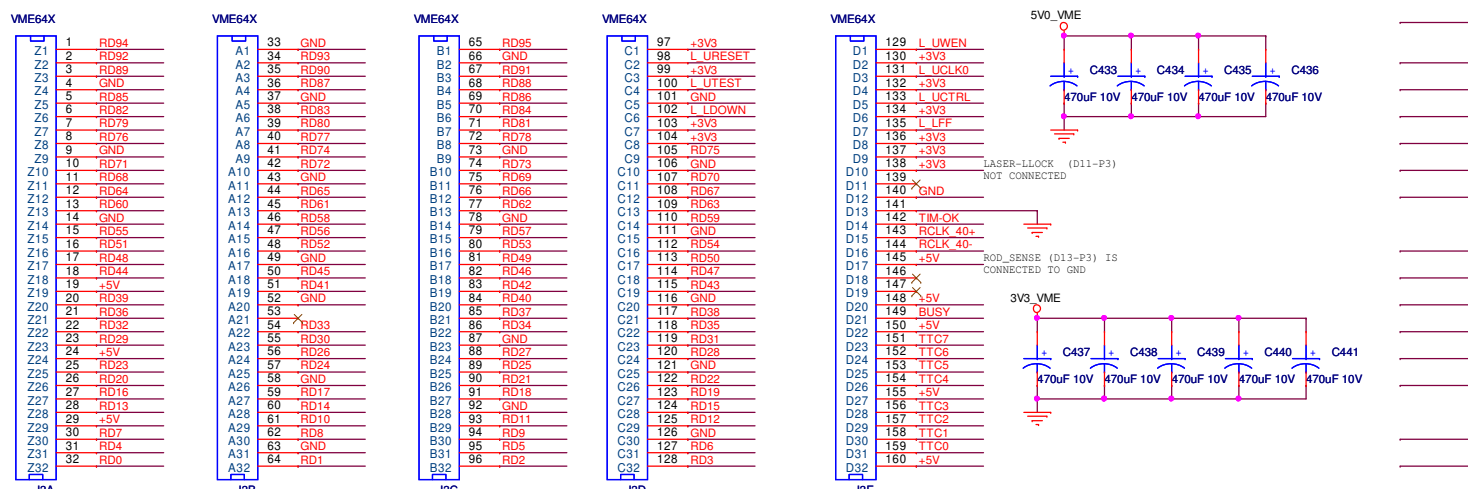
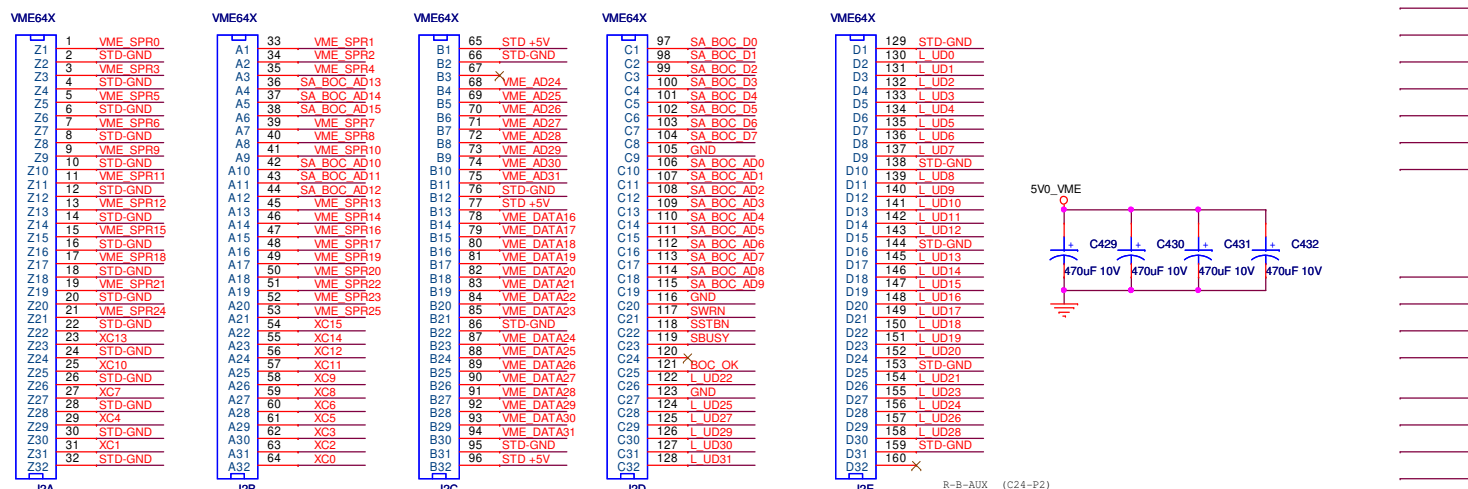
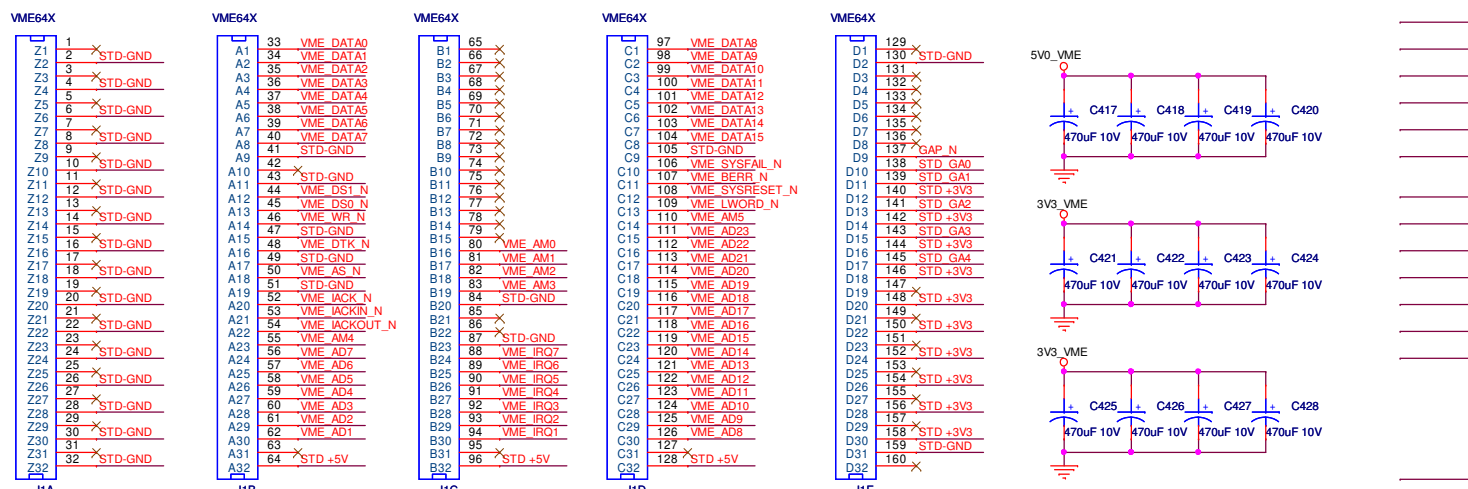
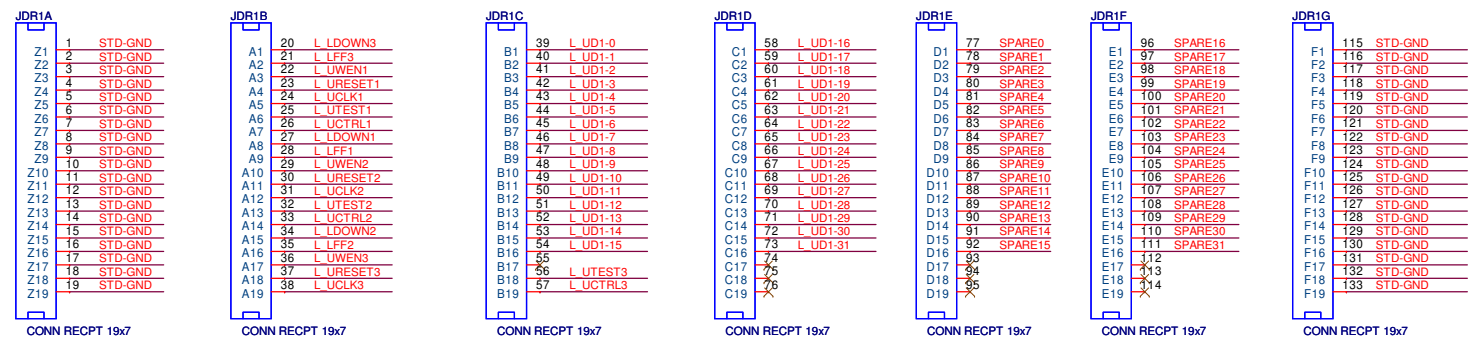
capacitors: ceramic, 0805, 5%, 100nF

Title		
TIM BUFFER		
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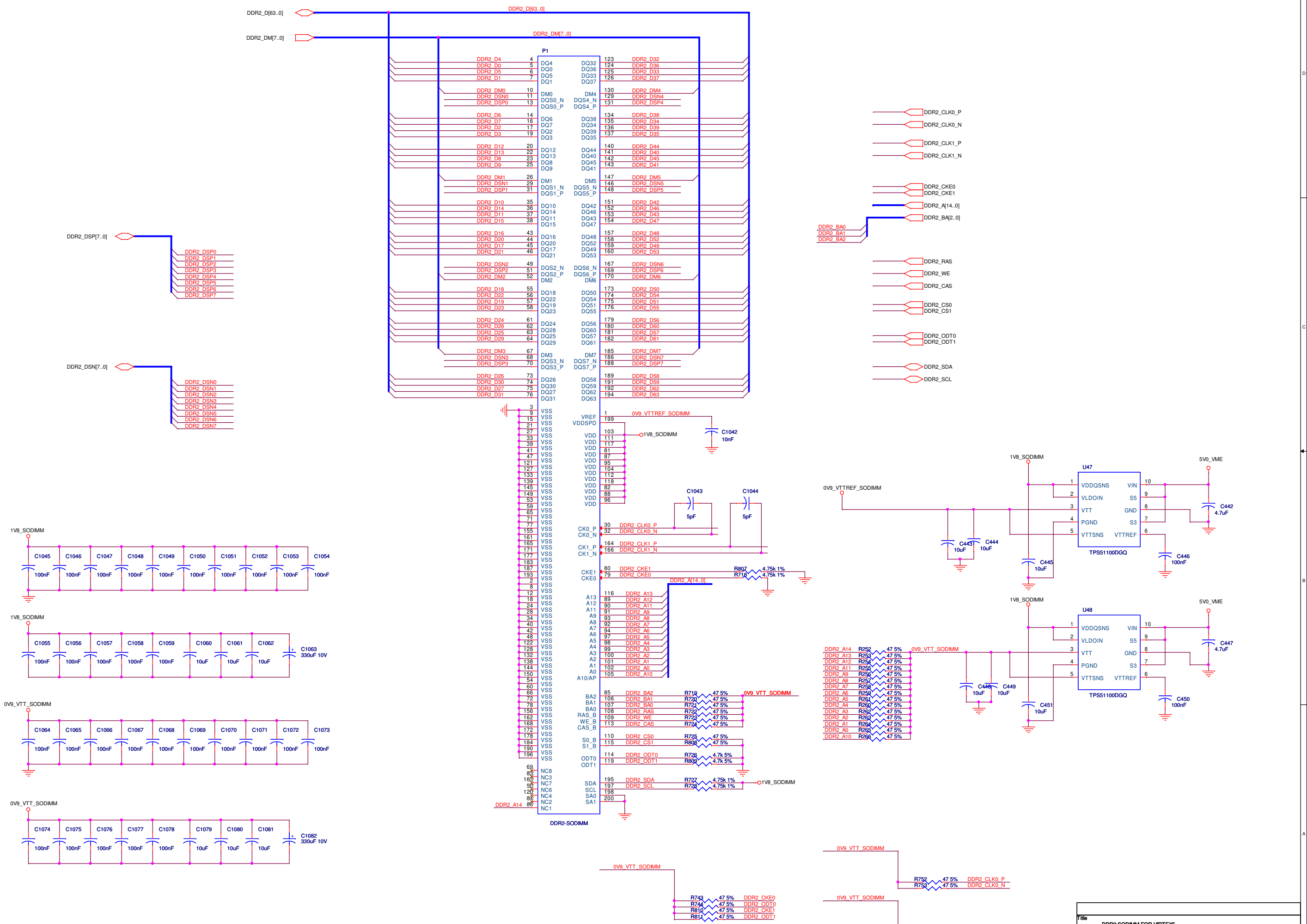
VME INTERFACE



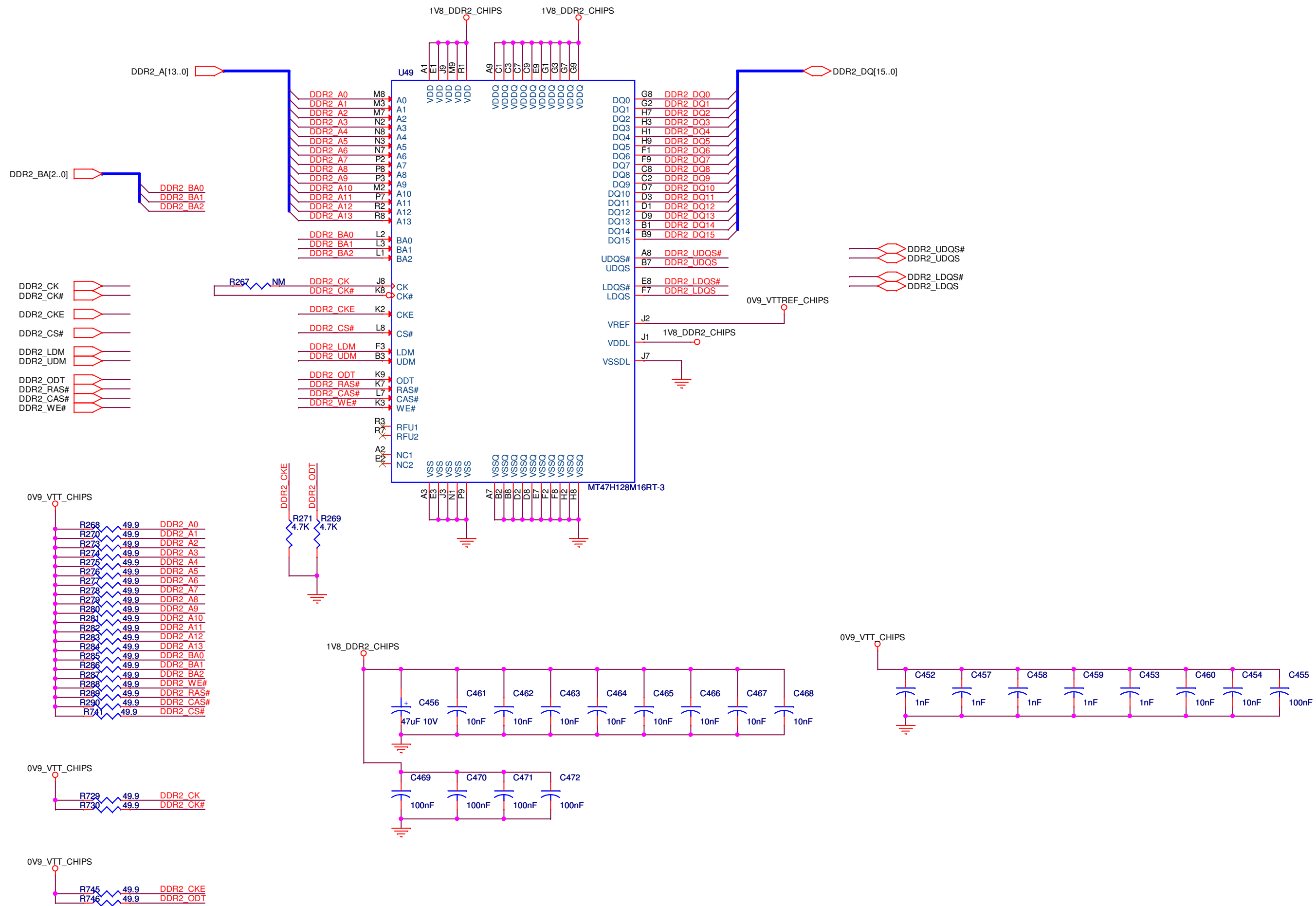
VME CONNECTORS



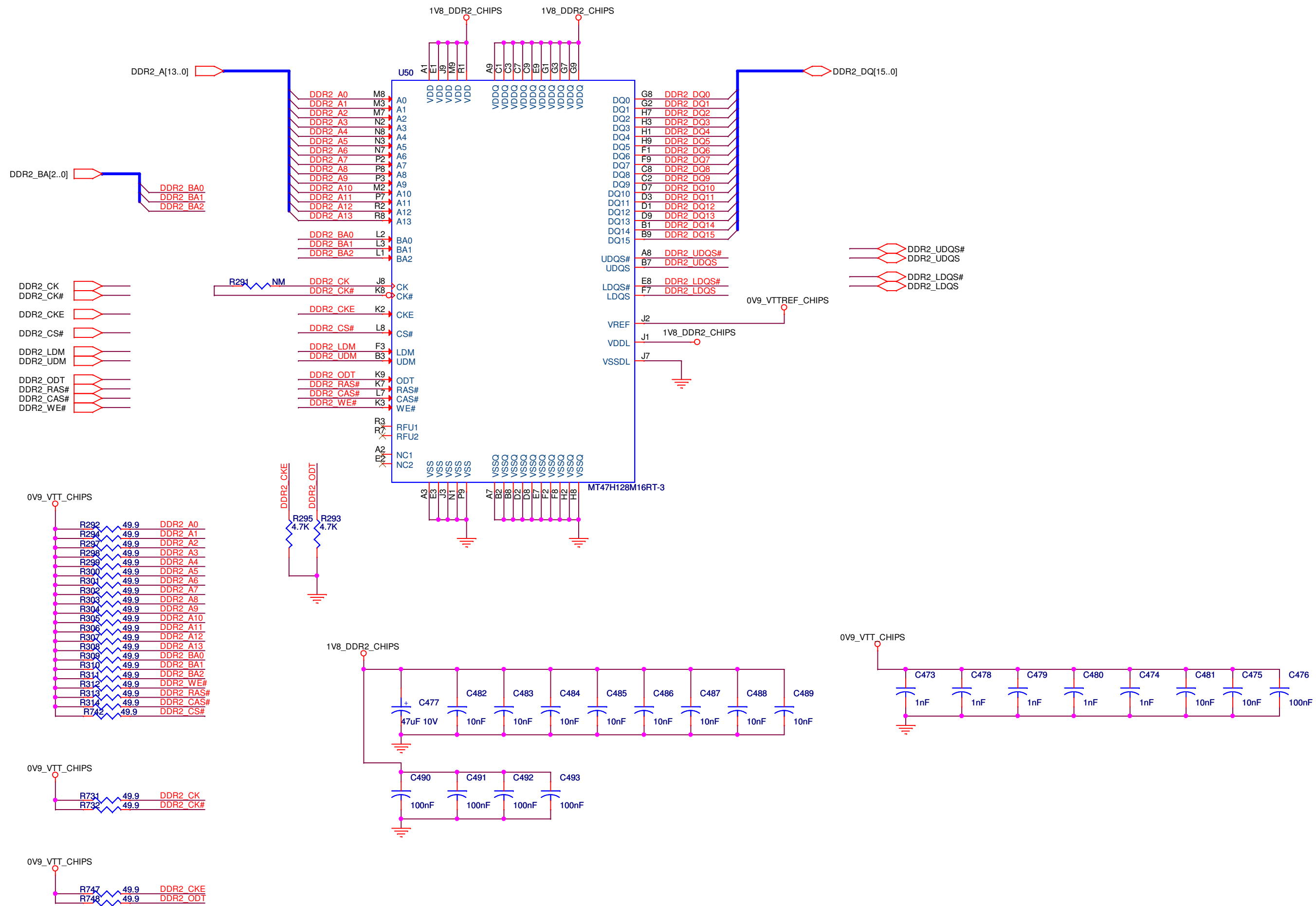
DDR2 SODIMM FOR VIRTEX5

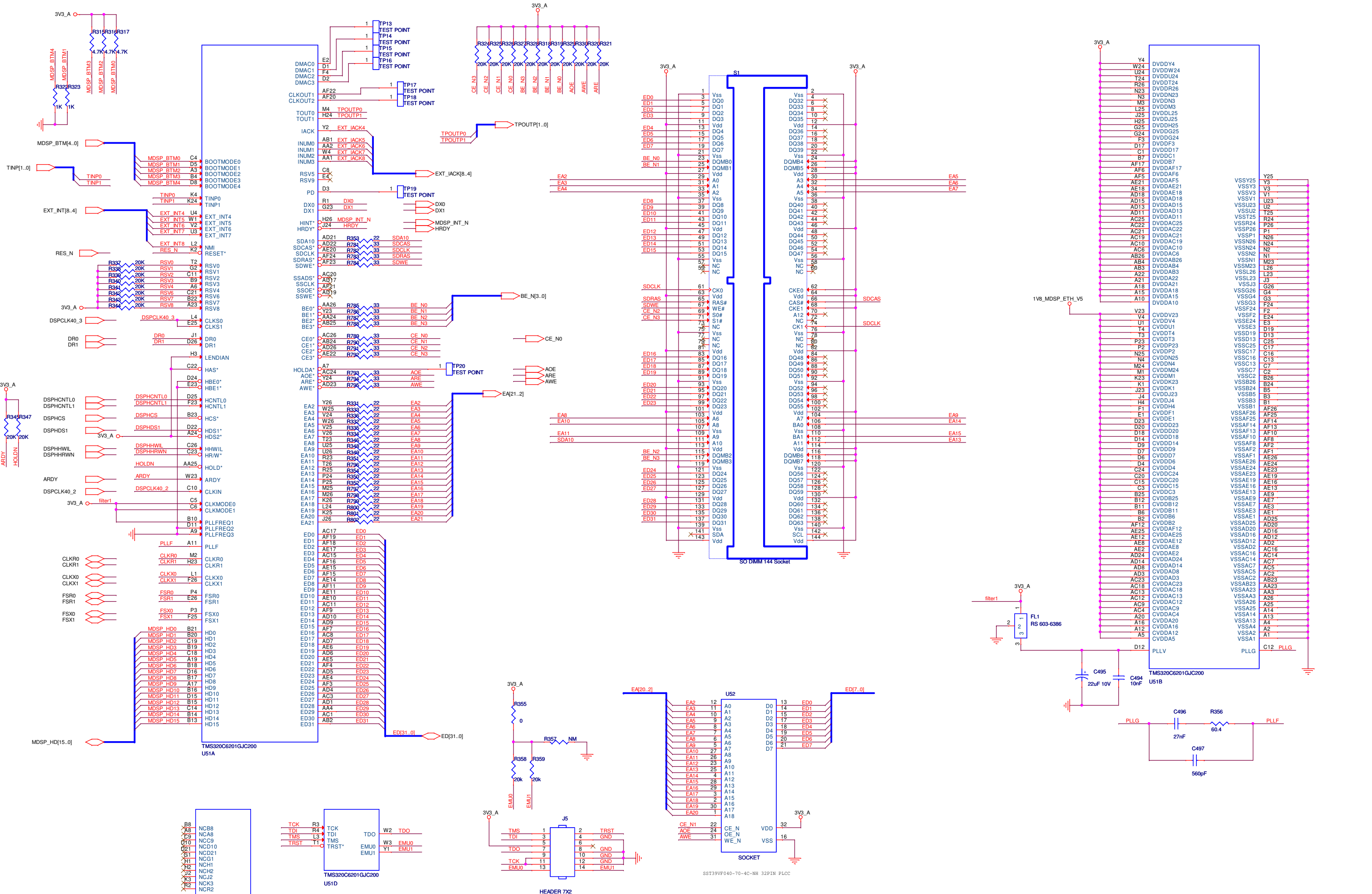


DDR2 CHIP FOR SPARTAN6



DDR2 CHIP FOR SPARTAN6



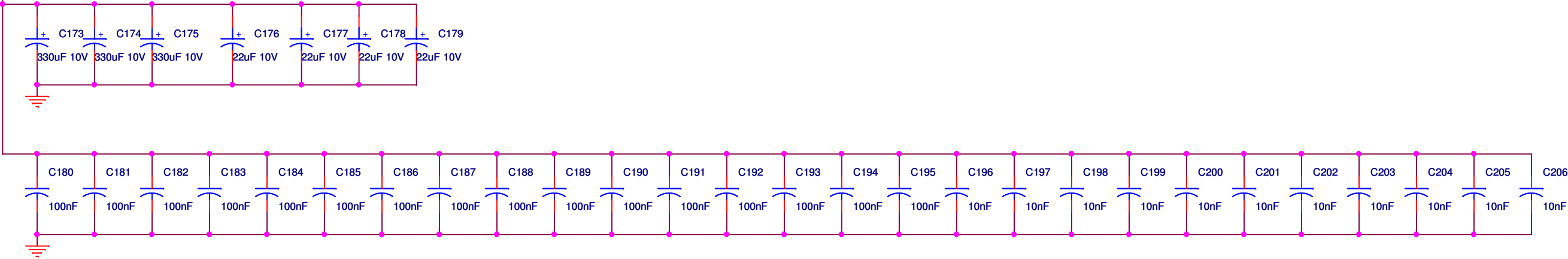


Mettere il connettore vicino al DSP

MASTER DSP PAGE 2/2

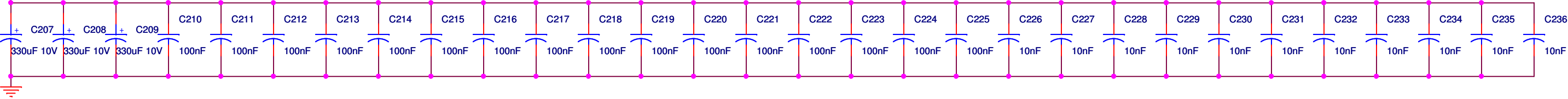
1V8_MDSP_ETH_V5

VCCINT=1.8V (DSP 6201) Ceramic Chip Capacitors 0402 0.22uF 6.3V 10% X5R



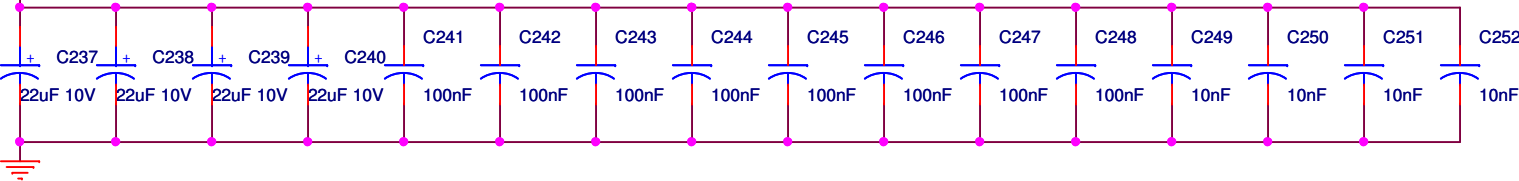
3V3_A

VCCIO=3.3V (DSP 6201) Ceramic Chip Capacitors 0402 0.22uF 6.3V 10% X5R



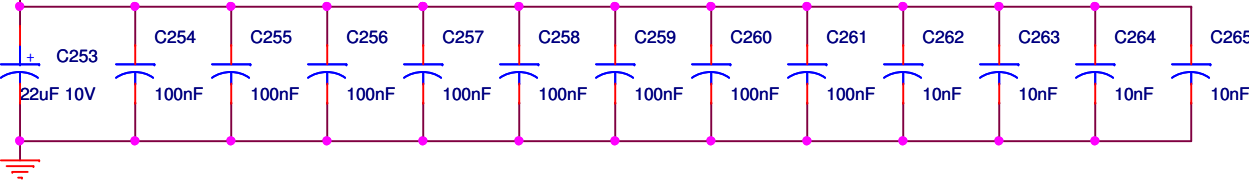
3V3_A

VCCIO=3.3V (DSP SDRAM) Ceramic Chip Capacitors 0402 0.22uF 6.3V 10% X5R

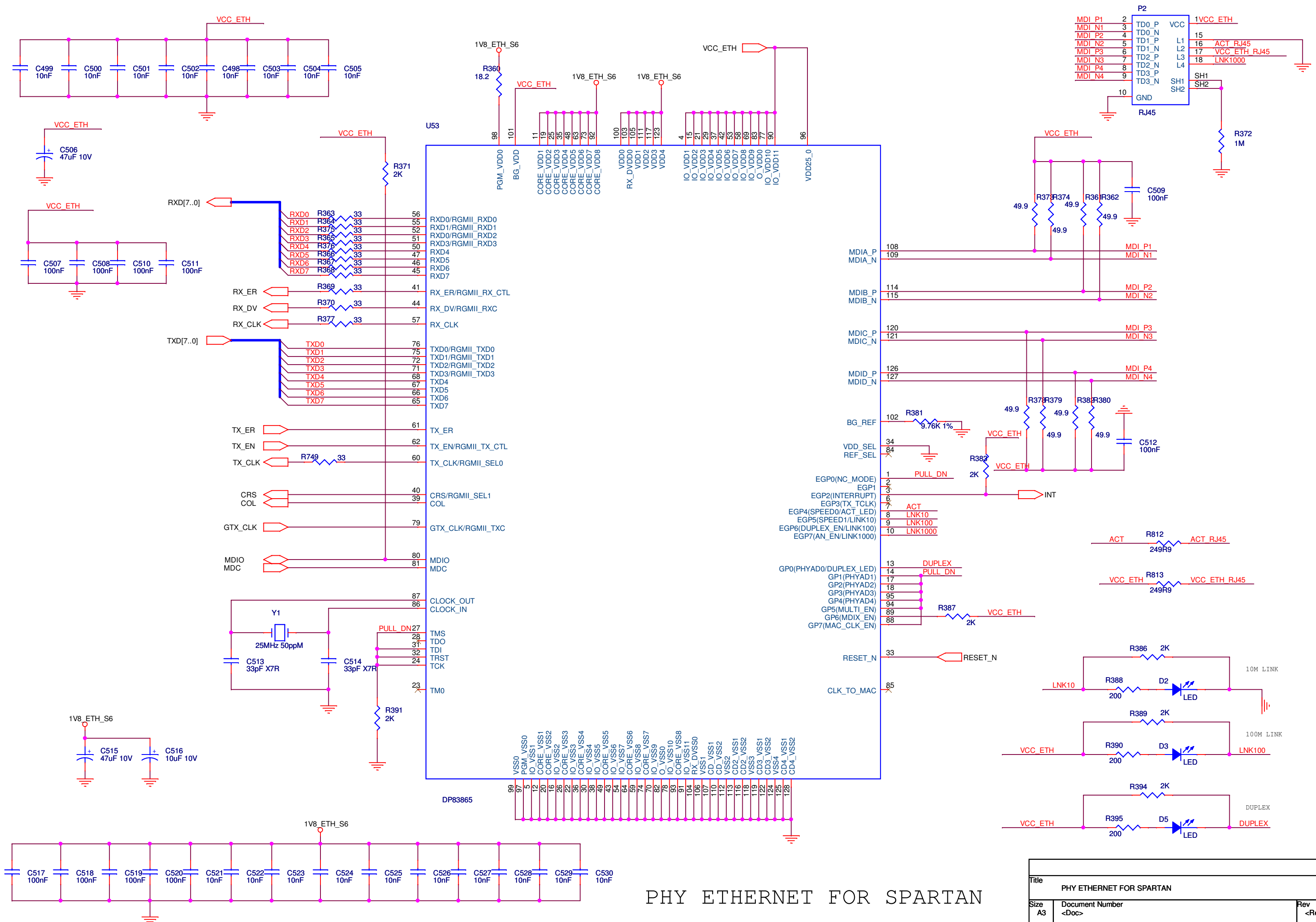


3V3_A

VCCIO=3.3V (DSP FLASH SST39VF040-70-4C-NH) Ceramic Chip Capacitors 0402 0.22uF 6.3V 10% X5R

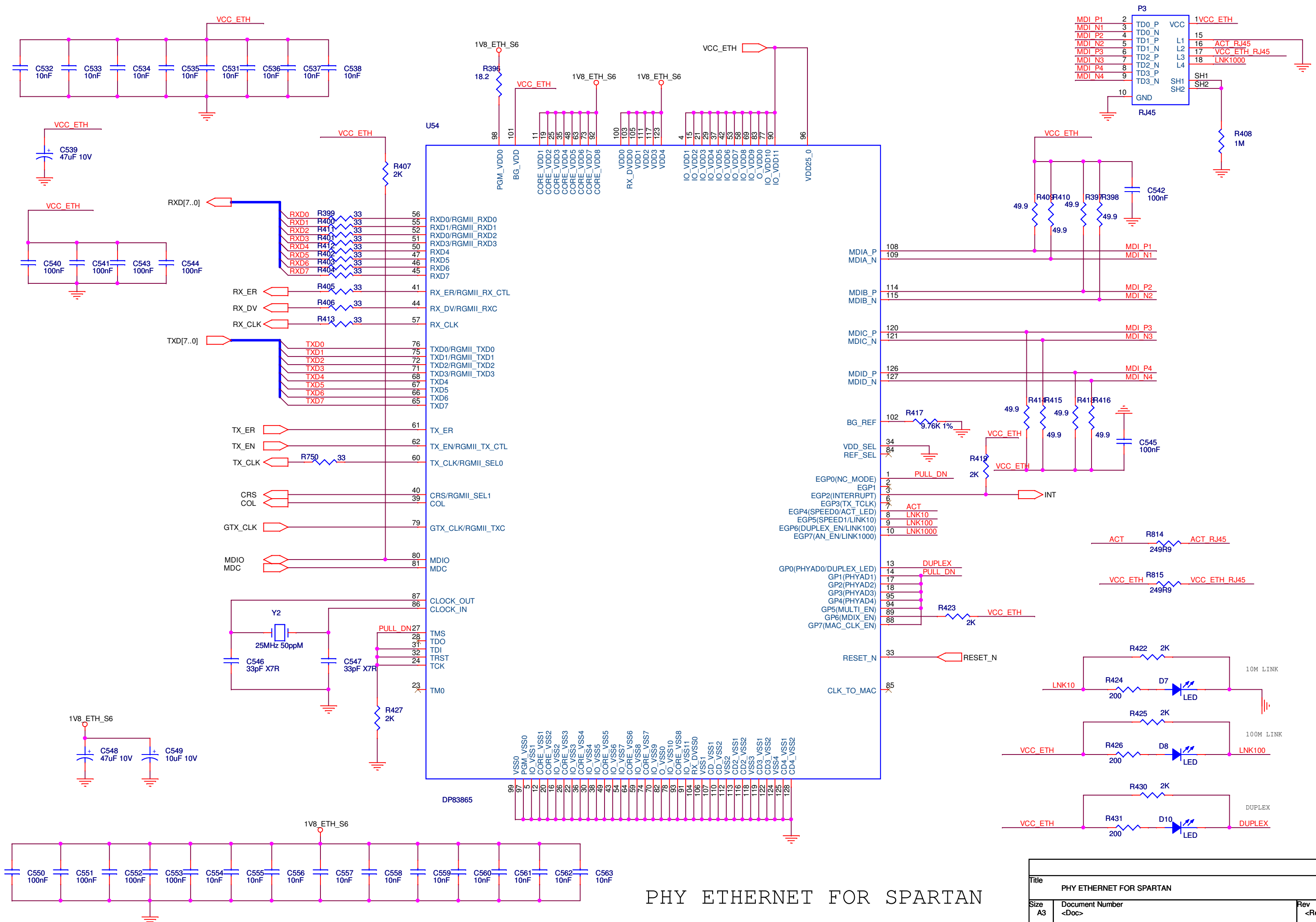


Title		
MASTER DSP PAGE 2/2		
Size	Document Number	Rev
A3	<Doc>	<RevC>
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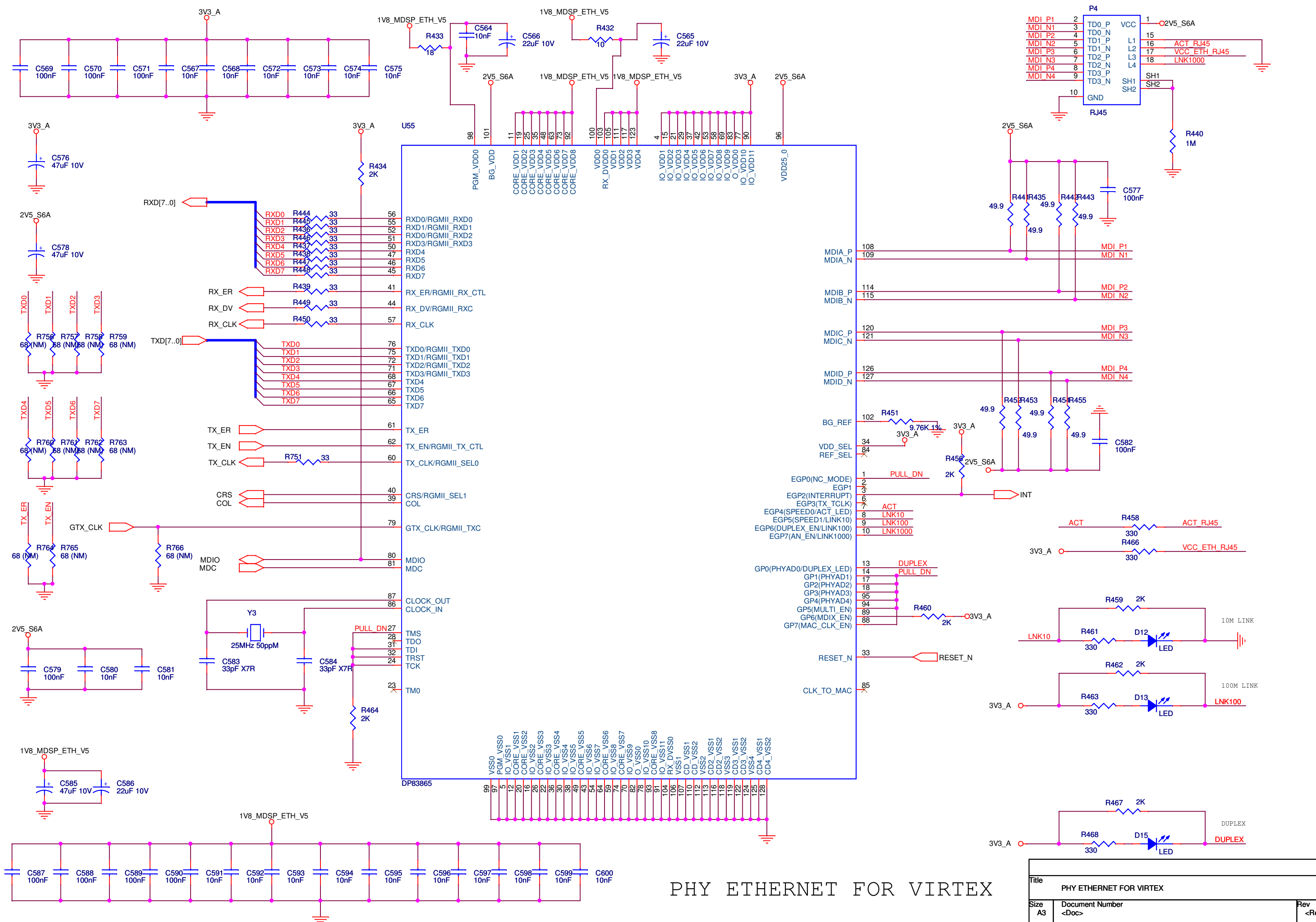
PHY ETHERNET FOR SPARTAN

Title				
PHY ETHERNET FOR SPARTAN				
Size	Document Number			Rev
A3	<Doc>			<R
Date:	Thursdav. November 29, 2012	Sheet	24	of 59



PHY ETHERNET FOR SPARTAN

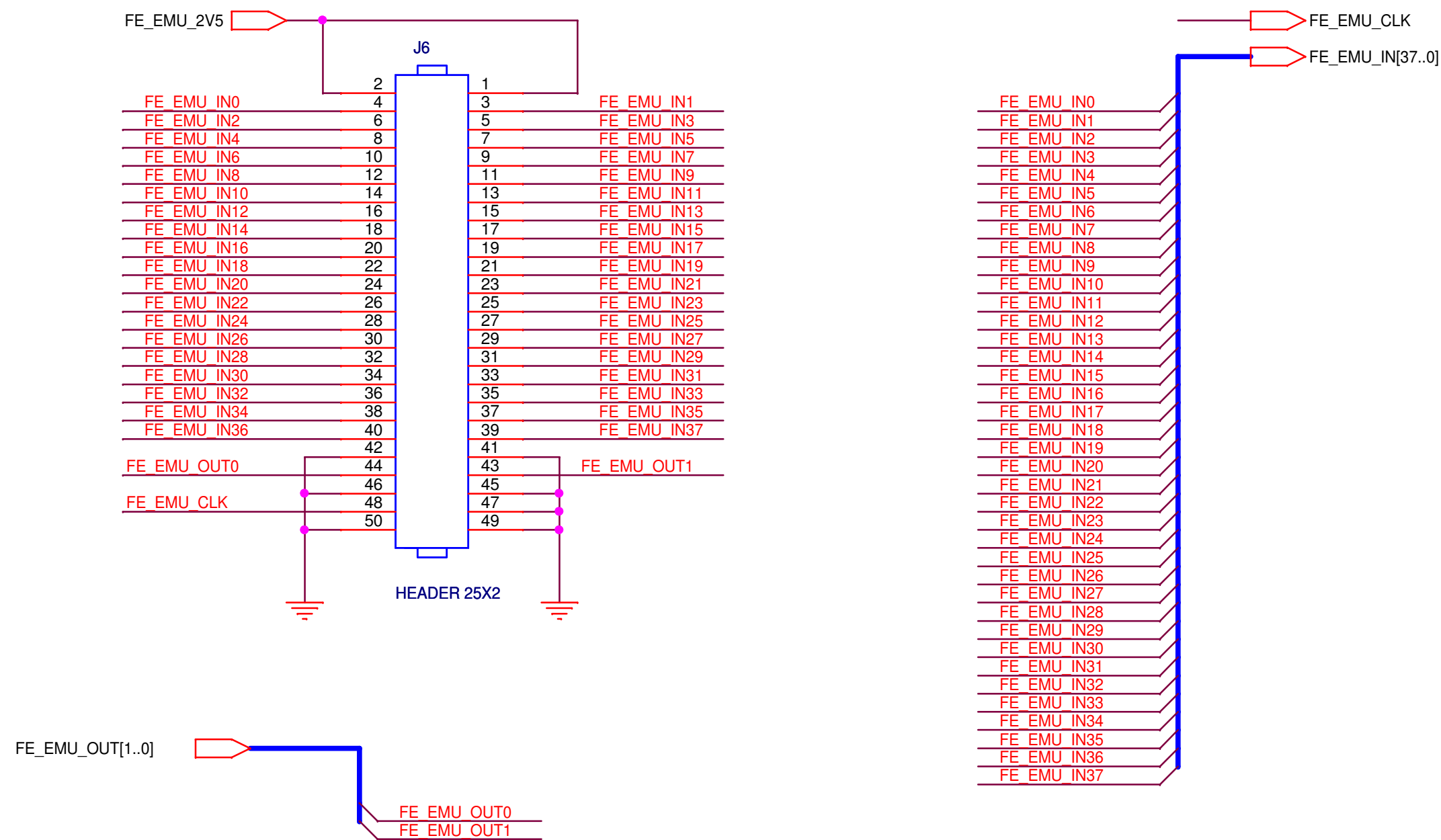
Title			PHY ETHERNET FOR SPARTAN
Size	A3	Document Number	<Doc>
Date:	Thursday, November 29, 2012	Sheet	25 of 59



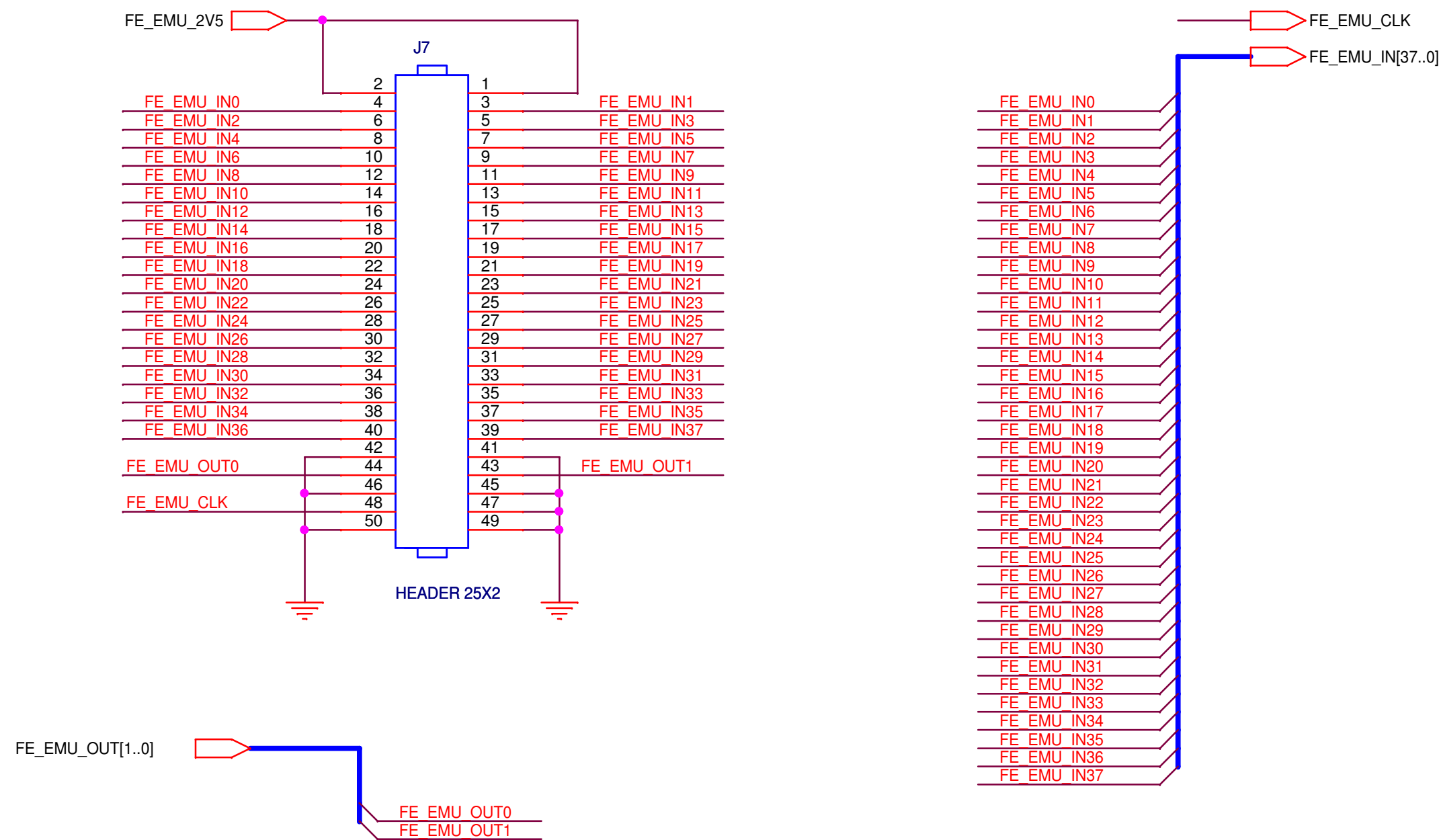
PHY ETHERNET FOR VIRTEX

Title				
PHY ETHERNET FOR VIRTEX				
Size	Document Number			Rev
A3	<Doc>			<Rev>
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FE EMULATOR CONNECTOR

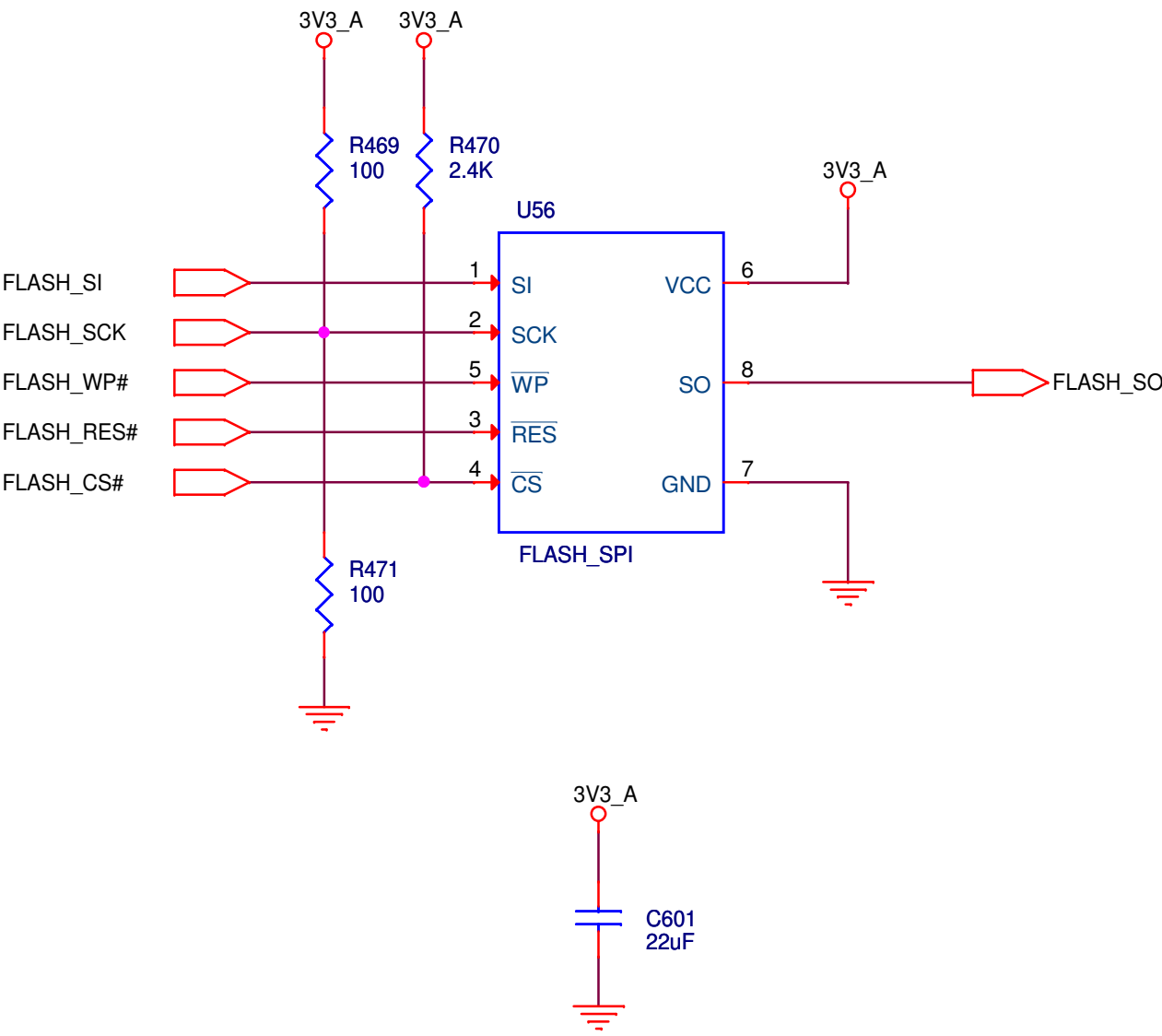


FE EMULATOR CONNECTOR



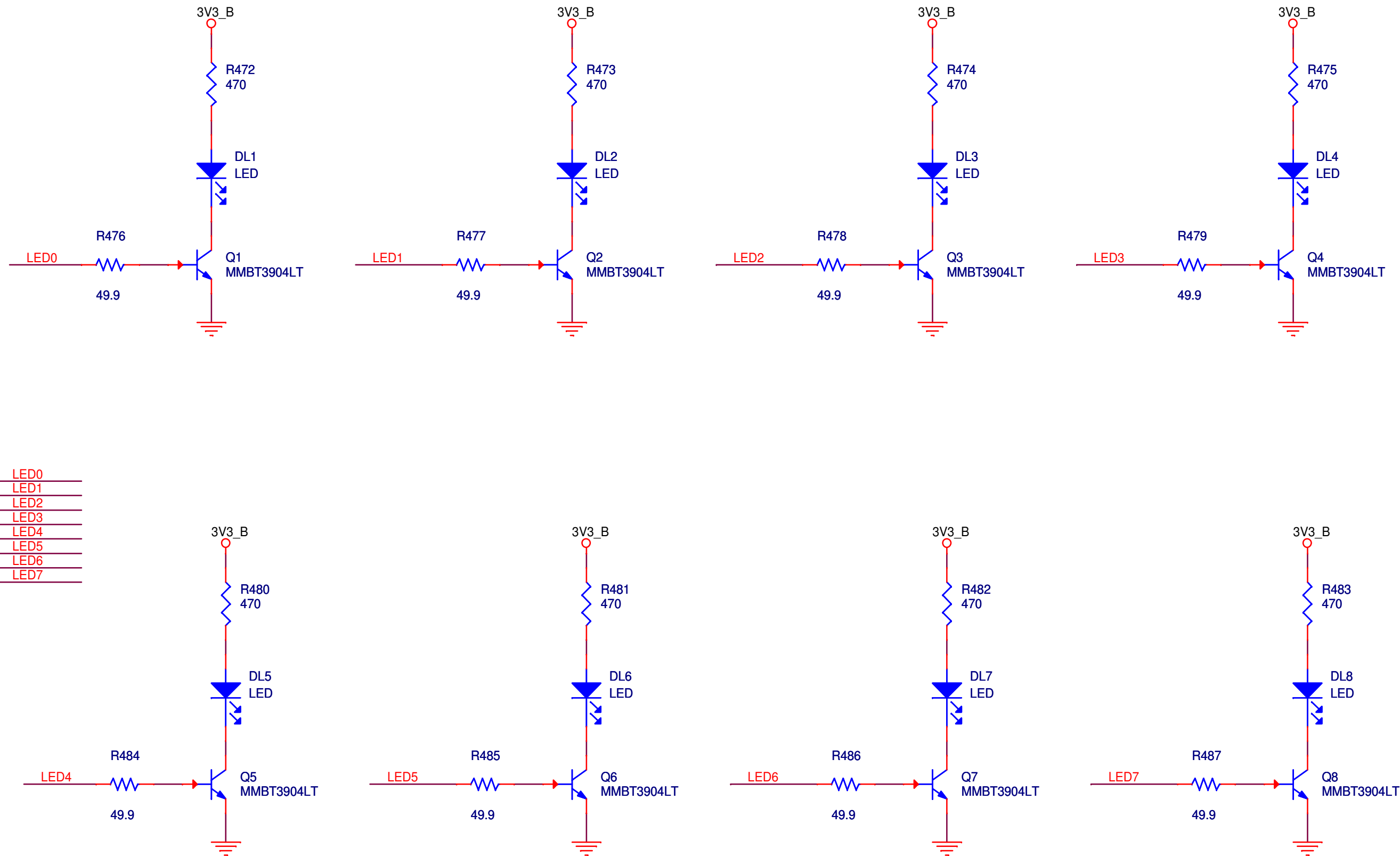
Title		
FE EMULATOR CONNECTOR		
Size	Document Number	Rev
A4	<Doc>	<RevCoc>
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SPI FLASH FOR THE POWERPC



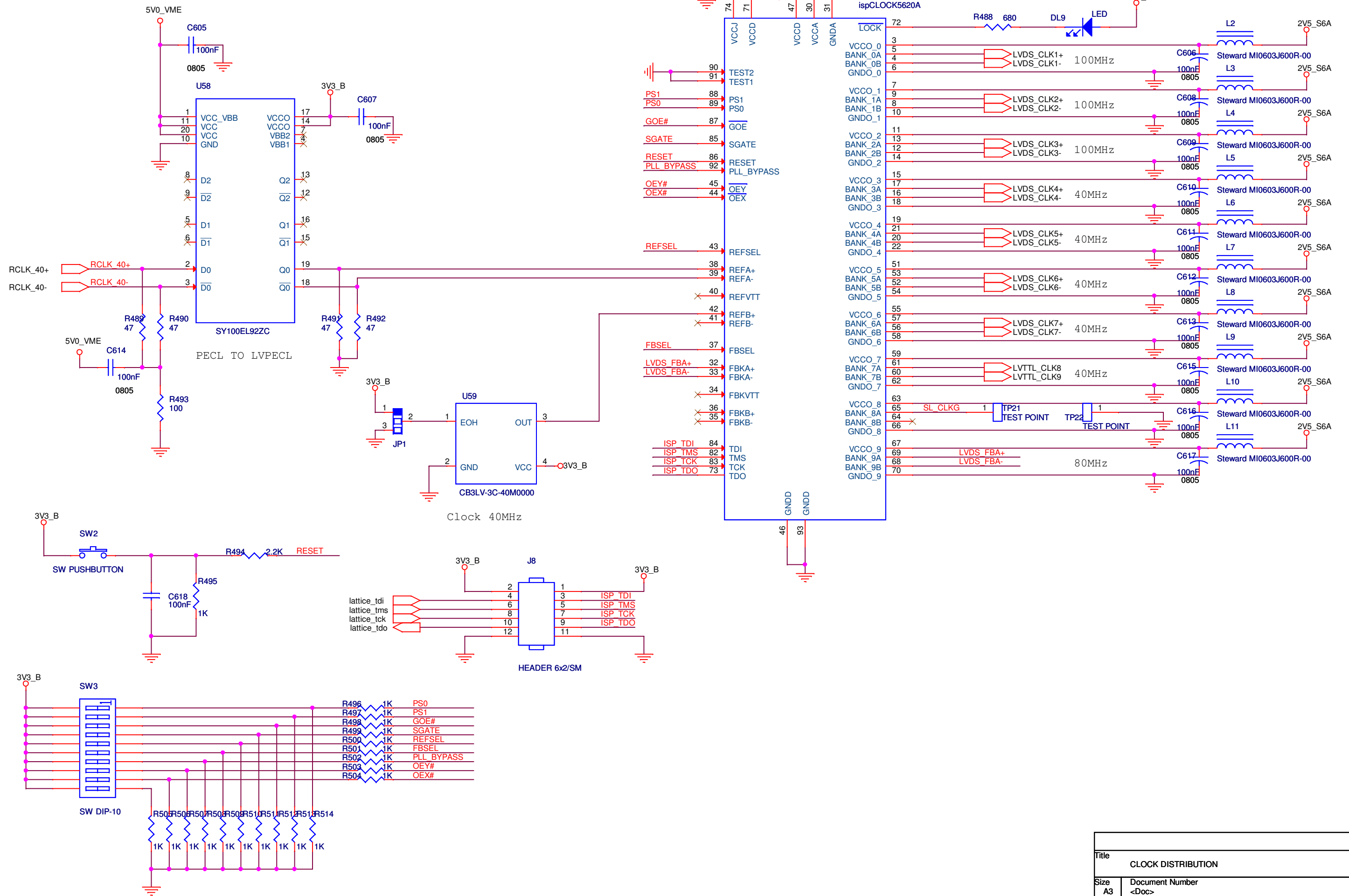
Title		
SPI FLASH FOR THE POWERPC		
Size	Document Number	Rev
A4	<Doc>	<RevCoc>
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LEDS ON THE FRONT PANEL

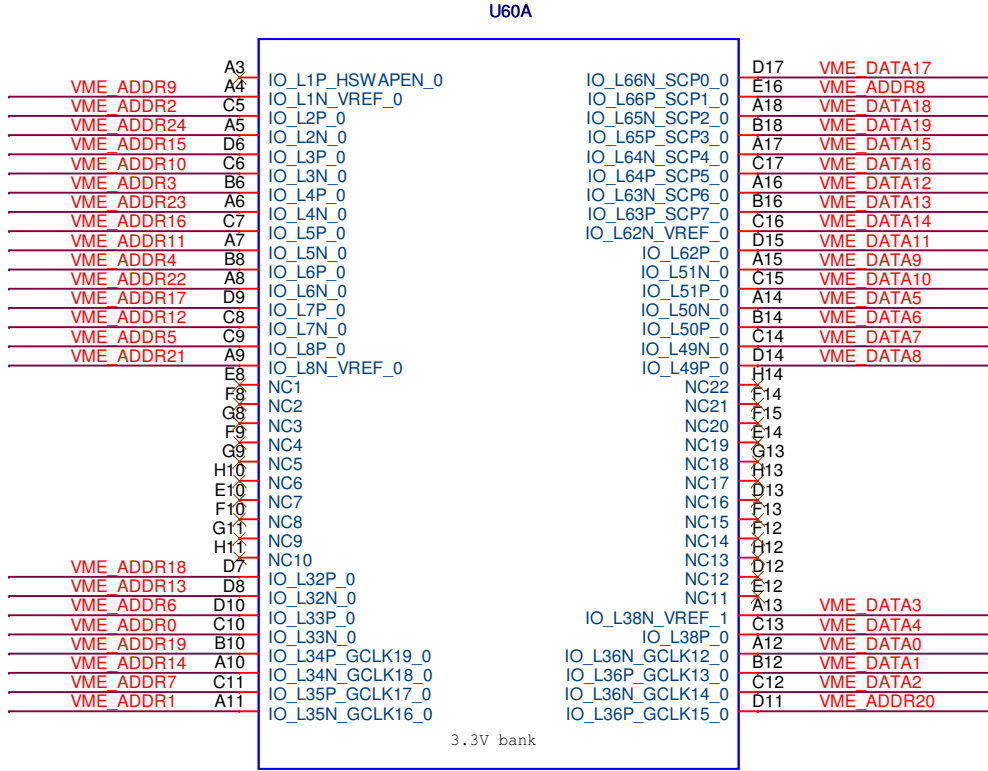


Title		
LEDS ON THE FRONT PANEL		
Size	Document Number	Rev
A4	<Doc>	1
Date:	Monday, November 19, 2012	Sheet 30 of 59

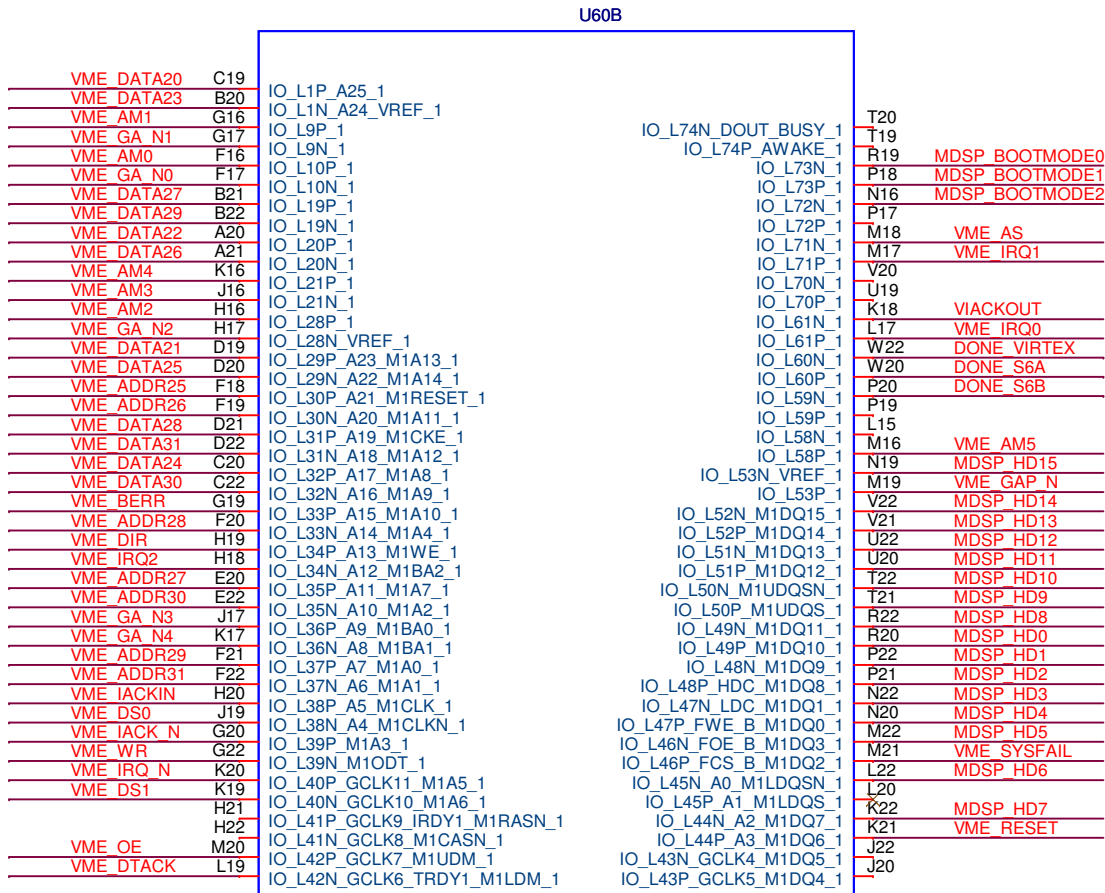
CLOCK DISTRIBUTION



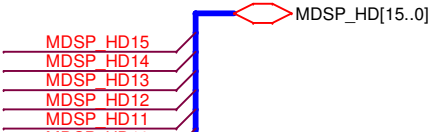
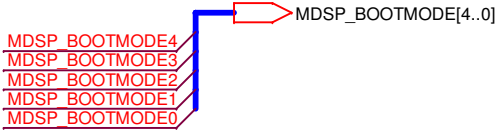
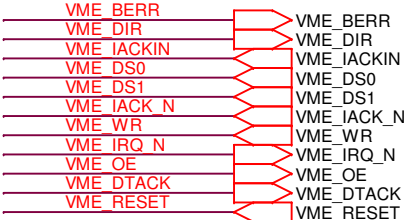
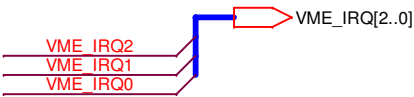
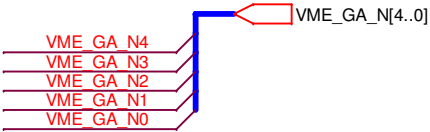
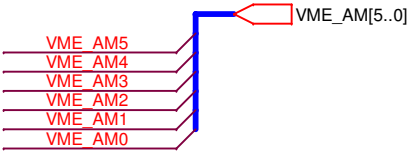
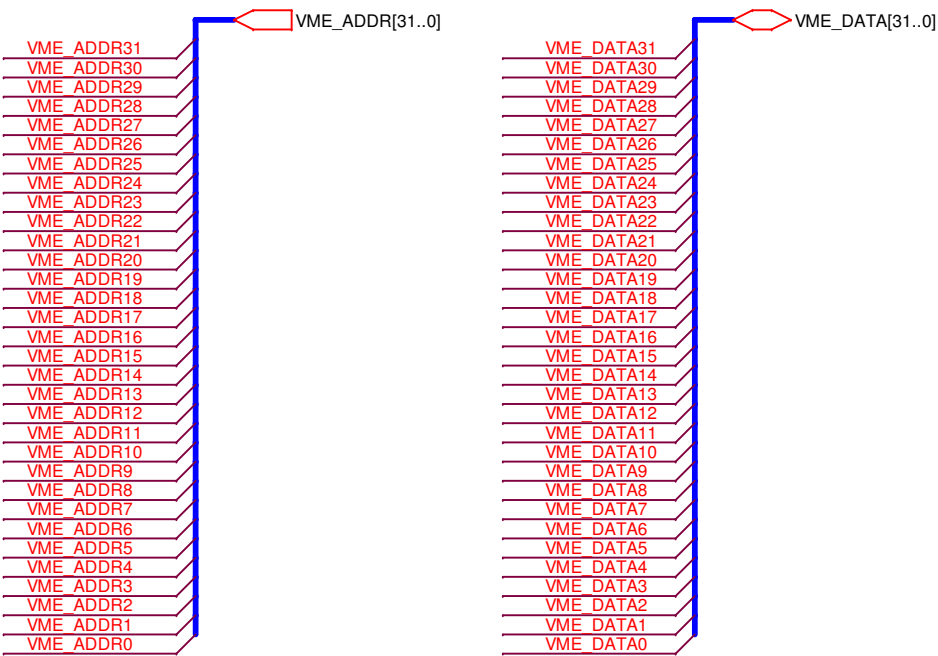
Title CLOCK DISTRIBUTION			
Size A3	Document Number <Doc>		Rev <Rev>
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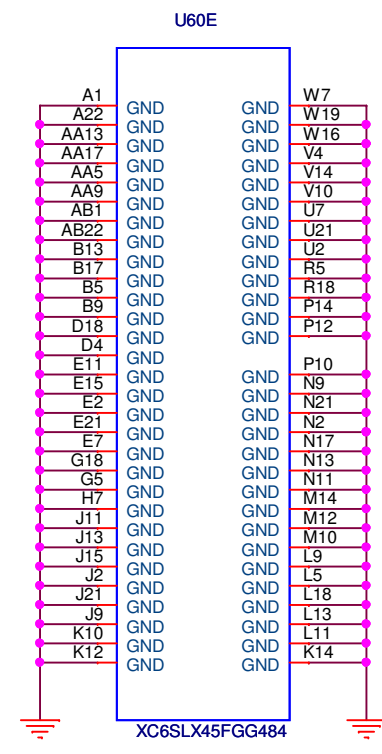
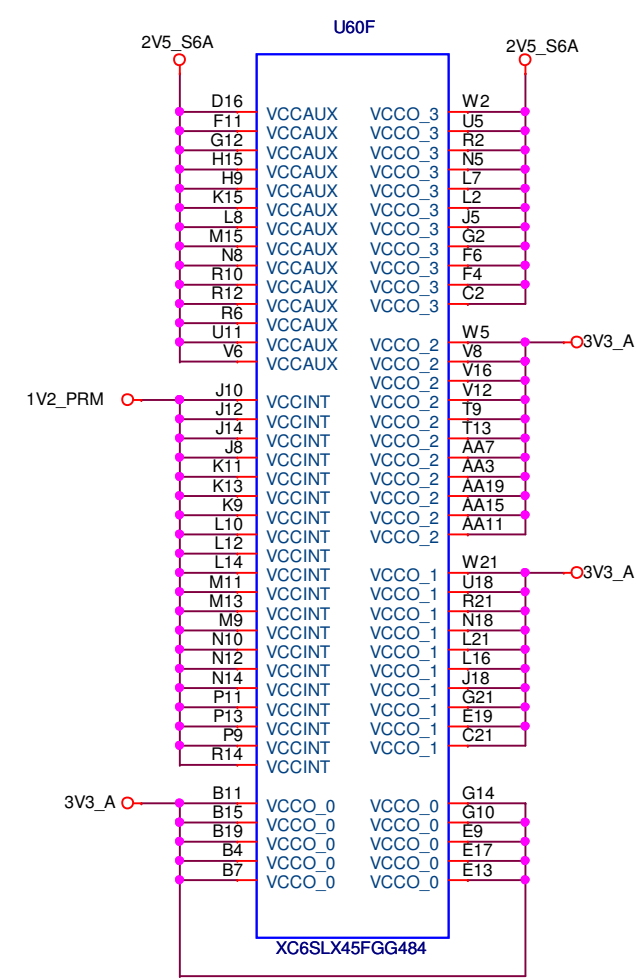


XC6SLX45FGG484

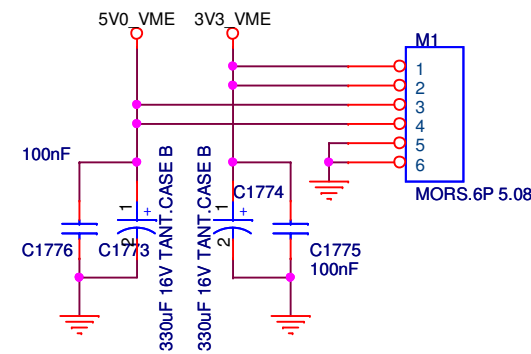


XC6SLX45FGG484

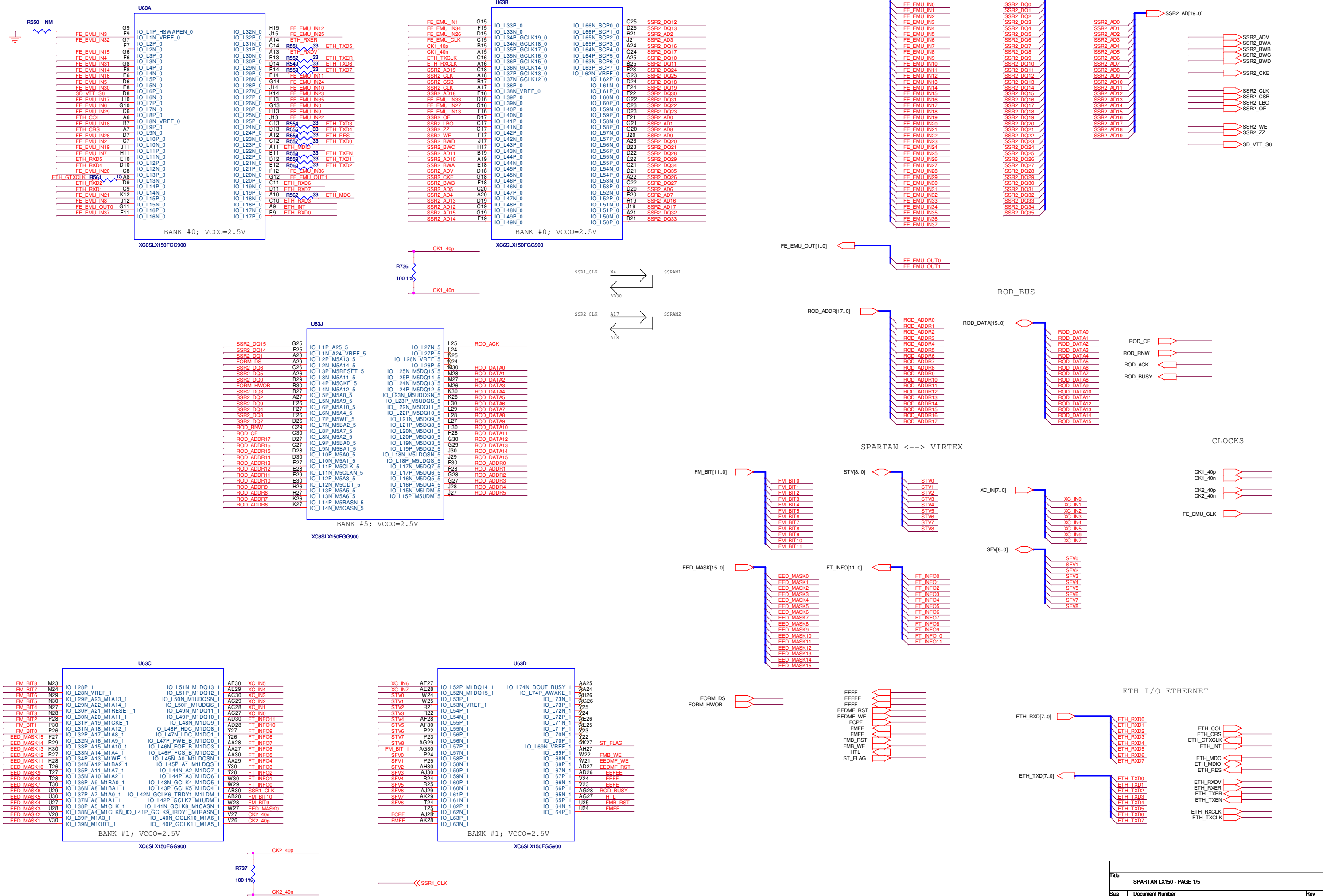




POWER SUPPLY

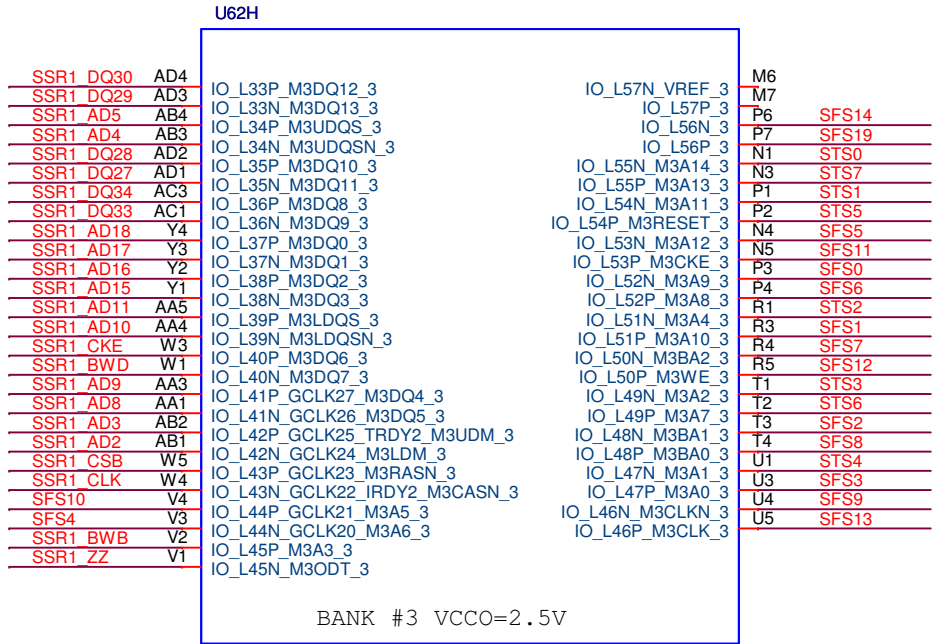


5V and 3.3V power supply shall
be supplied ALTERNATIVELY
via the VME connector
or via this terminal

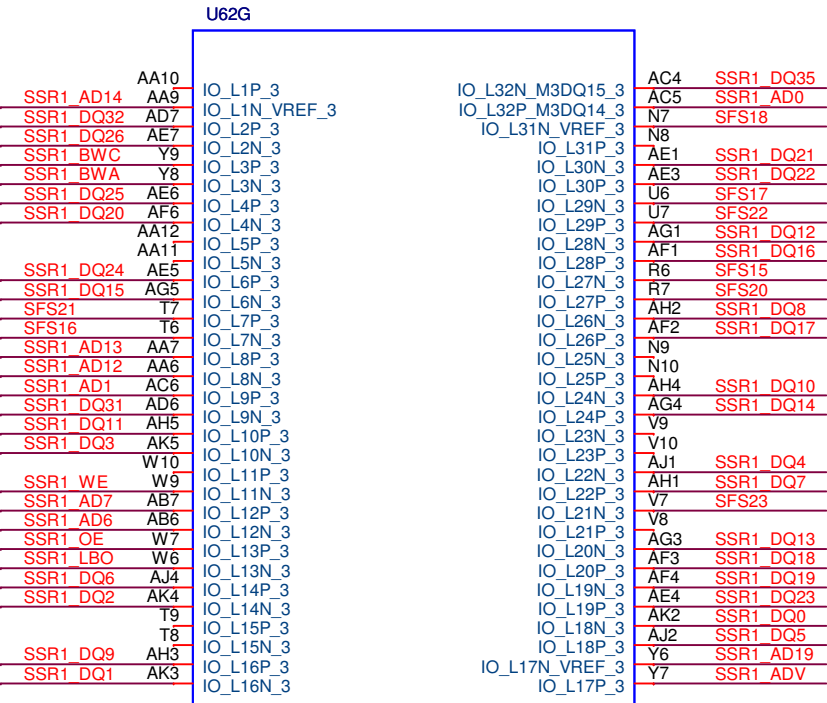


SSRAM1

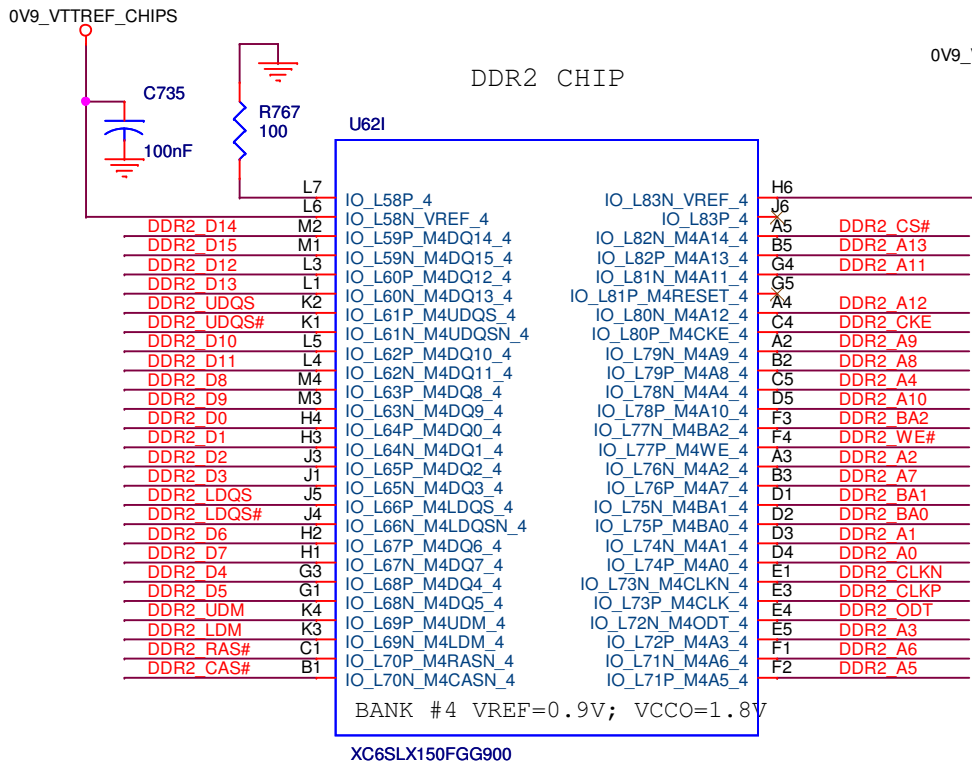
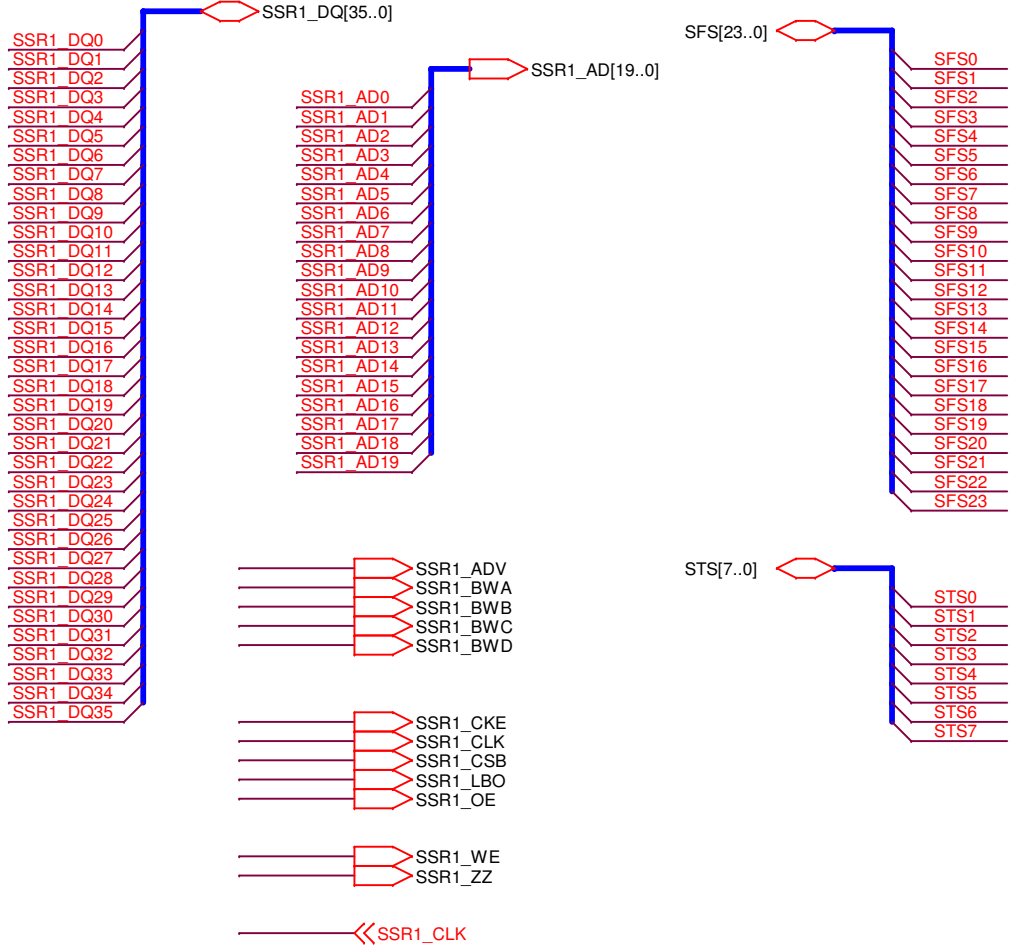
SSRAM1



XC6SLX150FGG900

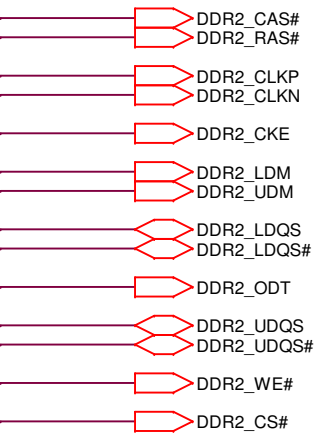
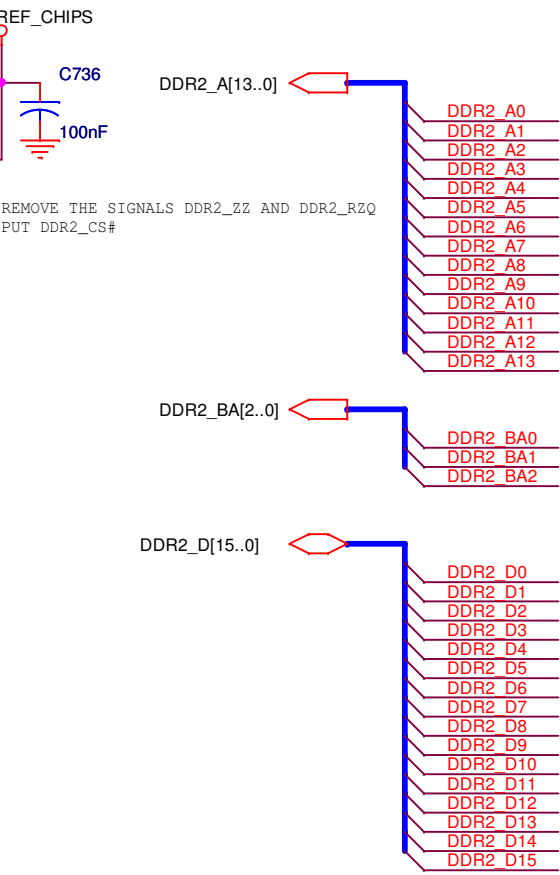


XC6SLX150FGG900



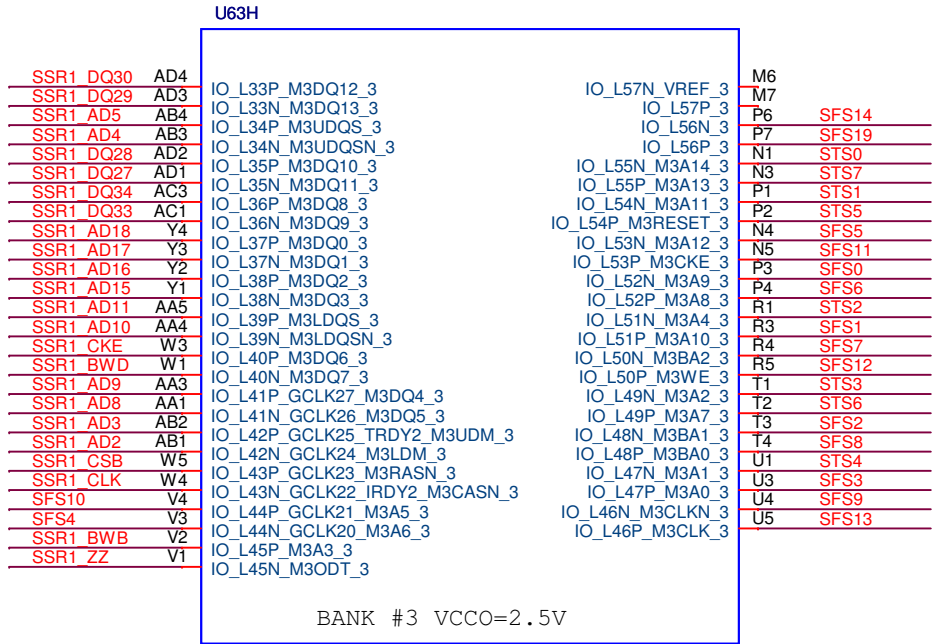
XC6SLX150FGG900

SIGNALS TO DDR2

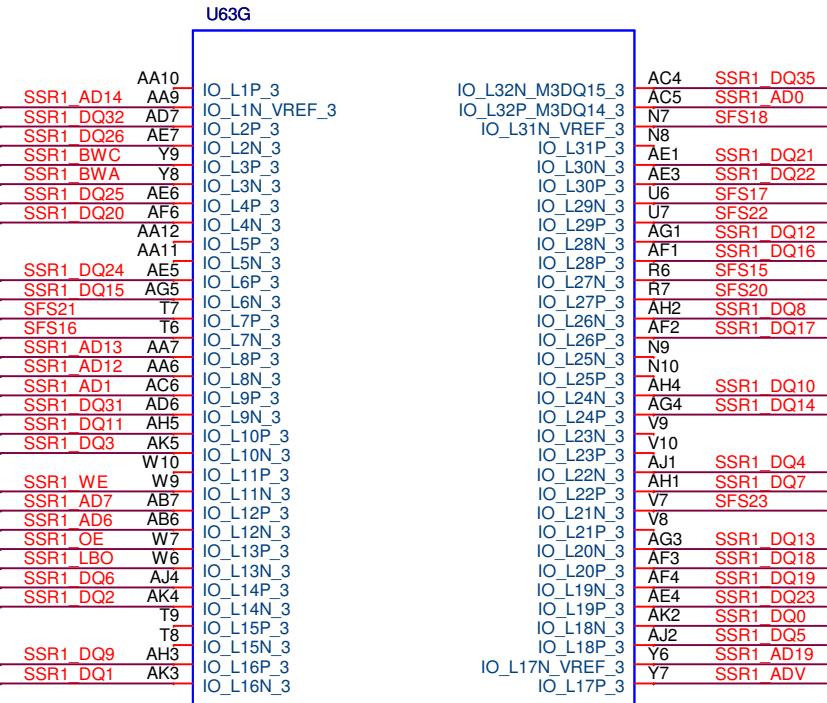


SSRAM1

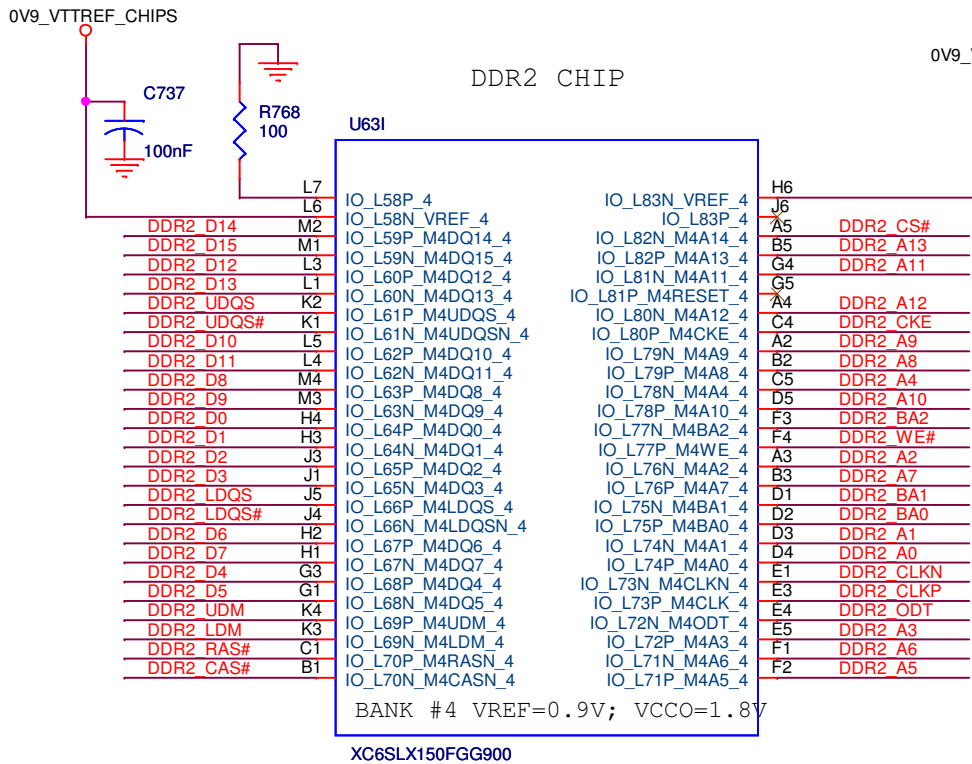
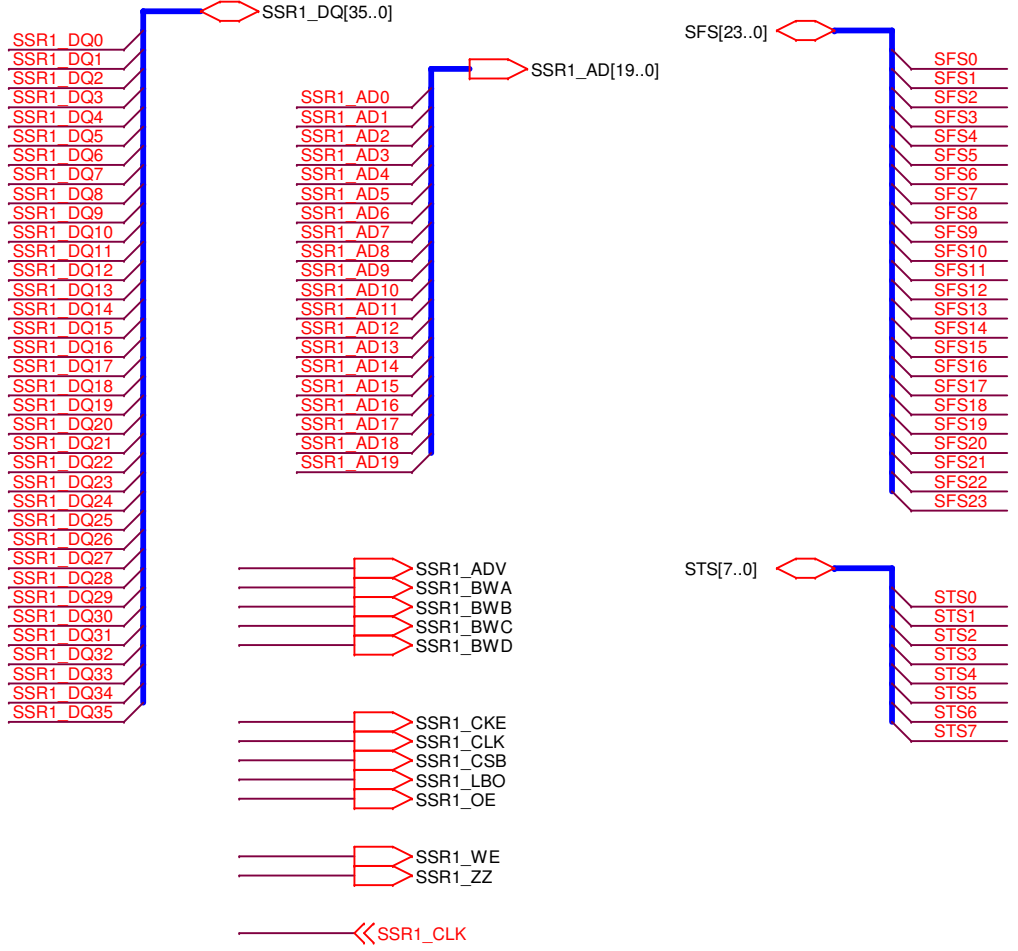
SSRAM1



XC6SLX150FGG900

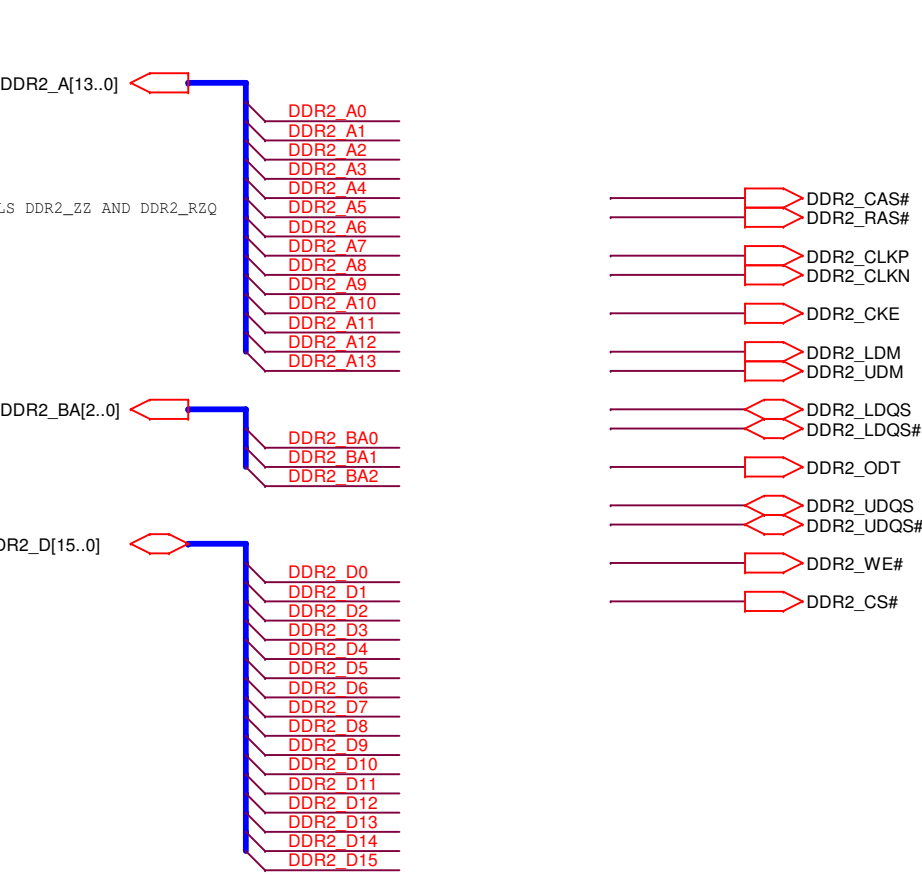


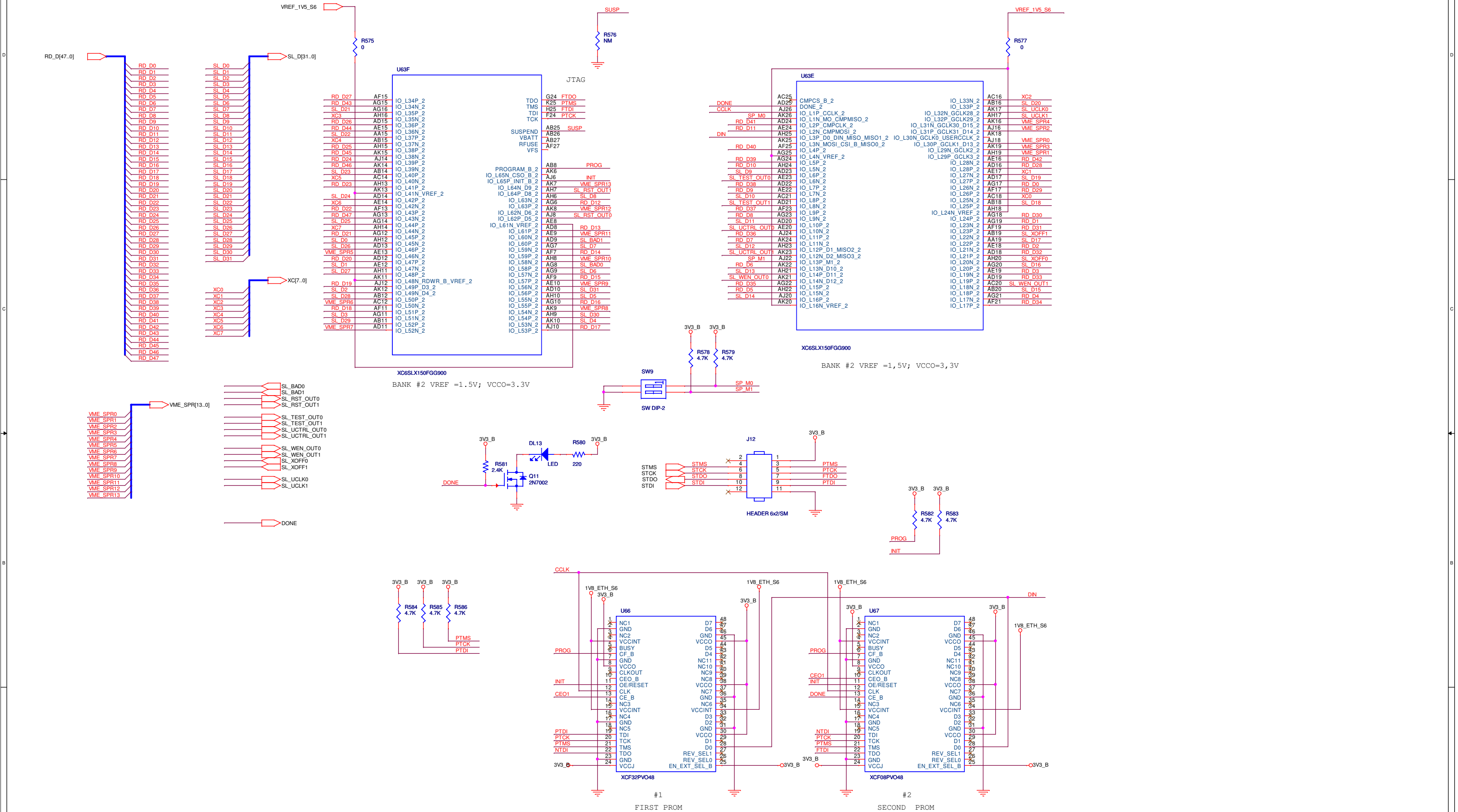
XC6SLX150FGG900

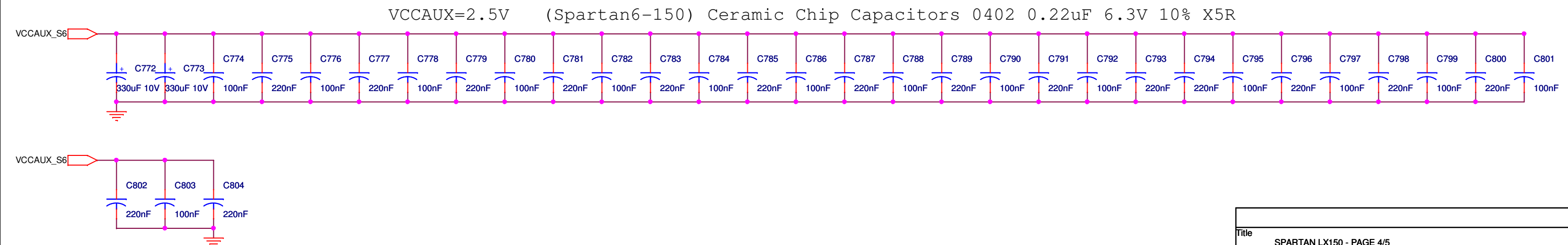
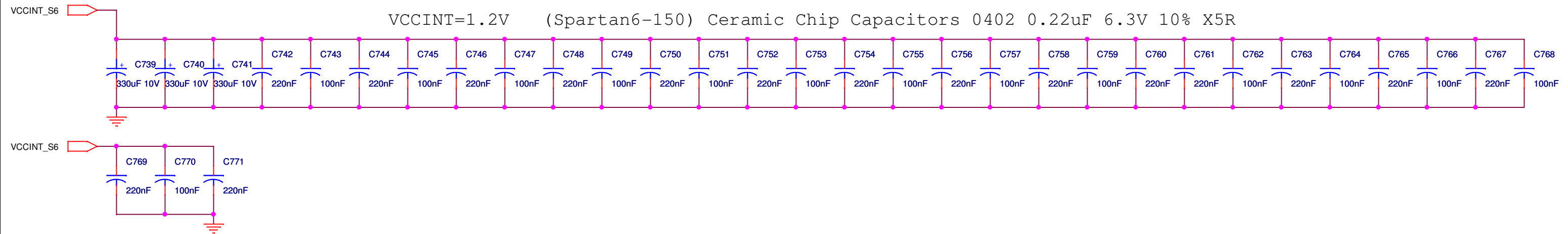
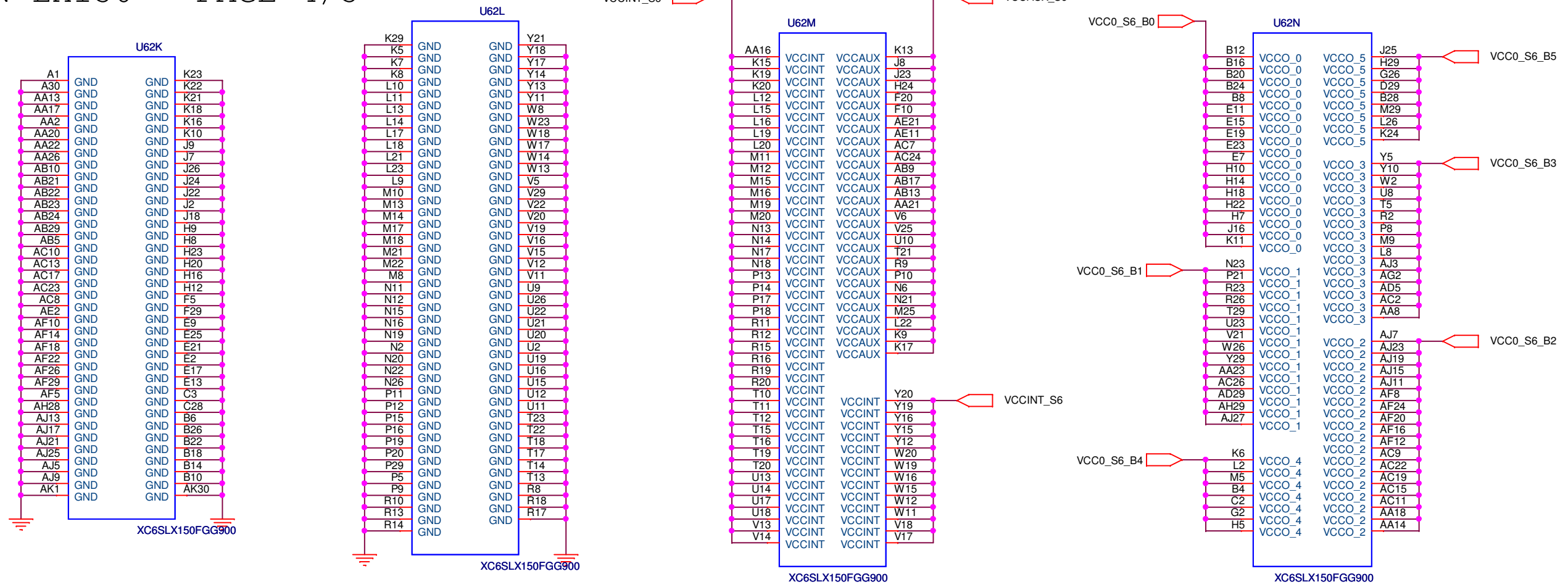


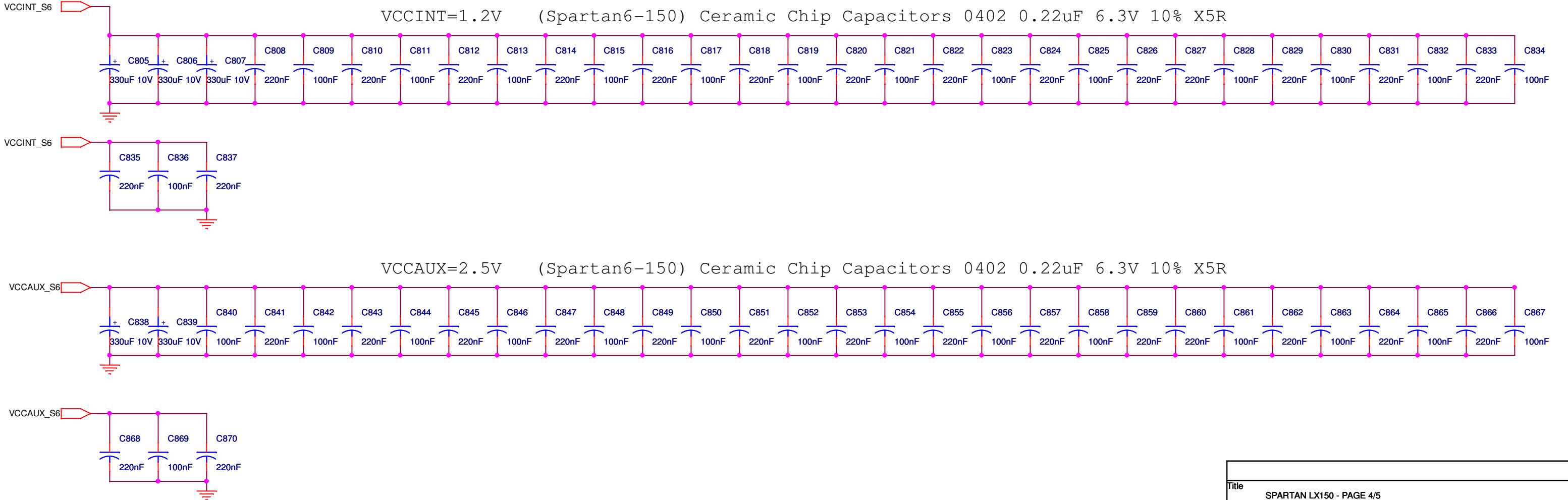
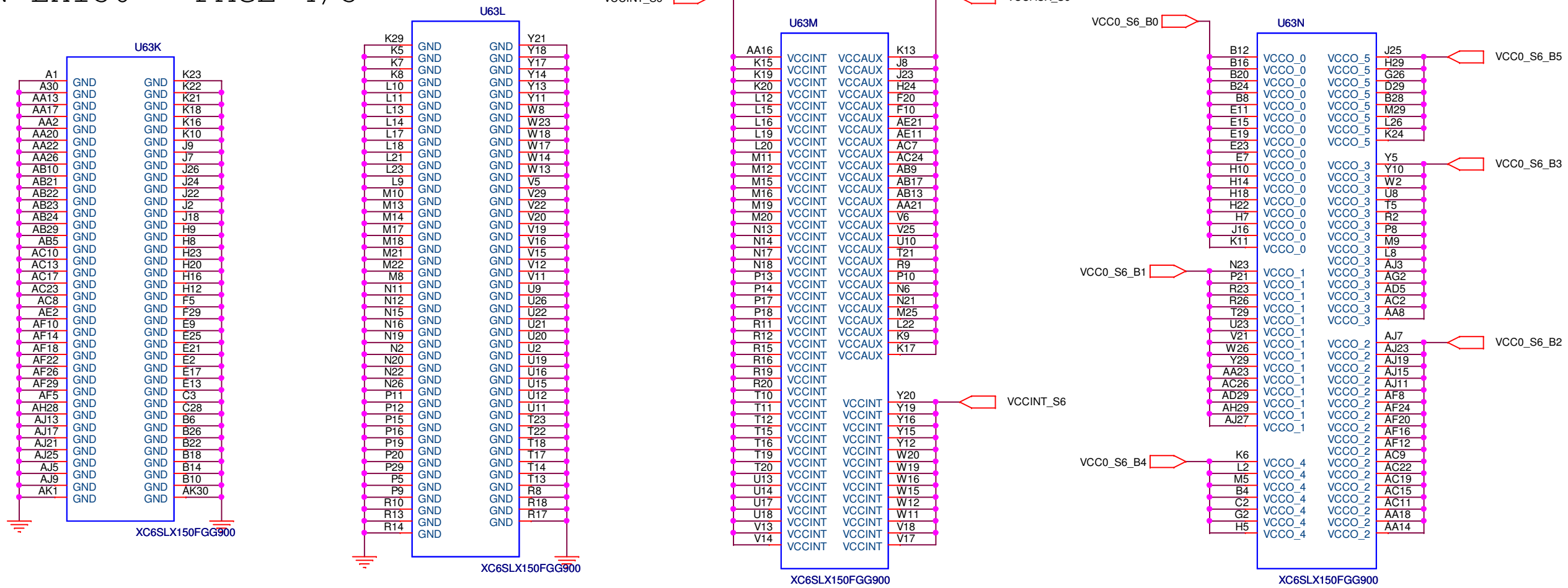
XC6SLX150FGG900

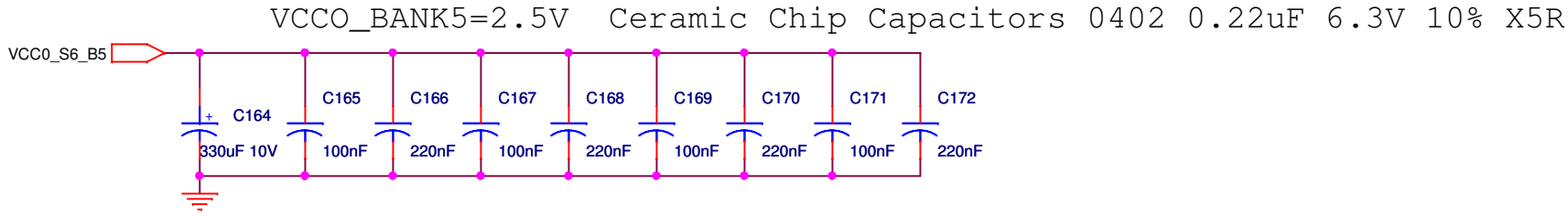
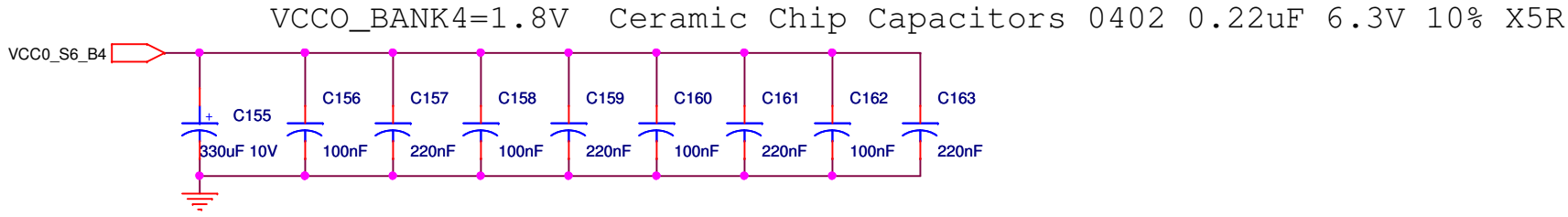
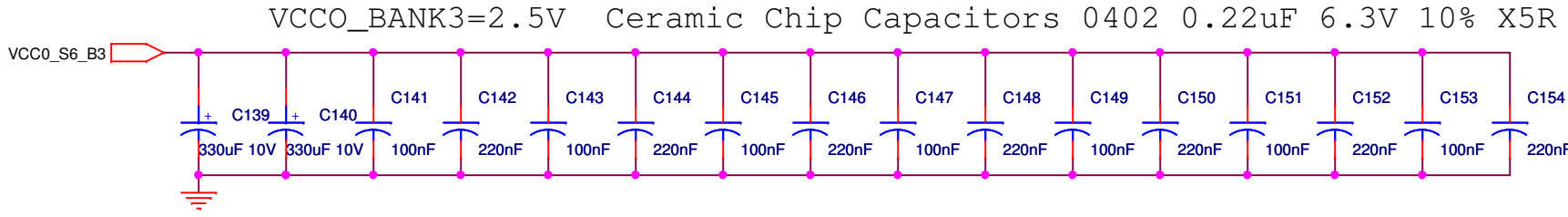
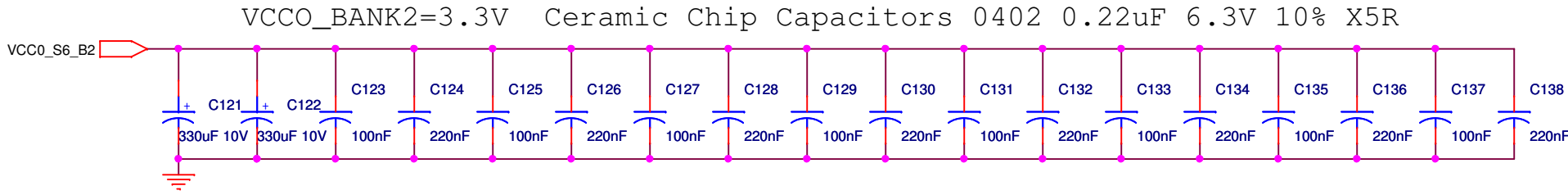
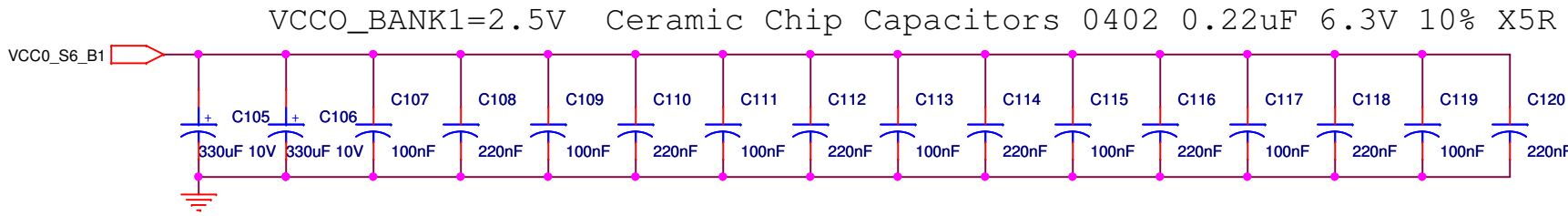
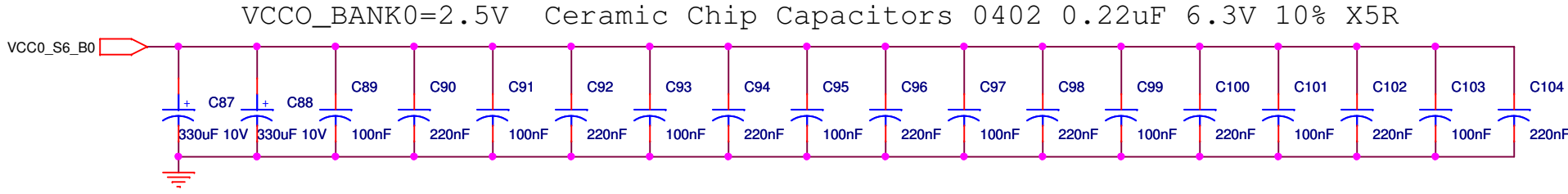
SIGNALS TO DDR2

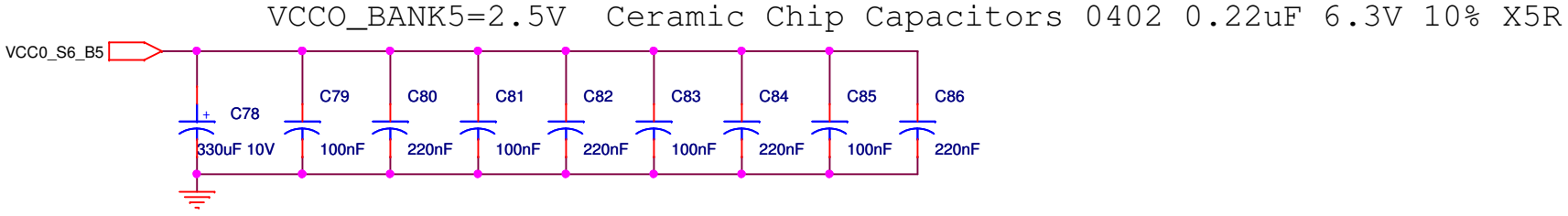
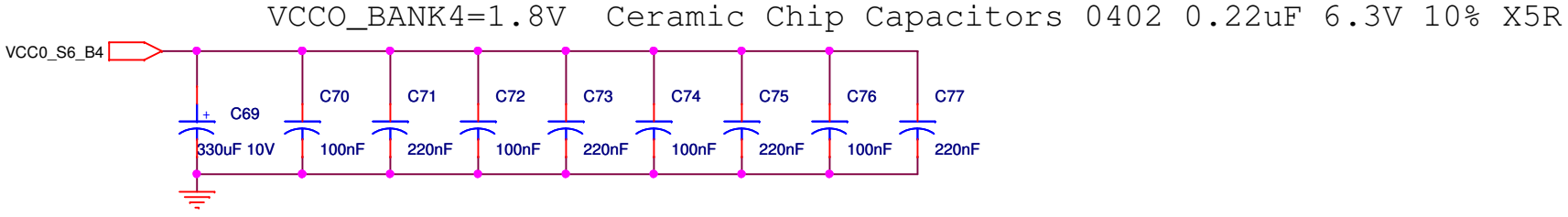
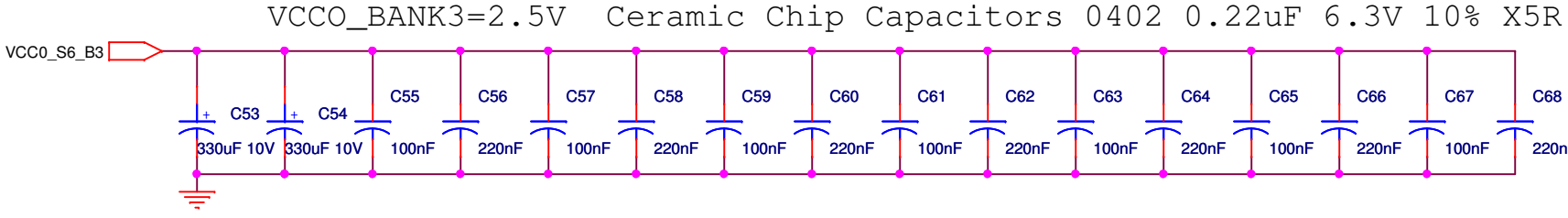
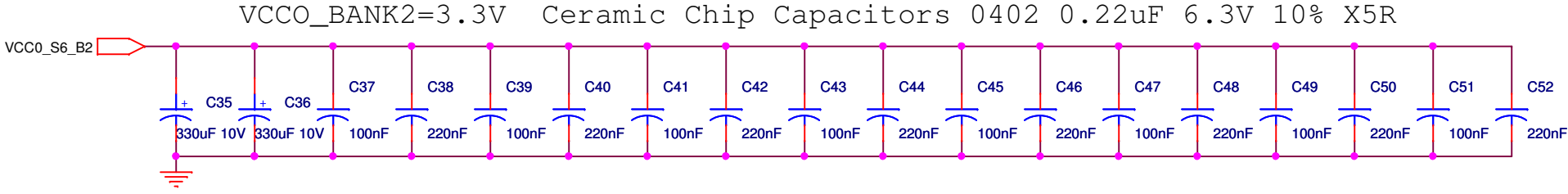
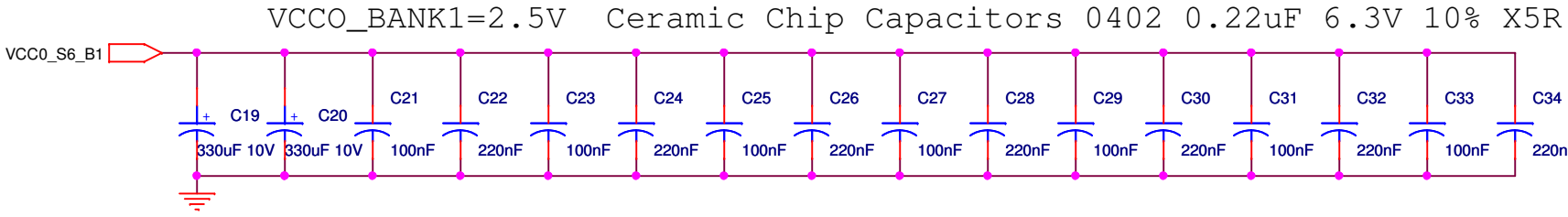
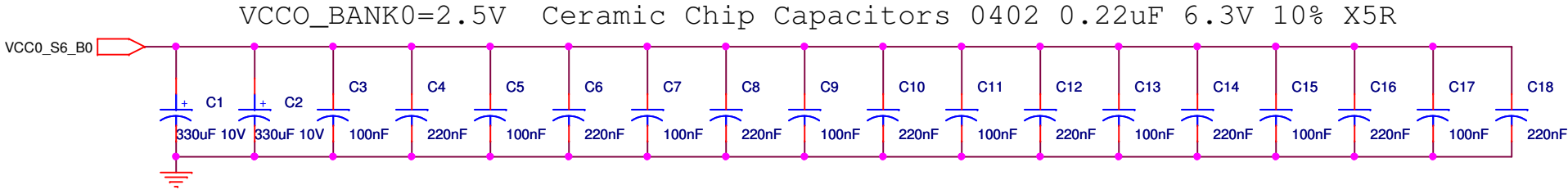


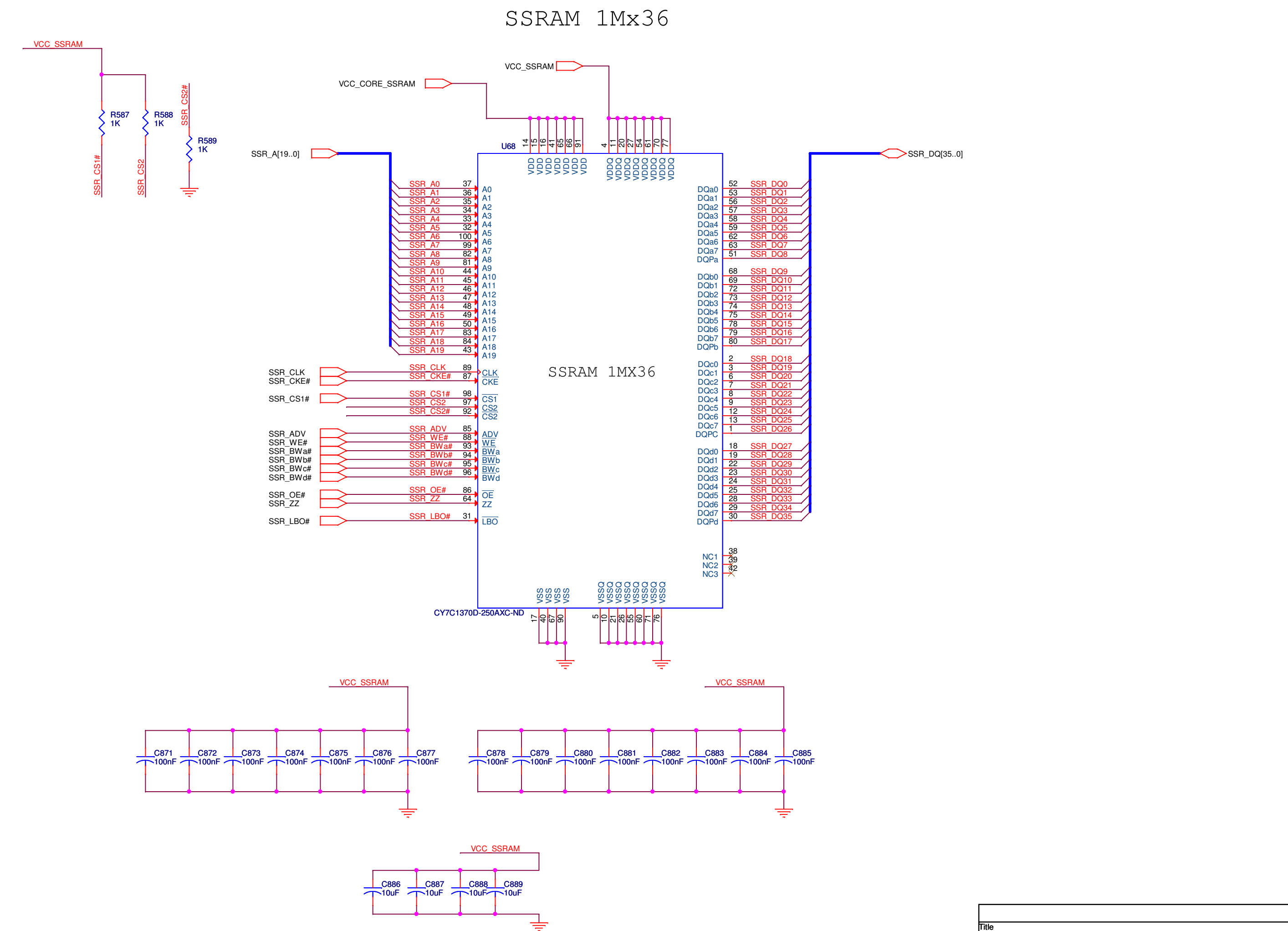


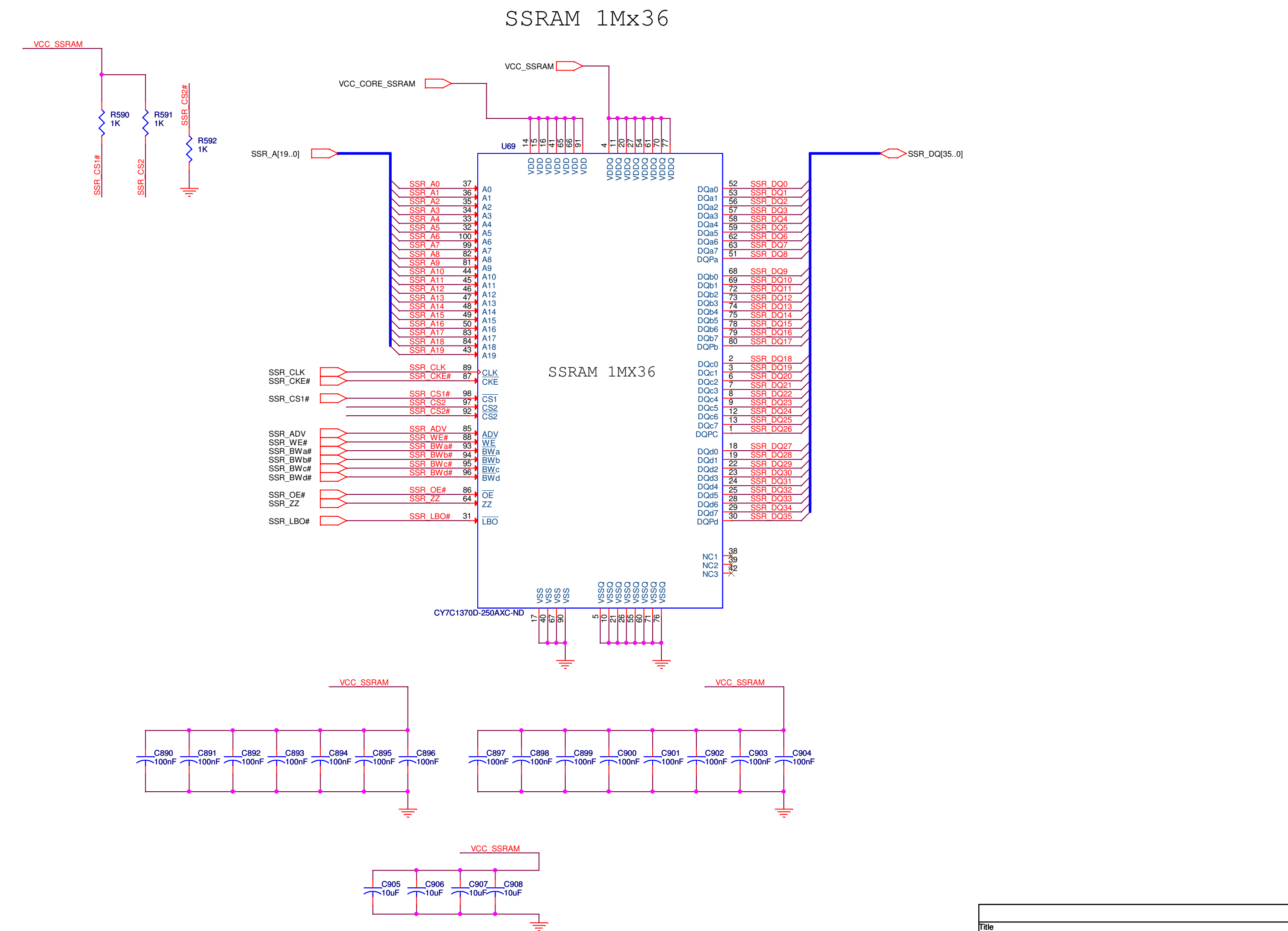


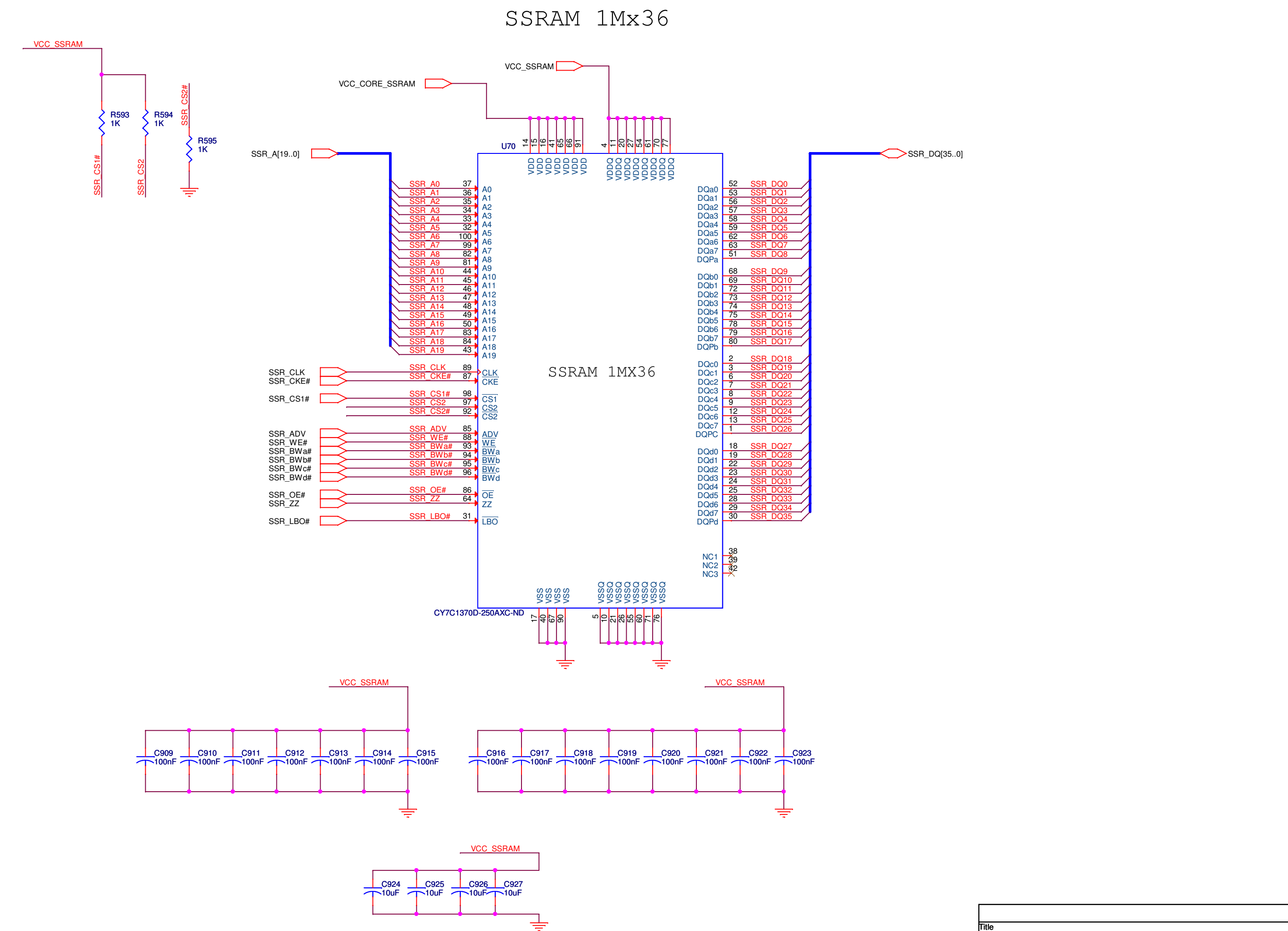


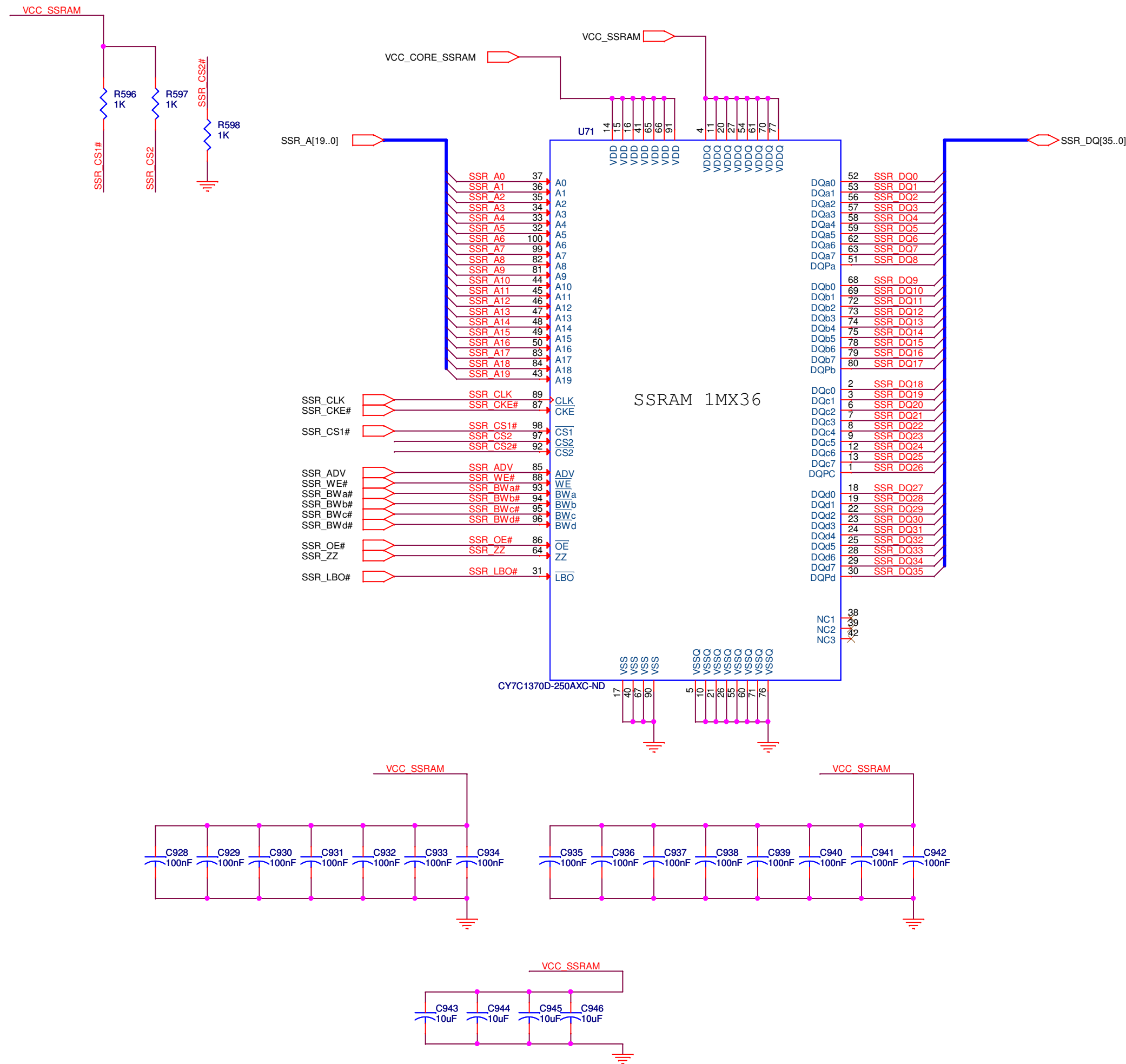






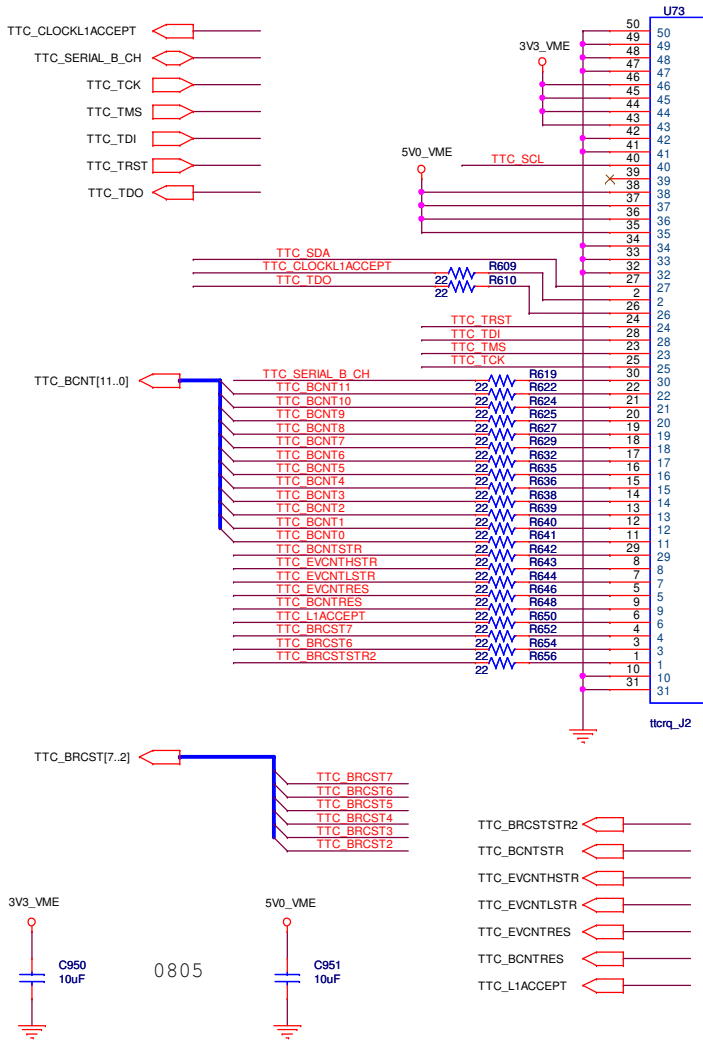
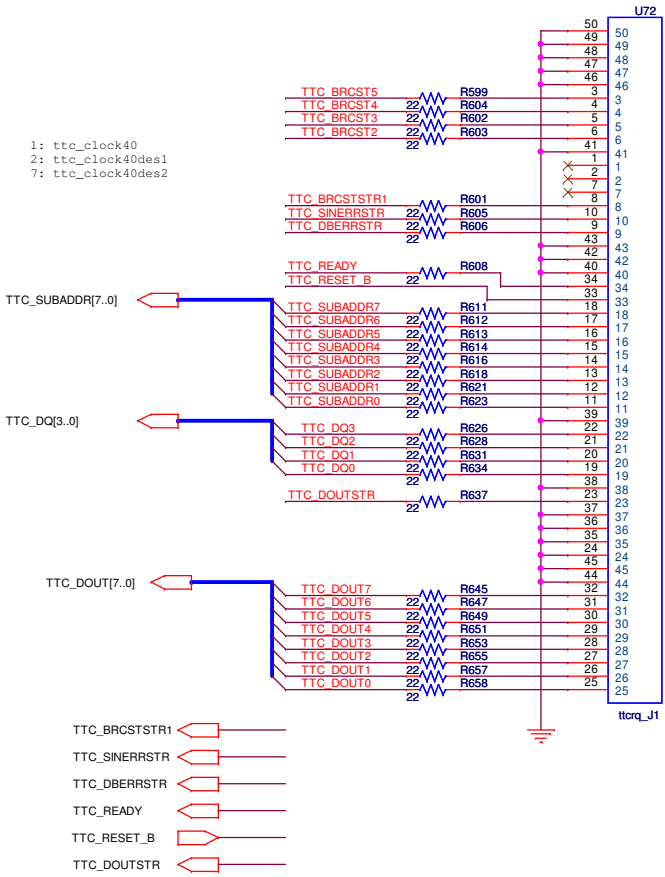




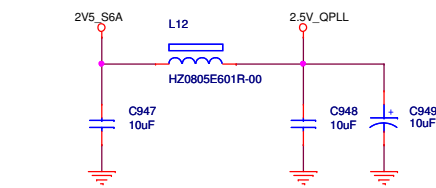
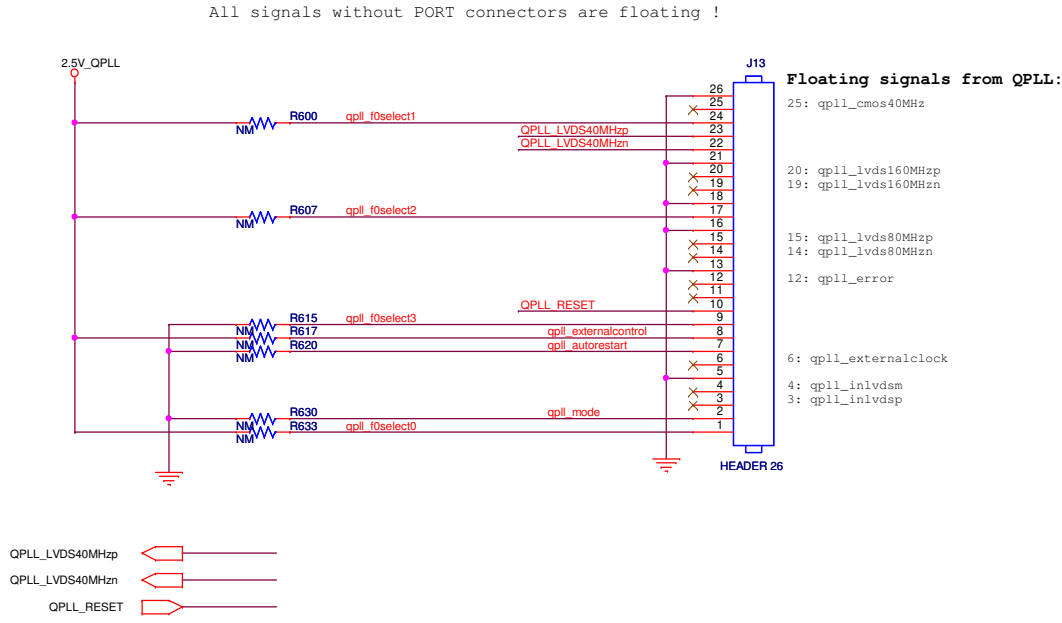


TTCrq

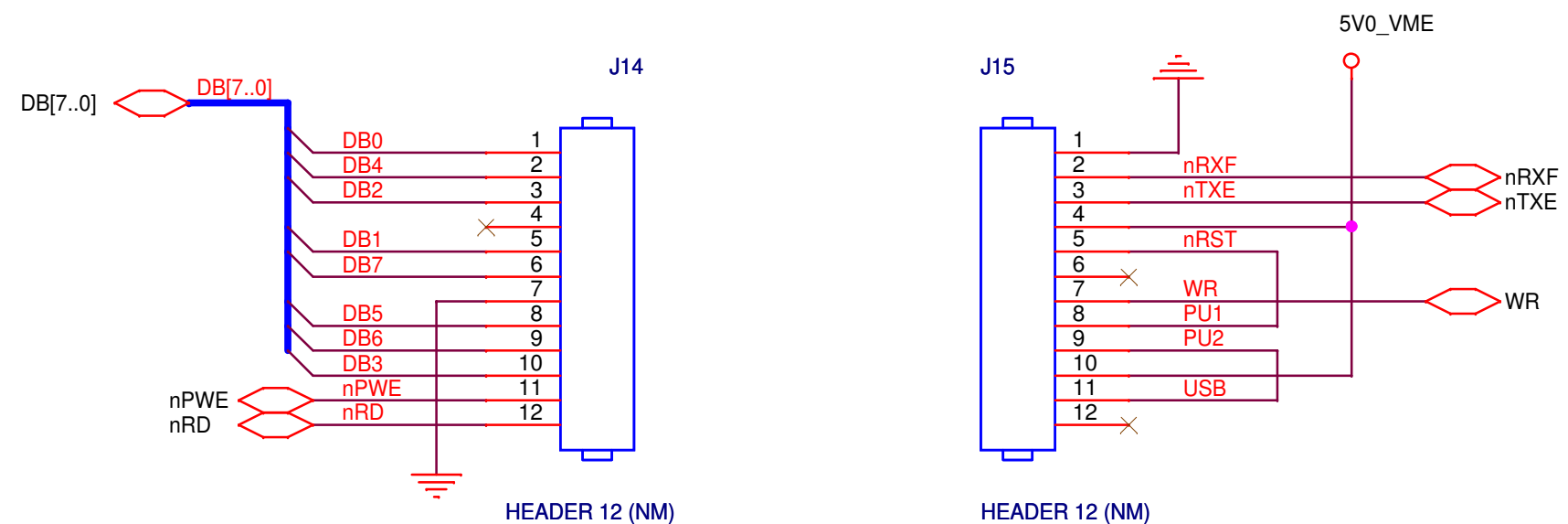
PIN 39 of J2 left unconnected:
it should be used to provide 2.5V to TTCrq,
but the TTCrq has an internal regulator



ALL SIGNALS CONNECTED TO VIRTEX5



USB PIGGYBACK



FOR ALL DIMENSIONS, SEE DATASHEET
STRIP CONNECTOR FOR UM245R FTDI
CONNECTED TO VIRTEX5

Title		
UM245R CONNECTOR FOR USB		
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