## Sailfish: Towards Improving the Latency of DAG-based BFT

Abstract— Directed Acyclic Graph (DAG) based BFT protocols balance consensus efforts across different parties and maintain high throughput even when some designated parties fail. However, existing DAG-based BFT protocols exhibit long latency to commit decisions. The primary reason for such a long latency is having a leader every 2 or more "rounds". Even under honest leaders, these protocols require two or more reliable broadcast (RBC) instances[Kartik: need to generalize the previous two lines to include cordial miners/mysticeti too] to commit the proposal submitted by the leader (leader vertex), and additional RBCs to commit other proposals (non-leader vertices). Recent works, Shoal (FC'23) and Mysticeti, have deemed supporting a leader vertex in each round particularly difficult, if not impossible.

In this work, we present Sailfish, the first DAG-based BFT that supports a leader vertex in each round. Under honest leaders, Sailfish maintains a commit latency of one RBC round plus  $1\delta$  to commit the leader vertex (where  $\delta$  is the actual transmission latency of a message) and only an additional RBC round to commit non-leader vertices. We also extend Sailfish to Multi-leader Sailfish, which facilitates multiple leaders within a single round and commits all leader vertices in a round with a latency of one RBC round plus  $1\delta$ . Through experimental evaluation, we demonstrate that our protocols achieve slightly better throughput and significantly better latency compared to state-of-the-art DAG-based protocols.

#### 1. Introduction

Byzantine fault-tolerant state machine replication (BFT SMR) protocols form the core underpinning for blockchains. At a high level, a BFT-SMR enables a group of parties to agree on a sequence of values, even if some of these parties are Byzantine (arbitrarily malicious). Owing to the need for efficient blockchains in practice, there has been a lot of recent progress in improving the key efficiency metrics namely, latency, communication complexity, and throughput under various network conditions. Assuming the network is partially synchronous, existing SMR protocols can commit with a latency overhead of  $3\delta$  (where  $\delta$  represents the actual network delay) [9], [10], [18] and also achieve linear communication complexity [30], [43] under optimistic conditions (such as an honest leader).

Most of these protocol designs rely on a designated leader who is the party responsible for proposing transactions and driving the protocol forward while other parties agree on the proposed values and ensure that the leader keeps making progress. From an efficiency standpoint, this approach results in two key drawbacks. First, there is an un-

even scheduling of work among the parties. While the leader is sending a proposal, the other parties' processors and their network is not used leading to uneven resource usage across parties. Second, in typical leader-based protocols progress stops if the leader fails and until it is replaced. Several techniques proposed in the literature can potentially mitigate these concerns. These include the use of erasure coding techniques [2], [33] or the data availability committees [22], [23], [41] to disseminate the data more efficiently.

Recently, a novel approach known as DAG-based BFT has emerged [5], [14], [24], [28], [29], [38], [39]. These protocols enable all participating parties to propose in parallel, maximizing bandwidth utilization and ensuring equitable distribution of workload. Consequently, these protocols have demonstrated improved throughput compared to the leader-based counterparts under moderate network sizes [15], [38]. However, existing DAG-based protocols incur a high latency compared to their "leader-heavy" counterparts [10], [18], [25], [30], [43]. Is high latency inherent for such DAG-based protocols? This paper works towards addressing this question.

In the following, we first discuss the core structure involved in a DAG-based protocol, then describe the latency of the state-of-the-art protocols compared to ours, and then explain the key challenges and our contributions.

Typical structure of DAG-based BFT. A DAG-based BFT progresses through a series of *rounds*. In each round r, each party makes a proposal, represented as a DAG vertex. The vertex includes references to at least 2f+1 vertices proposed in round r-1 (where f is the maximum number of Byzantine faults). These references form the edges of the DAG. The edges and paths formed from these edges are used for committing vertices in the DAG. Many DAG-based protocols [28], [37], [38] rely on a reliable broadcast protocol (RBC) [8] to disseminate the vertices; this ensures non-equivocation and guaranteed delivery. Depending on whether a communication-optimal [17] or latency-optimal [1] RBC protocols are used, the RBC would incur a latency of  $4\delta$  and  $2\delta$  respectively.

Partially synchronous DAG-based protocols rely on designated parties called leaders to commit vertices. In these protocols, the vertices proposed by the leaders (leader vertices) are committed whereas non-leader vertices are ordered as part of the causal history of leader vertices.

Latency in state-of-the-art partially synchronous DAG-based BFT protocols. The state-of-the-art partially synchronous DAG-based protocols are Bullshark [38], [39],

TABLE 1: Comparison of DAG-based BFT protocols, after GST

	RBC Used	LV Commit Latency	NLV Commit <sup>(1)</sup> Latency	Communication Complexity	Leader Frequency		NLV Latency (2) Under Failure	Modular?
Bullshark [38], [39]	Das et al. [17]	$8\delta$	$+8\delta$	$O(n^3)$	1/2	X	$+(8\Delta + 8\delta)$	<b>√</b>
Shoal [37]	Das et al. [17]	$8\delta$	$+4\delta$	$O(n^3)$	1	$\checkmark$	$+(8\Delta+4\delta)$	✓
Cordial Miners [29]	None	$3\delta$	$+3\delta$	$O(n^4)$	1/3	X	$+6\Delta$	×
Mysticeti [4]	None	$3\delta$	$+3\delta$	$O(n^4)$	1/3	$\checkmark$	$+6\Delta$	X
Sailfish Sailfish	Das et al. [17] Abraham et al. [1]	$\frac{5\delta}{3\delta}$	$^{+4\delta}_{+2\delta}$	$O(n^3) \\ O(n^4)$	1 1	<b>√</b> ✓	$+(8\Delta + 2\delta)  +(4\Delta + 2\delta)$	√ √

LV implies leader vertex. NLV implies non-leader vertex. We use the erasure-coded reliable broadcast from Das et al. [17] which incurs 4 communication steps and  $O(n^2)$  communication complexity to propagate O(n)-sized message. Bullshark (and Shoal) can also use RBC protocol of Abraham et al. [1] to achieve a commit latency of  $4\delta$  for leader vertices and additional  $4\delta$  ( $2\delta$  for Shoal) for non-leader vertices. (1) This column lists the additional latency to commit non-leader vertices that share a round with the previous leader vertex; the commit latency of these vertices is the maximum among non-leader vertices between two leader rounds. (2) The column lists the increase in latency to commit non-leader vertices when a single Byzantine failure occurs between honest leaders.

Shoal [37], Cordial miners [29] and Mysticeti [4]. We elaborate on the results obtained by these protocols in Table 1.

In Bullshark, each round employs an RBC to disseminate the proposal, and a leader is assigned every 2 rounds. The round after the leader round serves to "vote" the leader vertex; hence called the voting round. Thus, committing the leader vertex requires two RBC rounds. On the other hand, non-leader vertices that share a round with previous leader require a minimum of 4 RBCs.

Shoal introduced a "pseudo-pipelining" technique to reduce the commit latency of non-leader vertices by employing multiple instances of the Bullshark sequentially, ensuring that a leader vertex is present in every round. However, their protocol relies on Bullshark to commit some vertex before initiating a new instance with a leader in the next round. If Bullshark fails to commit, Shoal requires an additional two RBCs to initiate a new instance, which undermines its ability to guarantee a leader vertex in every round. While Shoal uses leader reputation [11] to elect good leaders and ensure a leader vertex in each round, we view reputation system as orthogonal to consensus with its own set of limitations (detailed in Section 6); it is crucial to study these protocols purely from the consensus standpoint. Furthermore, Shoal inherits 2 RBCs latency for committing the leader vertex.

Cordial Miners recently improved the latency of DAGbased BFT protocols by using best-effort broadcast (BEB) instead of RBC. They achieved a commit latency of  $3\delta$ for leader vertices and  $6\delta$  for non-leader vertices that coincide with the leader round, with the leader round repeating every 3 rounds. Building on this, Mysticeti [4] adds support to accommodate multiple leaders within a single round and commit multiple leaders from the same round during the optimistic cases (with no guarantee these vertices be committed). However, when the Byzantine parties "selectively" send their proposals only to some honest parties, Mysticeti incurs additional latency to download to missing vertices. Additionally, both protocols maintain a communication complexity of  $O(n^4)$  per round (where n is the number of parties) and lack modularity. In contrast, DAG-based protocols that utilize RBC can offer a range of communication complexities and commit latencies by leveraging existing RBCs from the literature.

In this work, we concentrate on modular DAG-based BFT protocols that use RBC. To the best of our knowledge, existing modular DAG-based protocols do not truly support a leader vertex in every (RBC) round and necessitate a minimum of 2 RBCs to commit the leader vertex. To address these concerns, we introduce Sailfish, the first DAG-based BFT protocol that achieves support for a leader vertex in each round while achieving a latency of 1RBC plus  $1\delta$ time to commit the leader vertex, along with an additional RBC to commit the non-leader vertices. When employing the optimal latency RBC [1], Sailfish incurs only  $3\delta$  to commit the leader vertex, effectively matching the best latency achieved by classical approaches [10] and DAG-based BFT not relying on RBC [4], [29]. When using a communicationoptimal RBC [17], our protocol incurs  $5\delta$  latency to commit the leader vertex. Compared to the state-of-the-art DAGbased BFT that rely on RBC, Sailfish improves the latency for committing leader vertices by at least  $1\delta$  (when using [1]) and  $3\delta$  time (when using [17]). Additionally, compared to DAG-based protocols relying on BEB, Sailfish improves the latency to commit the non-leader vertices by at least  $1\delta$ (when using [1]).

## **Challenges and Key Contributions**

The key technical challenge. In DAG-based protocols, a crucial safety invariant that needs to be maintained is: when a round r leader vertex  $v_k$  is committed by an honest party, the leader vertex of any round r' > r should have a path to  $v_k$ . In earlier protocols,  $v_k$  is committed when a sufficient  $(f+1 \text{ or more}) \text{ round } r+1 \text{ vertices have a path to } v_k \text{ and }$ the safety invariant is achieved by having a leader vertex in every two or more rounds. As a round r+2 vertex has paths to 2f + 1 round r + 1 vertices, a round r + 2 leader vertex will trivially have a path to  $v_k$ . Similarly, the leader vertex of round r' > r + 2 will have a path to  $v_k$ . However, the round r+1 leader vertex lacks paths to other round r+1vertices. Consequently, even if  $v_k$  is committed, the round r+1 leader vertex cannot establish a path to it via other round r+1 vertices. The only way it can form a path to  $v_k$ is by awaiting its delivery. However, waiting for  $v_k$  to be delivered poses liveness concerns. Alternatively, if the round r+1 leader vertex is proposed (after a timeout), it can lack a path to  $v_k$  even when other parties have committed  $v_k$ , violating the safety requirement. This is the key challenge when supporting a leader vertex in each round.

Shoal noted that the above challenge is particularly difficult, while Mysticeti claimed it is unsolvable. In their own words, Shoal writes "Our attempts to solve the problem by delving into the inner workings of the protocol and exploring complex quorum intersection ordering rules have not been fruitful. Intuitively, this is because consensus requires a voting round after each leader proposal and the next leader should link to the decisions (votes) on the previous one". Similarly, Mysticeti states, "Unfortunately, it is not trivial to increase the number of slots, as the commit rule of Bullshark relies on the fact that every proposer slot has a link to every other proposer slot, something that is not possible even if there is a single proposer per round, let alone n". Here, slot implies leader in our context. Consequently, both protocols resort to optimistic techniques to support a leader vertex in each round (for Shoal) and multiple leaders in a round (for Mysticeti), but these approaches have inherent limitations.

Towards having a leader vertex in each round. Our solution to the above challenge is as follows: in our protocol, we mandate the round r+1 leader vertex to have a path to  $v_k$  or contain a proof that shows a sufficient number of honest parties did not vote for  $v_k$ . When such a proof exists, we can guarantee  $v_k$  cannot be committed; it is thus safe for the round r+1 leader vertex to lack a path to  $v_k$ .

The requirement for the round r+1 leader vertex to wait for  $v_k$  or the proof marginally increases the timeout duration a party has to wait in a round compared to existing protocols, potentially impacting latency under failures. To address this concern, we conduct a thorough analysis of the latency. Our analysis indicates that despite the increased timeout, our latencies outperform the state-of-the-art in the presence of a single Byzantine failure between honest leaders (see Table 1).

Towards improving the commit latency to 1RBC plus  $1\delta$  for leader vertices. In a typical RBC protocol [8], [17], [32], the sender first sends its value to all other parties, followed by multiple rounds of message exchanges among the parties. When the sender is honest, the first value received from the sender is the value that is eventually delivered. We rely on this observation and decide based on the first received values of the round r+1 vertices, i.e., we do not require the RBC of round r+1 vertices to be delivered to commit the round r leader vertex. However, when the sender is faulty, the first value received from the sender can be different from the final delivered value. In order to account for such Byzantine behavior, our protocol commits the round r leader vertex only when 2f + 1 round-(r + 1)vertices have paths to the round r leader vertex. Out of the 2f + 1 first messages for the round r + 1 vertices, at least f + 1 are sent by honest parties which will be delivered by all honest parties.

This approach ensures the safety invariant while enabling our protocol to commit the leader vertex with a latency of 1 RBC plus  $1\delta$ , and an additional RBC to commit the non-leader vertices. We further note that this optimization is unique to our protocol and does not apply to the other protocols as it can cause liveness concerns. We provide the intuition behind this reasoning in detail in Section 3.

Towards supporting multiple leaders in a round. In order to improve the latency for multiple vertices, we extend Sailfish to support multiple leaders within a single round. We categorize these leaders as the main leader and secondary leaders. The primary role of the main leader remain identical to that of Sailfish: its leader vertex must either establish a path to all leader vertices from the previous round or contain a proof that some missing leader vertices cannot be committed; thus maintaining the safety invariant. We term this extended version Multi-leader Sailfish.

The commit rule closely resembles that of Sailfish, with some additional constraints. In an ideal scenario where all leaders are honest, the respective leader vertices can be committed with a latency of one RBC plus  $1\delta$ .

**Evaluation.** We implement and evaluate the performance of Sailfish, comparing it with state-of-the-art DAG-based protocols. In our evaluation, we observe that Sailfish achieves significantly better latency than Bullshark [38], [39] and Shoal [37], with slightly better throughput. This improvement stems from Sailfish's support for a leader vertex in each round and its ability to commit the leader vertex with one RBC plus  $1\delta$ , and an additional RBC to commit the non-leader vertices.

We also evaluate the performance of Multi-leader Sailfish. Our results indicate that in failure-free cases, the average latency of the protocol reduces with the increase in the number of leaders in a round as more vertices are committed with a latency of one RBC plus  $1\delta$ .

**Organization.** The rest of the paper is organized as follows: In Section 2, we present the system model and preliminaries for our work. Section 3 presents Sailfish, the first DAG-based BFT that supports a leader vertex in each round and achieves a commit latency of one RBC plus  $1\delta$  to commit the leader vertex and an additional RBC to commit the non-leader vertices. We further enhance this protocol in Section 4 to accommodate multiple leaders within a single round, aiming to achieve improved average latency. An evaluation of our protocols is presented in Section 5, followed by a comprehensive discussion of related works in Section 6.

#### 2. Preliminaries

We consider a system  $\mathcal{P}:=P_1,\ldots,P_n$  consisting of n parties out of which up to  $f=\lfloor\frac{n-1}{3}\rfloor$  parties can be Byzantine. The Byzantine parties may behave arbitrarily. A party that is not faulty throughout the execution is considered to be *honest* and executes the protocol as specified.

We consider the partial synchrony model of Dwork et al. [21]. Under this model, the network starts in an initial

state of asynchrony during which the adversary may arbitrarily delay messages sent by honest parties. However, after an unknown time called the *Global Stabilization Time* (GST), the adversary must ensure that all messages sent by honest parties are delivered to their intended recipients within  $\Delta$  time of being sent. We use  $\delta$  to characterize the actual (variable) transmission latencies of messages and observe that  $\delta \leq \Delta$  after GST. Additionally, we assume the local clocks of the parties have *no clock drift* and *arbitrary clock skew*.

We make use of digital signatures and a public-key infrastructure (PKI) to prevent spoofing and replays and validate messages. We use  $\langle x \rangle_i$  to denote a message x digitally signed by party  $P_i$  using its private key.

**Byzantine reliable broadcast.** We use Byzantine reliable broadcast (RBC) as a building block. In an RBC, a designated sender  $P_k$  invokes  $\operatorname{r\_bcast}_k(m,r)$  to propagate its input m in some round  $r \in \mathbb{N}$ . Each party  $P_i$  then outputs the message m via  $\operatorname{r\_deliver}_i(m,r,P_k)$  where  $P_k$  is the designated sender and r is the round number in which sender  $P_k$  sent the message m. The reliable broadcast primitive satisfies the following properties:

- **Agreement.** If an honest party  $P_i$  outputs  $r_deliver_i(m, r, P_k)$ , then every other honest party  $P_j$  eventually outputs  $r_deliver_j(m, r, P_k)$ .
- **Integrity.** For every round  $r \in \mathbb{N}$  and party  $P_k \in \mathcal{P}$ , an honest party  $P_i$  outputs  $\mathbf{r}$ \_deliver $_i$  at most once regardless of m.
- Validity. If an honest party  $P_k$  calls  $r_b cast_k(m, r)$  then every honest party eventually outputs  $r_d eliver(m, r, P_k)$ .

#### 2.1. Problem Definition

Following Bullshark [38], we focus on the Byzantine Atomic Broadcast (BAB) problem as defined below:

**Definition 1** (Byzantine atomic broadcast [28], [38]). Each honest party  $P_i \in \mathcal{P}$  can call  $a\_bcast_i(m,r)$  and output  $a\_deliver_i(m,r,P_k)$ ,  $P_k \in \mathcal{P}$ . A Byzantine atomic broadcast protocol satisfies reliable broadcast properties (agreement, integrity, and validity) as well as:

- **Total order.** If an honest party  $P_i$  outputs  $a\_deliver_i(m, r, P_k)$  before  $a\_deliver_i(m', r', P_\ell)$ , then no honest party  $P_j$  outputs  $a\_deliver_j(m', r', P_\ell)$  before  $a\_deliver_j(m, r, P_k)$ .

### 3. The Sailfish Protocol

This section presents Sailfish, a protocol that supports a leader vertex in each round and improves the latency to commit both leader and non-leader vertices. Specifically, Sailfish incurs one RBC plus  $1\delta$  to commit the leader vertex and an additional RBC to commit the non-leader vertex.

**Round based execution.** Our protocol progresses through a sequence of numbered *rounds*. Rounds are numbered by non-negative integers starting with 1. Each round r consists of a designated leader, denoted by  $L_r$ , which is selected via a deterministic method based on the round number.

**Basic data structures.** At a high level, the communication among parties is represented in the form of DAG. In each round, each party proposes a single vertex containing a (possibly empty) block of transactions along with references to at least 2f+1 vertices proposed in an earlier round. Those references serve as the edges in the DAG. The proposed vertices are propagated using RBC to ensure non-equivocation and guarantee all honest parties eventually deliver them.

The basic data structures and utilities for DAG construction are presented in Figure 1. Each party maintains a local copy of the DAG and different honest parties may observe different views of the DAG. However, due to the reliable broadcast of the vertices, each party will eventually converge on the same view of the DAG. The local view of DAG for party  $P_i$  is represented as  $DAG_i$ . Each vertex is associated with a unique round number and a unique sender (source). When  $P_i$  delivers a round r vertex, it is added to  $DAG_i[r]$ .  $DAG_i[r]$  contains up to n vertices.

Each vertex consists of two sets of outgoing edges — strong edges and weak edges. The strong edges of round r vertex v consist of at least 2f+1 vertices from round r-1 while the weak edges of the vertex consist of up to f vertices from rounds < r-1 such that there is no path from v to these vertices. A path from vertex  $v_k$  to  $v_\ell$  following the strong edges is called a strong path. Compared to Bullshark [38], we add two additional fields in the structure of the vertex - (i) v.nvc, which stores a no-vote certificate (consisting of a quorum of no-vote messages in a round), and (ii) v.tc, which store timeout certificate (consisting of a quorum of timeout messages in a round). We explain the purpose of these fields shortly.

**DAG construction protocol.** The DAG construction protocol is presented in Figure 2. In each round r, each party  $P_i$ proposes one vertex v. A round r vertex proposed by  $L_r$ is referred to as the round r leader vertex while the other round r vertices are non-leader vertices. In order to propose a vertex in a round r,  $P_i$  waits to receive at least 2f+1 round r-1 vertices along with the round r-1 leader vertex until a timeout occurs in round r-1. In the event that  $P_i$  receives 2f + 1 round r - 1 along with round r - 1 leader vertex,  $P_i$  can immediately enter round r and propose a round rvertex (see Line 36). We note that including a reference to the round r-1 leader vertex serves as a "vote" towards the round r-1 leader vertex. These votes are later used to commit the leader vertex. Thus, waiting for the leader vertex until a timeout helps honest parties to vote for the leader vertex and helps commit the leader vertex with a small latency when the leader is honest (after GST).

If  $P_i$  did not receive the round r-1 leader vertex before the timeout, it multicasts  $\langle \text{timeout}, r-1 \rangle_i$  to all other parties (see Line 38). In addition, an honest party  $P_j$  in round  $r' \leq r-1$  also multicasts  $\langle \text{timeout}, r-1 \rangle_j$  messages if it receives f+1 distinct round r-1 timeout messages (see Line 40). Upon receiving 2f+1 round r-1 timeout messages (denoted by  $\mathcal{TC}_{r-1}$ ),  $P_i$  can enter round r and propose a round r vertex as long as it has received at least 2f+1 round r-1 vertices (see Line 36). In our protocol,

```
Local variables:
                                                                                                                              > The struct of a vertex in the DAG
      struct vertex v:
         v.round - the round of v in the DAG
          v.source - the party that broadcast v
          v.block - a block of transactions
          v.strongEdges - a set of vertices in v.round that represent strong edges
          v.weakEdges - a set of vertices in rounds < v.round-1 that represent weak edges
          v.nvc - a no-vote certificate for v.round - 1 (if any)
          v.tc - a timeout certificate for v.round - 1 (if any)
      DAG_i An array of sets of vertices (indexed by rounds)
      blocksToPropose - A queue, initially empty, P_i enqueues valid blocks of transactions from clients
      leaderStack \leftarrow initialize empty stack
                                                                                    > Check if exists a path consisting of strong and weak edges in the DAG
        return exists a sequence of k \in \mathbb{N}, vertices v_1, \ldots, v_k s.t.
                v_1 = v, \, v_k = u, \, \text{and} \, \, \forall j \in [2,..,k]: v_j \in \bigcup_{r > 1} DAG_i[r] \wedge (v_j \in v_{j-1}.weakEdges \cup v_{j-1}.strongEdges)
3: procedure strong_path(v, u)
                                                                            \triangleright Check if exists a path consisting of only strong edges from v to u in the DAG
        return exists a sequence of k \in \mathbb{N}, vertices v_1, \ldots, v_k s.t.
                v_1=v,\,v_k=u,\, \mathrm{and}\,\, \forall j\in [2,..,k]: v_j\in \bigcup_{r\geq 1}DAG_i[r] \wedge v_j\in v_{j-1}.strongEdges
5: procedure set_weak_edges(v, r)

    Add edges to orphan vertices

                                                                                     18: procedure broadcast_vertex(r)
       v.weakEdges \leftarrow \{\}
                                                                                     19:
                                                                                              v \leftarrow \text{create\_vertex}(r)
6:
       for r' = r - 2 down to 1 do
7:
                                                                                     20:
                                                                                              try_add_to_dag(v)
           for every u \in DAG_i[r'] s.t. \neg path(v, u) do
8:
                                                                                     21:
                                                                                              r_bcast_i(v, r)
9.
               v.weakEdges \leftarrow v.weakEdges \cup \{u\}
                                                                                     22: procedure order_vertices()
10: procedure get\_vertex(p, r)
                                                                                     23:
                                                                                              while \neg leaderStack.isEmpty() do
        if \exists v \in DAG_i[r] s.t. v.source = p then
                                                                                     24:
                                                                                                  v \leftarrow leaderStack.\mathsf{pop}()
11:
                                                                                     25:
                                                                                                  verticesToDeliver \leftarrow \{v' \in \bigcup_{r>0} DAG_i[r] \mid path(v, v')\}
12:
            return v
                                                                                                  \land v' \not\in deliveredVertices
13:
        return 1
                                                                                                  for every v' \in verticesToDeliver in some deterministic
                                                                                     26:
14: procedure get_leader_vertex(r)
                                                                                        order do
15:
        return get_vertex(L_r, r)
                                                                                     27:
                                                                                                       output a deliver<sub>i</sub>(v'.block, v'.round, v'.source)
16: procedure a\_bcast_i(b, r)
                                                                                     28:
                                                                                                       deliveredVertices \leftarrow deliveredVertices \cup \{v'\}
        blocksToPropose.enqueue(b)
```

Figure 1: Basic data structures for Sailfish. The utility functions are adapted from [28], [38].

```
Local variables:
       round \leftarrow 1; buffer \leftarrow \{\}
29: upon r_deliver_i(v, r, p) do
30:
         if v.source = p \land v.round = r \land |v.StrongEdges| \ge 2f + 1 \land is\_valid(v) then
             if \neg \text{try\_to\_add\_to\_dag}(v) then
31:
32:
                 buffer \leftarrow buffer \cup \{v\}
33:
             else
                 \quad \text{for } v' \in \textit{buffer}: v'.round \leq r \,\, \text{do}
34:
35:
                      try_to_add_to_dag(v')
36: upon |DAG_i[r]| \ge 2f + 1 \land (\exists v' \in DAG_i[r] : v'.source = L_r \lor \mathcal{TC}_r is received) for r \ge round do
37:
        advance\_round(r + 1)
38: upon timeout do
                                                                                            55: procedure try_to_add_to_dag(v)
39.
         multicast \langle timeout, round \rangle_i
                                                                                                     if \forall v' \in v.strongEdges \cup v.weakEdges : v' \in
                                                                                            56:
                                                                                               \bigcup_{k\geq 1} DAG_i[k] then
40: upon receiving f + 1 (timeout, r)* such that r \ge round do
                                                                                            57:
                                                                                                          DAG_i[v.round] \leftarrow DAG_i[v.round] \cup \{v\}
41:
         multicast \langle \mathsf{timeout}, r \rangle_i
                                                                                            58:
                                                                                                          if |DAG_i[v.round]| \ge 2f + 1 then
                                                                                                              \mathsf{try\_commit}(v.round-1, DAG_i[v.round])
                                                                                            59:
42: upon receiving \mathcal{TC}_r such that r \geq round do
                                                                                                          \textit{buffer} \leftarrow \textit{buffer} \setminus \{v\}
43:
         multicast \mathcal{TC}_r
                                                                                            60:
                                                                                            61:
                                                                                                          return true
44: procedure create_new_vertex(r)
                                                                                            62:
                                                                                                      return false
45:
         v.round \leftarrow r
         v.source \leftarrow P_i
46:
                                                                                            63: procedure advance_round(r)
47:
         v.block \leftarrow blocksToPropose.dequeue()
                                                                                                      if \not\exists v' \in DAG_i[r-1]|: v'.source = L_{r-1} then
                                                                                            64:
         v.strongEdges \leftarrow DAG_i[r-1]
48:
                                                                                            65:
                                                                                                          send (no-vote, r-1)<sub>i</sub> to L_r
49:
         set_weak_edges(v, r)
                                                                                            66:
                                                                                                      if P_i = L_r then
                                                                                                          wait until \exists v' \in DAG_i[r-1] : v'.source = L_{r-1} or
50:
         if \exists v' \in DAG_i[r-1] : v'.source = L_{r-1} then
                                                                                            67:
             n.tc \leftarrow \mathcal{TC}_{r-1}
51:
                                                                                                          \mathcal{NVC}_{r-1} is received
52:
             if P_i = L_r then
                                                                                            68:
                                                                                                      round \leftarrow r; start timer
53:
                 v.nvc \leftarrow \mathcal{NVC}_{r-1}
                                                                                            69:
                                                                                                      broadcast_vertex(round)
54:
        return v
```

Figure 2: Sailfish: DAG construction protocol for party  $P_i$ 

we require a round r vertex to either have a strong path to the round r-1 leader vertex or include  $\mathcal{TC}_{r-1}$  in v.tc. This is a constraint that we place on all vertices. We will clarify the purpose of this constraint shortly.

When  $P_i$  proposes a round r vertex without a strong path to the round r-1 leader vertex, it also sends a no-vote message to  $L_r$  indicating that  $P_i$  did not vote for round r-1 leader vertex. Upon entering round r,  $P_i$  starts a timer which is set to some  $\tau$  time. We will shortly provide more details on the value of  $\tau$ .

We place an additional constraint on the leader vertex. A round r leader vertex needs to either have a strong path to the round r-1 leader vertex or contain a quorum of round r-1 no-vote messages (denoted by  $\mathcal{NVC}_{r-1}$ ). The  $\mathcal{NVC}_{r-1}$  serves as a proof that a quorum of parties did not "vote" for the round r-1 leader vertex. Hence, the round r-1 leader vertex cannot be committed and it is safe to lack a strong path to the round r-1 leader vertex.

Upon delivering a round r vertex v, each party  $P_i$  checks if these constraints are met via is\_valid(v) function. In particular, is\_valid(v) checks whether v consists of either a strong path to round r-1 leader vertex or  $\mathcal{TC}_{r-1}$  (and  $\mathcal{NVC}_{r-1}$  for the round r leader vertex). In addition,  $P_i$  also checks if vertex v consists of at least 2f+1 strong edges to round r-1 vertices. Once these checks are satisfied, vertex v is added to  $DAG_i[r]$  via try\_add\_to\_dag(v) which succeeds when  $P_i$  has delivered all the vertices that have a path from vertex v in the DAG. If try\_add\_to\_dag(v) fails, the vertex is added to the buffer for a later retry. In addition, when try\_add\_to\_dag(v) succeeds, the vertices in the buffer are re-attempted to be added to the  $DAG_i$ .

**Jumping rounds.** Apart from advancing the rounds sequentially, our protocol allows honest parties in round r' < r to "jump" to a higher round r when they observe 2f+1 round r-1 vertices along with round r-1 leader vertex or receive a  $\mathcal{TC}_{r-1}$ . If  $L_r$  is the lagging party, it additionally needs to wait to receive either  $\mathcal{NVC}_{r-1}$  or round r-1 leader vertex in order to propose round r leader vertex. When jumping rounds from r' to r, parties do not propose vertices between those rounds.

Committing and ordering the DAG. In our protocol, only the leader vertices are committed. The non-leader vertices are ordered (in some deterministic order) as part of the causal history of a leader vertex when the leader vertex is (directly or indirectly) committed as shown in order\_vertices function (see Line 22).

The commit rule is presented in Figure 3. An honest party  $P_i$  directly commits a round r leader vertex  $v_k$  when it observes 2f+1 "first messages" (of the RBC) for round r+1 vertices with strong paths to the round r leader vertex, i.e.,  $P_i$  does not need to wait for the RBC of round r+1 vertices to terminate. This is because when the sender of the RBC is honest, the first observed value (i.e., the first message of the RBC) is the value that will eventually be delivered. Among the 2f+1 round r+1 vertices, at least f+1 vertices are sent by honest parties which will eventually be delivered such that the delivered value is equal to the first received

```
Local variables:
      committedRound \leftarrow 0
70: upon receiving a set S of \geq 2f+1 first messages for round r+1
  vertices do
71:
        try\_commit(r, S)
72: procedure try_commit(r, S)
73:
        v \leftarrow \text{get\_leader\_vertex}(r)
74:
        votes \leftarrow \{v' \in \mathcal{S} \mid strong\_path(v', v)\}
75:
        if votes > 2f + 1 then
76:
            commit_leader(v)
77:
    procedure commit_leader(v)
78:
        leaderStack.push(v)
79:
        r \leftarrow v.round - 1
80:
81:
        while r > committedRound do
82:
             v_s \leftarrow \text{get\_leader\_vertex}(r)
83:
            if strong\_path(v', v_s) then
84:
                leaderStack.push(v_s)
85:
                v' \leftarrow v_s
86:
            r \leftarrow r-1
87:
         committedRound \leftarrow v.round
88:
        order vertices()
```

Figure 3: Sailfish: The commit rule for party  $P_i$ 

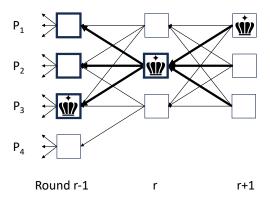


Figure 4: Depicts the view of one of the parties committing blocks from rounds r and r-1. When the party observes 2f+1 votes on the round r leader vertex, it commits that vertex and deterministically orders non-leader vertices from earlier rounds (bold vertices). We assume round r-1 leader vertex was committed earlier. Note that performing this commit requires only the receipt of round r+1 vertices; they do not need to be RBC-delivered. Moreover, since the round r leader does not refer to  $P_4$ 's round r-1 vertex, it is not ordered as yet.

value (in the first message of RBC). This is sufficient to ensure  $\mathcal{NVC}_r$  will not exist and any round r' > r leader vertex (if it exists) will have strong paths to the round r leader vertex; thus ensuring the safety of a commit.

In addition to the above commit rule, our protocol also allows party  $P_i$  to directly commit a round r leader vertex  $v_k$  if it delivers (via RBC) 2f+1 round r+1 vertices that have strong paths to  $v_k$  (see Line 59). This commit rule is helpful in scenarios when the RBC delivers a vertex without having received the first message of the RBC. Such scenarios arise when the sender of the RBC is faulty or during an asynchronous period.

Upon directly committing  $v_k$  in round r,  $P_i$  first indirectly commits leader vertices  $v_m$  in smaller rounds such

that there exists a strong path from  $v_k$  to  $v_m$  (based on its local copy of the DAG) until it reaches a round r' < r in which it previously directly committed a leader vertex. In this protocol, we ensure that when a round r leader vertex  $v_k$  is directly committed by some honest party, leader vertices for any round r' > r have a strong path to  $v_k$ . This ensures  $v_k$  will be (directly or indirectly) committed by all honest parties.

Remark on timeout parameter  $\tau$ . The value of timeout parameter  $\tau$  depends on two factors (i) the underlying RBC primitive used to propagate the vertices, and (ii) how an honest party  $P_i$  entered round r.

Several RBC primitives [1], [2], [8], [32] have been proposed in the literature with various tradeoffs in communication complexity, number of steps required, setup assumptions, etc. For a comprehensive list of RBC primitives, we refer to the recent work by Alhaddad et al. [2]. The value of parameter  $\tau$  should be long enough to ensure that when an honest party enters round r, it can deliver the round r leader vertex broadcast by an honest leader along with 2f+1 round r vertices before its timeout occurs. In particular, when  $P_i$  enters round r, the parameter  $\tau$  should accommodate the time it takes for other honest parties to enter the common round r, including  $L_r$  (if honest) and deliver their round r vertices before the timeout occurs for  $P_i$ .

The timeout parameter  $\tau$  also depends on whether party  $P_i$  entered round r via  $\mathcal{TC}_{r-1}$  or not. When  $\mathcal{TC}_{r-1}$  exists and  $L_r$  does not deliver round r-1 leader vertex,  $L_r$  has to collect  $\mathcal{NVC}_{r-1}$  before proposing a round r leader vertex which may require up to  $2\Delta$  time. Accordingly, party  $P_i$  has to wait for  $2\Delta$  additional time in round r when entering round r via  $\mathcal{TC}_{r-1}$  compared to when it enters round r via receiving round r-1 leader vertex.

The RBC primitive of Das et al. [17] has 4 communication steps and delivers a value within  $4\Delta$  time (see Property 1). In addition, it also ensures that when an honest party delivers a value at time t, all honest parties deliver the value by  $t+2\Delta$  (see Property 2). Accordingly, party  $P_i$  sets its parameter  $\tau$  to  $6\Delta$  when it enters round r after delivering round r-1 leader vertex and to  $8\Delta$  when it enters round r via  $\mathcal{TC}_{r-1}$ . We note that different honest parties may set different values for  $\tau$  depending on how they entered a round.

Intuition behind including a timeout certificate on the vertex. As mentioned above, we place a constraint on all the vertices: a valid round r+1 vertex should either have a strong path to round r leader vertex or include a  $\mathcal{TC}_r$ . This is to prevent Byzantine parties from driving the protocol too fast and prevent an honest leader vertex from getting directly committed (even after GST). Note that our protocol requires 2f+1 round r+1 vertices with strong paths to round r leader vertex for the round r leader vertex to be directly committed. In addition, our protocol also supports parties to "jump" to a higher round r' > r when they observe 2f+1 round r'-1 vertices including the round r'-1 leader vertex or  $\mathcal{TC}_{r'-1}$ . If  $\mathcal{TC}_r$  were not included in the vertex, the f Byzantine parties can propose round r+1 vertices without

strong paths to the round r leader vertex. And, as soon as f+1 honest parties propose round r vertices (with strong paths to the round r leader vertex), the protocol can move to round r+1 while f honest parties are lagging behind in some lower round  $r'' \leq r$ . Relying on the same technique, the protocol can proceed to round r' > r. The adversary can then deliver 2f+1 round r' vertices along with round r' leader vertex to the f lagging honest parties; causing them to enter round r'+1 such that these f lagging honest parties do not propose a round r+1 vertex. This prevents the round r leader vertex from being committed.

After GST, when  $L_r$  is honest, honest parties do not timeout in round r. Therefore, Byzantine parties cannot propose round r+1 vertex without voting for the round r leader vertex, ensuring it gets directly committed.

**Explicit round-synchronization.** Our protocol consists of an explicit round-synchronization via multicasting of timeout messages and  $\mathcal{TC}_r$  when  $L_r$  is faulty. This is to ensure all honest parties can receive  $\mathcal{TC}_r$  and 2f+1 round r vertices within  $2\Delta$  time and send  $\langle \text{no-vote}, r \rangle$  to  $L_{r+1}$ . This allows  $L_{r+1}$  to collect a  $\mathcal{NVC}_r$  in a timely manner and allows all honest parties to receive the round r+1 leader vertex before they timeout in round r+1.

## 3.1. Efficiency Analysis

**Commit latencies.** The commit latency of the leader vertex is the time taken to propagate round r vertices (via RBC), and one communication step required to receive the first messages for 2f+1 round r+1 vertices i.e., one RBC, plus  $1\delta$ . When employing the RBC protocol due to Das et al. [17], the commit latency of the leader vertex is  $5\delta$ . The non-leader vertices require an additional RBC (i.e.  $4\delta$ ) to be committed.

We note that the Bullshark (and Shoal) cannot support a commit with a latency with one RBC, plus  $1\delta$ . This is due to the following reasons. First, Bullshark waits for only f + 1round r+1 vertices with strong paths to round r leader vertex to commit the round r leader vertex. Out of these round r+1 vertices, up to f could be sent by Byzantine parties. If we rely only on the first received value of the RBC (based on the first message), the final delivered value could be different when its sender is faulty. In this case, the final delivered vertices may not have strong path to the round r leader vertex for up to f vertices. A single round r+1 vertex from an honest party with a strong path to the round r leader vertex is insufficient to ensure the safety of a commit. On the other hand, if Bullshark were to be modified to commit upon receiving 2f + 1 round r + 1vertices with strong paths to round r leader vertex, it may fail to commit any leader vertices. This is because Bullshark does not require a round r+1 vertex to include  $\mathcal{TC}_r$  when it does not have a strong path to round r vertex leader. As explained above, this allows Byzantine parties to drive the protocol fast and prevent a commit (even after GST).

Latency analysis under failures. Note that  $\tau$  of our protocol is  $6\Delta$  in the round following an honest leader and  $8\Delta$ 

in the round following a Byzantine leader. The additional timeout is required because the round r leader vertex needs to wait for  $\mathcal{NVC}_{r-1}$  when  $L_{r-1}$  is faulty. In contrast, Bullshark (and Shoal) requires  $\tau$  of  $6\Delta$  in all scenarios (when using the RBC primitive of Das et al. [17]).

Despite our protocol having a slightly larger  $\tau$  compared to Bullshark (and Shoal), the commit latency does not worsen when a single Byzantine failure occurs between two honest leaders. This is because both our protocol and Bullshark (and Shoal) require honest parties to wait for  $6\Delta$  in the round corresponding to the Byzantine leader. In the subsequent round, the honest leader can obtain  $\mathcal{NVC}$  and propose responsively, meaning the increased value of  $\tau$  does not increase latency in practice (when messages arrive in  $\delta < \Delta$  time). In fact, our protocol incurs less latency despite the need to wait for  $\mathcal{TC}$  and  $\mathcal{NVC}$ , primarily due to having a leader every round and smaller commit latency.

As a concrete example, we consider the commit latency of the non-leader vertices of round r-1 when  $L_r$  is Byzantine and both  $L_{r-1}$  and  $L_{r+1}$  are honest. For both our protocol and Bullshark (and Shoal), honest parties need to wait for  $6\Delta$  time in round r. Let t be the time when the first honest party enters round r. Since honest parties may enter round r within  $2\Delta$  of each other, all honest parties receive  $\mathcal{TC}_r$  by time  $t+8\Delta+\delta$  and  $L_{r+1}$  receives  $\mathcal{NVC}_r$  by  $t+8\Delta+2\delta$ . As  $L_{r+1}$  is honest, its leader vertex can be committed in the next  $5\delta$  time; committing round r-1 non-leader vertices in  $8\Delta+11\delta$  time (compared to  $9\delta$  when  $L_r$  is honest.)

In the case of Bullshark (and Shoal), apart from  $6\Delta$  wait in round r, honest parties would need to wait for round r+1 vertices from some honest parties that entered round r late (since honest parties enter a round within  $2\Delta$  of each other). Moreover, in their case, the round r+2 leader vertex is the next vertex to be committed in round r+3. In total, the latency to commit round r-1 non-leader vertices is  $8\Delta+16\delta$  (compared to  $12\delta$  when  $L_r$  is honest, in the case of Shoal). Thus, under a single Byzantine failure between honest leaders, our protocol still performs better compared to both Bullshark and Shoal.

However, when there is a sequence of two or more faulty leaders in between honest leaders, honest parties need to wait for  $\tau$  of  $8\Delta$  time, and hence our protocol would slightly underperform compared to Bullshark (and Shoal) in terms of latency.

**Communication complexity.** The size of each vertex is O(n) since it consists of references to up to n vertices and, may contain  $\mathcal{TC}$  and  $\mathcal{NVC}$ . The size of these certificates is O(1) assuming threshold signatures [7] (O(n) without threshold signatures). In each round, each party propagates a single vertex via RBC. The RBC protocol of Das et al. [17] incurs  $O(n^2)$  communication to propagate O(n)-sized messages. Thus, the total communication complexity is  $O(n^3)$  per round. Similarly, all-to-all multicast of timeout certificates incurs  $O(n^2)$  communication assuming threshold signatures (or  $O(n^3)$  without threshold signatures). Thus, the overall communication complexity is  $O(n^3)$  per round

(when using [17]).

We note that a single vertex can contain O(n) transactions without increasing its size. This results in amortized linear communication complexity per round.

We present detailed security analysis in Appendix B.

## 4. Multi-leader Sailfish

In Sailfish, the latency to commit the leader vertex is shorter than that for non-leader vertices. To improve the latency for multiple vertices, we extend Sailfish to support multiple leaders within a single round. In the best-case scenario, when all these leaders are honest, the respective leader vertices can be committed with a latency of one RBC plus  $1\delta$ .

Multiple leaders in a round. In this protocol, multiple leaders are chosen within a round based on the round number. One of these leaders serves as the main leader, while the others are designated as secondary leaders. The vertex proposed by the main leader is referred to as the main leader vertex, and the vertices proposed by the secondary leaders are termed secondary leader vertices. The responsibilities of the main leader in Multi-leader Sailfish are consistent with those in Sailfish: either the main leader vertex must have a strong path to all leader vertices from the previous round or the main leader must collect a no-vote certificate for any missing leader vertices.

To determine the multiple leaders in a given round, we define a deterministic function, get\_multiple\_leaders(r), which returns an ordered list of leaders for round r. The first leader in this list serves as the main leader, while the subsequent leaders are designated as secondary leaders. Analogous to Sailfish, the main leader for round r is denoted as  $L_r$ . We use  $\mathcal{ML}_r$  to denote the ordered list of leaders provided by get\_multiple\_leaders(r).  $\mathcal{ML}_r[x]$  denotes the  $x^{th}$  element in the list. Additionally,  $\mathcal{ML}_r[x]$  represents the first x leaders, while  $\mathcal{ML}_r[x+1:]$  denotes the leaders in the list excluding the first x leaders.

**DAG construction protocol.** The basic data structures are identical to Sailfish. In order to accommodate multiple leaders in a round, we modify how parties advance rounds. The modified protocol is presented in Figure 5.

Recall that in Sailfish, each party  $P_i$  waits for the round r leader vertex until a timeout. If the leader vertex is not delivered before the timeout,  $P_i$  sends  $\langle \text{timeout}, r \rangle$  message. Upon receiving either the round r leader vertex or  $\mathcal{TC}_r$  (along with 2f+1 round r vertices)  $P_i$  advances to round r+1. When  $P_i$  advances to round r+1 via  $\mathcal{TC}_r$ , it sends  $\langle \text{no-vote}, r \rangle$  to  $L_{r+1}$ . Additionally,  $L_r$  must collect  $\mathcal{NVC}_r$  before proposing a round r+1 leader vertex.

In Multi-leader Sailfish,  $P_i$  sends  $\langle \text{timeout}, r \rangle$  only when it does not deliver the round r main leader vertex before the timeout; it does not send timeout messages when the secondary leader vertices are not delivered.

To handle multiple leaders, various strategies can be employed for advancing through rounds. For instance, party  $P_i$  could wait for all leaders in  $\mathcal{ML}_r$  or  $\mathcal{TC}_r$  (along with 2f+1

```
89: upon timeout do
                                                                                                 116: procedure try_commit(r, S)
90:
         if \not\exists v' \in DAG_i[r] : v'.source = L_{round} then
                                                                                                             \mathcal{CLS} \leftarrow [\ ]
                                                                                                 117:
91:
              multicast \langle timeout, round \rangle_i
                                                                                                 118:
                                                                                                             \mathcal{ML} \leftarrow \text{get\_multiple\_leaders}(r)
                                                                                                 119:
                                                                                                             for p \in \mathcal{ML} do
92:
     procedure advance\_round(r)
                                                                                                 120:
                                                                                                                 v \leftarrow \text{get\_vertex}(p, r)
93:
         \mathcal{ML} \leftarrow \text{get\_multiple\_leaders}(r-1)
                                                                                                  121:
                                                                                                                 votes \leftarrow \{v' \in \mathcal{S} \mid strong\_path(v', v)\}
94:
         for p \in \mathcal{ML} do
                                                       \triangleright iterate over \mathcal{ML} in order
                                                                                                                 if votes \ge 2f + 1 then \mathcal{CLS} \leftarrow \mathcal{CLS} \mid\mid v
                                                                                                 122:
95:
              if \exists v' \in DAG_i[r-1]| : v'.source = p then
                                                                                                 123:
96:
                   send (no-vote, p, r-1)<sub>i</sub> to L_r
                                                                                                 124:
                                                                                                                 else break
97:
         if P_i = L_r then
                                                                                                 125:
                                                                                                             commit_leaders(CLS)
98:
              wait until \exists v' \in DAG_i[r-1] : v'.source = p \ \forall p \in \mathcal{ML}[:x]
                                                                                                  126:
                                                                                                        procedure commit_leaders(cls)
              or \mathcal{NVC}_{r-1}^{p'} is received, where p' = \mathcal{ML}[x+1]
                                                                                                 127:
                                                                                                             leaderStack.push(cls)
         round \leftarrow r; start timer
                                                                                                             v' \leftarrow cls[0]
99:
                                                                                                  128:
100:
           broadcast_vertex(round)
                                                                                                 129:
                                                                                                             r \leftarrow v'.round - 1
                                                                                                  130:
                                                                                                             while r > committedRound do
101: procedure create_new_vertex(r)
                                                                                                 131:
                                                                                                                 \mathcal{CMV} \leftarrow [\ ]
           v.round \leftarrow r
                                                                                                 132:
                                                                                                                 \mathcal{ML} \leftarrow \text{get\_multiple\_leaders}(r)
           v.source \leftarrow P_i
103:
                                                                                                 133:
                                                                                                                 for p \in \mathcal{ML} do
           v.block \leftarrow blocksToPropose.dequeue()
104:
                                                                                                 134:
                                                                                                                      v \leftarrow \text{get\_vertex}(p, r)
           v.strongEdges \leftarrow DAG_i[r-1]
105:
                                                                                                 135:
                                                                                                                      if strong_path(v', v) then
           set\_weak\_edges(v, r)
106.
                                                                                                 136:
                                                                                                                          \mathcal{CMV} \leftarrow \mathcal{CMV} || v
107:
           if \exists v' \in DAG_i[r-1] : v'.source = L_{r-1} then
                                                                                                 137:
                                                                                                                      else break
               v.tc \leftarrow \mathcal{TC}_{r-1}
108:
                                                                                                                 if \mathcal{CMV} \neq [\ ] then v' \leftarrow \mathcal{CMV}[1]
                                                                                                 138:
           if P_i = L_r then
109:
                                                                                                                                                   \triangleright main leader vertex for round r
                                                                                                 139:
110:
               \mathcal{ML} \leftarrow \text{get\_multiple\_leaders}(r-1)
                                                                                                 140:
                                                                                                                 leaderStack.push(\mathcal{CMV})
               for p \in \mathcal{ML} do
111:
                                                                                                 141:
                                                                                                                 r \leftarrow r-1
                    112:
                                                                                                 142:
                                                                                                             committedRound \leftarrow cls[0].round
113:
                                                                                                 143:
                                                                                                             order vertices()
114:
                    break
115:
           return v
144: procedure order_vertices()
145:
           while ¬leaderStack.isEmpty() do
146:
               \mathcal{CMV} \leftarrow leaderStack.pop()
                                                                                                                                                      \triangleright iterate over \mathcal{CMV} in order
147:
               for v \in \mathcal{CMV} do
                    verticesToDeliver \leftarrow \{v' \in \bigcup_{r>0} DAG_i[r] \mid path(v,v') \land v' \not\in deliveredVertices\}
148:
                    for every v' \in verticesToDeliver in some deterministic order do
149.
                        output a_deliver<sub>i</sub>(v'.block, v'.round, v'.source)
150:
                        deliveredVertices \leftarrow deliveredVertices \cup \{v'\}
151:
```

Figure 5: Multi-leader Sailfish

round r vertices) before advancing to round r+1. Upon advancing to round r+1,  $P_i$  sends  $\langle \text{no-vote}, p, r \rangle$  for all  $p \in \mathcal{ML}_r$  from which  $P_i$  did not deliver the corresponding round r leader vertex. In the ideal scenario, when all leaders in  $\mathcal{ML}_r$  are honest and after GST, all honest parties will responsively receive all round r leader vertices and move to round r+1. However, a single faulty leader can cause the protocol to await its leader vertex, thereby slowing down the protocol.

Alternatively, each party  $P_i$  could choose to wait solely for the round r main leader vertex or  $\mathcal{TC}_r$  (along with 2f+1 round r vertices) before progressing to round r+1. Subsequently,  $P_i$  would send  $\langle \text{no-vote}, p, r \rangle$  for all  $p \in \mathcal{ML}_r$  from which  $P_i$  did not receive the round r leader vertex by the time it advances to round r+1. While this approach prioritizes the fastest leaders in  $\mathcal{ML}_r$  for voting, it may result in slow leaders not being voted on, potentially causing the the slow leaders to not achieve the best possible latency.

We adjust the constraint on the main leader vertex as follows: The round r+1 main leader vertex must establish strong paths to all leader vertices corresponding to leaders in  $\mathcal{ML}_r[:x]$  (for some x>0) and include a quorum of  $\langle \text{no-vote}, p, r \rangle$  (referred to as  $\mathcal{NVC}_r^p$ ), where  $p=\mathcal{ML}_r[x+1]$ 

1] (see Line 98). If the main leader vertex has strong paths to all leader vertices corresponding to leaders in  $\mathcal{ML}_r$ , it is not required to include  $\mathcal{NVC}_r$  for any round r leaders. The constraint on other round r+1 vertices remain unchanged; specifically, the round r+1 vertex must include  $\mathcal{TC}_r$  only if it lacks a strong path to the round r main leader vertex. The is\_valid() function is also appropriately updated to ensure that these constraints are met.

Committing and ordering the DAG. Similar to Sailfish, only the leader vertices are committed, and the non-leader vertices are ordered (in some deterministic order) as part of the causal history of a leader vertex when the leader vertex is (directly or indirectly) committed, as illustrated in the order\_vertices function (refer to Line 144).

In this protocol, an honest party  $P_i$  directly commits a round r leader vertex  $v_k$  corresponding to  $\mathcal{ML}_r[x]$  when it observes 2f+1 "first messages" (of the RBC) for round r+1 vertices with strong paths to the vertex  $v_k$  and when all round r leader vertices corresponding to leaders in  $\mathcal{ML}_r[:x-1]$  have been directly committed. If  $v_k$  fails to be directly committed, party  $P_i$  refrains from committing the leader vertices corresponding to the leaders in  $\mathcal{ML}_r[x+1:]$ ,

even if there are 2f+1 round r+1 vertices with strong paths to the leader vertices corresponding to the leaders in  $\mathcal{ML}_r[x+1:]$ . We will shortly explain why it is necessary to skip committing leader vertices corresponding to leaders in  $\mathcal{ML}_r[x+1:]$  in this case. The commit rule is presented in try\_commit() function (see Line 126).

In addition to the above commit rule, Multi-leader Sailfish also enables party  $P_i$  to directly commit round r leader vertex  $v_k$  corresponding to  $\mathcal{ML}_r[x]$  when it delivers (via RBC) 2f+1 round r+1 vertices that have strong paths to  $v_k$  and when all round r leader vertices corresponding to leaders in  $\mathcal{ML}_r[:x-1]$  have been directly committed.

Upon directly committing the main leader vertex  $v_m$  in round r,  $P_i$  first indirectly commits leader vertices corresponding to  $\mathcal{ML}_{r'}[: y]$  (for some y > 0) in an earlier round r' < r such that there exists strong paths from  $v_m$  to all leader vertices corresponding to  $\mathcal{ML}_{r'}[: y]$ . Subsequently, this process of indirectly committing leader vertices of earlier rounds is repeated for leader vertices that have strong paths from leader vertex corresponding to  $\mathcal{ML}_{r'}[1]$  (i.e., the main leader vertex of round r') until it reaches a round  $r^* < r$  in which it previously directly committed a leader vertex (see Line 126). When round r'leader vertices corresponding to leaders in  $\mathcal{ML}_{r'}[:y]$  are directly committed, we ensure that any future main leader vertex has a strong path to these round r' leader vertices. This ensures that these leader vertices will be (directly or indirectly) committed by honest parties who missed directly committing these leader vertices.

The order\_vertices() function is also appropriately modified to handle multiple leaders in a round (see Line 144).

Intuition behind skipping leaders in  $\mathcal{ML}_r[x+1:]$  when  $\mathcal{ML}_r[x]$  is not directly committed. As mentioned earlier, if  $P_i$  does not directly commit a leader vertex  $v_k$  corresponding to  $\mathcal{ML}_r[x]$ , it also refrains from committing the leader vertices for the leaders in  $\mathcal{ML}_r[x+1:]$ , even if there are sufficient votes for these leader vertices. This precaution is taken because the main leader vertex of a higher round r'' > r may still have a strong path to  $v_k$ . When this main leader vertex from round r'' is committed, the leader vertices corresponding to  $\mathcal{ML}_r[:y]$  (for some y>0) are also indirectly committed in order, provided there are strong paths from the round r'' main leader vertex to the leader vertices corresponding to  $\mathcal{ML}_r[:y]$ . If y > x,  $v_k$  would be committed before the leader vertices corresponding to the leaders in  $\mathcal{ML}_r[x+1:]$ . By skipping the commit of leader vertices corresponding to  $\mathcal{ML}_r[x+1:]$ , we ensure the total order property during the indirect commit.

Additional conditions required for committing the secondary leader vertices. We note two additional conditions required for committing the secondary leader vertices. First, to commit leader vertices corresponding to  $\mathcal{ML}_r[x+1:]$ , the leader vertex corresponding to  $\mathcal{ML}_r[x]$  must be committed beforehand. When  $\mathcal{ML}_r[x]$  is faulty, all leader vertices corresponding to leaders in  $\mathcal{ML}_r[x+1:]$  fail to be committed, despite having sufficient votes for these leader vertices. To address this concern, leader reputation schemes [37], [42]

can be employed to elect multiple leaders with a good reputation for a given round.

Secondly, recall that parties send  $\langle timeout, r \rangle$  messages only when the round r main leader vertex is not delivered in a timely manner. The requirement for a round r vertex to include  $TC_{r-1}$  when it lacks a strong path to the round r-1main leader vertex (say  $v_k$ ) can only prevent the Byzantine parties from proposing the round r vertex without a strong path to  $v_k$ . This ensures that sufficient honest parties vote for  $v_k$  in round r and  $v_k$  is committed by round r, after GST. However, this does not prevent Byzantine parties from "not voting" for the secondary leader vertices and send round rvertices with strong path only to  $v_k$ . With the help of f+1honest parties who vote for the secondary leader vertices, the adversary can cause the protocol to advance to a higher round r' > r while f honest parties are lagging behind in some lower round  $r'' \le r-1$ . The adversary can then deliver 2f + 1 round r' vertices along with round r' main leader vertex to the f lagging honest parties; causing them to enter round r'+1 such that these f lagging honest parties do not propose a round r vertex. This prevents the round r-1secondary leader vertices from being directly committed. This issue can potentially be addressed by introducing a timeout certificate for each leader in a round and requiring a round r vertex to include a timeout certificate for each missing round r-1 leader vertex; however the solution is less practical due to added synchronization overhead and increase in size of a vertex.

In this context, Multi-leader Sailfish ensures that the round r secondary leader vertices are committed by round r+1 only under an "optimistic condition" where at least 2f+1 parties (including Byzantine parties) "vote" for the proposed secondary leader vertices. Under normal conditions, these vertices will be committed in the next round when the round r+1 leader vertex is committed. We also note that these conditions apply to Mysticeti [4], although they did not explicitly state the latter requirement.

## 4.1. Efficiency Analysis

Commit latencies. We analyze the commit latencies under the optimistic condition where all parties vote for all proposed leader vertices. If parties wait for all leader vertices corresponding to  $\mathcal{ML}_r$ , and all leaders in  $\mathcal{ML}_r$  are honest, the corresponding leader vertices can be committed with a latency of one RBC plus  $1\delta$ . However, a single faulty leader can cause the protocol to await its leader vertex, resulting in a latency of  $O(\Delta)$ .

Alternatively, when parties wait solely for the round r main leader vertex before advancing to the next round, the subsequent main leader needs to collect  $\mathcal{NVC}_r$  for leaders for which it lacks strong paths to the corresponding leader vertices, incurring an additional  $1\delta$  time. Thus, the commit latency for the leader vertices is one RBC plus  $2\delta$ , while the non-leader vertices require an additional RBC. Under this strategy, when using the RBC protocol of Das et al. [17], as long as  $x > \frac{n-f+4}{4}$  leader vertices are directly committed in a round, the average latency is still better compared to Sailfish.

TABLE 2: Ping latencies (in ms) between GCP regions

		Destination*			
Source	us-e-1	us-w-1	eu-n-1	as-ne-1	au-se-1
us-east1-a	0.75	66.14	114.75	160.28	197.98
us-west1-a	66.15	0.66	158.13	89.56	138.33
europe-north1-a	115.40	158.38	0.69	245.15	295.13
asia-northeast1-a	159.89	90.05	246.01	0.66	105.58
australia-southeast1-a	197.60	139.02	294.36	108.26	0.58

<sup>\*</sup>Region names are abbreviated versions of the source regions.

Communication complexity. In Multi-leader Sailfish, unlike Sailfish, each party can send a no-vote message for every leader in  $\mathcal{ML}_r$  to the subsequent leader  $L_{r+1}$ . Even with a linear number of leaders in a round, sending these no-vote messages incurs only  $O(n^2)$  bits. Additionally, although  $\mathcal{NVC}_r$  can exist for multiple leaders in round r, the main leader vertex of round r+1 has to incorporate a single  $\mathcal{NVC}_r$ . Therefore, the communication complexity of Multi-leader Sailfish remains the same as that of Sailfish.

We present detailed security analysis in Appendix C.

### 5. Evaluation

We evaluate the performance of Sailfish and Multileader Sailfish, comparing their throughput and latency with modular DAG-based BFT protocols: Bullshark and Shoal, across different system sizes and under failure scenarios.

Implementation details. Our implementation modifies the core consensus logic of the open-source Bullshark [36] to create Sailfish and Multi-leader Sailfish. We also created a custom implementation of Shoal (since their implementation is not publicly accessible) which guarantees a leader in every round and commits the leader vertex with two RBCs. This customized Shoal implementation is better as it does not require reinterpreting the DAG. This implementation focuses purely on consensus, excluding leader reputation.

In the Bullshark implementation, the system has distinct clients, workers, and consensus nodes. Each consensus node includes several workers. Clients send transactions to their designated worker, which aggregates them into a batch and forwards it to workers of other consensus nodes. Upon receiving a batch, a worker sends an acknowledgment back to the originating worker. Once a worker receives a quorum of acknowledgments, it sends the batch digest to its associated consensus node, which then includes this digest in its next proposal.

Experimental setup. We carried out our evaluations on the Google Cloud Platform (GCP), distributing nodes evenly across five distinct GCP regions: us-east1-b (South Carolina), us-west1-a (Oregon), europe-north1-a (Hamina, Finland), asia-northeast1-a (Tokyo), and australia-southeast1-a (Sydney). We employed e2-standard-32 instances [34], each featuring 32vCPUs, 128GB of memory, and up to 16Gbps network bandwidth [35]. All nodes ran on Ubuntu 20.04, and we summarize round-trip latencies in Table 2. We used ED25519 signatures for authentication.

In our setup, one client and one worker is co-located within the consensus node. Each transaction is composed of 512 random bytes, and the batch size is configured to 500KB. We set the timeout parameter,  $\tau$  to 3 seconds. Each experimental run spans 180 seconds, and the data presented in the graphs represents an average across three independent runs. For latency, we measured the average time between the creation of a transaction (or a vertex) and its commit by all (non-faulty) nodes to compute the end-to-end (or consensus) latency. Throughput is measured as the number of committed transactions per second.

**Methodology.** In our evaluations, we gradually increased the input transactions. As depicted in Figure 9 (in Appendix), the throughput increases with increasing load without increasing latency up to a certain point before reaching saturation. After saturation, the latency starts to increase while the throughput either remains consistent or slightly increases. In the subsequent figures, we report the throughput and latency just before reaching this saturation point.

Performance of Sailfish under fault-free case. We initially assess the performance of Bullshark, Shoal, and Sailfish under fault-free scenarios across system sizes of 10, 20, and 50 nodes. The consensus latencies are presented in Figure 6a, while the corresponding end-to-end latencies and throughput are illustrated in Figure 6b and Figure 6c respectively. For completeness, we also present throughput-vs-latency graph at varying input in Figure 9.

In Figure 6a, LV represents the average latency to commit the leader vertices, NLV represents the average latency to commit the non-leader vertices a round before the leader vertex and Avg represents the average latency for all vertices (including those from prior rounds that were ordered when the leader vertex was committed). For Bullshark, the NLV latency is the average latency to commit the two layers of non-leader vertices before the leader round.

Consistent with our theoretical analysis, Sailfish significantly outperforms both Bullshark and Shoal in terms of these latencies. While Bullshark and Shoal achieve similar latencies for the leader vertex, Bullshark's additional layer of non-leader vertices results in higher latency compared to Shoal for non-leader vertices. The improvement in consensus latencies directly translates to an improvement in the overall end-to-end latency. As depicted in Figure 6b, Sailfish reduces the end-to-end latency by approx. 20% compared to Bullshark and Shoal across all system sizes. Furthermore, due to reduced latency of Sailfish, it achieves improved throughput before experiencing a latency spike as depicted in Figure 9 and Figure 6c.

TABLE 3: Consensus latencies (in ms) under failures at n = 10

	Leader vertices	Non-leader vertices	Average
Sailfish	754	2592	3234
Shoal [37]	1175	3003	6829
Bullshark [38]	1169	4960	7005

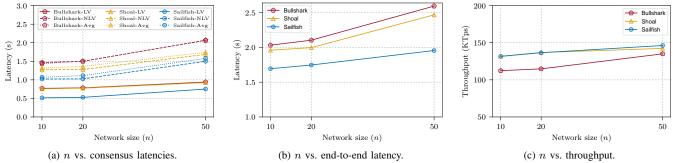


Figure 6: Performance in the absence of failures across different system sizes.

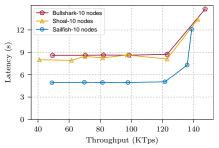


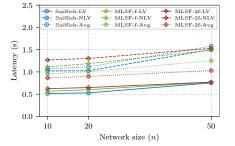
Figure 7: Throughput vs. end-to-end latency at n=10 with 3 failures and varying input

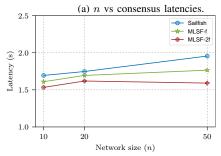
Performance of Sailfish under failures. We evaluated the performance under f crash failures at n=10, distributing the failed leaders across consecutive odd rounds. For Sailfish, this means a leader failure every other round over 2f rounds. In Bullshark, where leaders are designated exclusively in odd rounds, this equated to f consecutive leader failures. Consequently, Bullshark fails to commit for the first 2f rounds, with this pattern repeating every n rounds. Shoal, which relies on a Bullshark instance to commit some vertex before starting a new instance, also does not initiate a new instance until 2f rounds have passed.

Commit latency for the leader vertex increases slightly for all protocols compared to fault-free scenarios, as shown in Table 3. In fault-free cases, protocols commit with the fastest 2f+1 nodes. With f failures, the protocol must wait for all nodes, increasing commit latency for the leader vertex. Additionally, rounds with a failed leader incur  $\tau$  time, and non-leader vertices of failed rounds are only committed when the leader vertex of the following round is committed. This results in increased average latency for committing non-leader vertices for all protocols.

In Bullshark and Shoal, vertices of the first 2f rounds are only committed after 2f rounds, leading to worse average latency. As Sailfish supports a leader in every round, it can commit every other round, resulting in approx. 50% lesser average latency. We present the corresponding throughput and end-to-end latency in Figure 7. With the increased average commit latency, the end-to-end latency for Bullshark and Shoal worsens compared to Sailfish, while the throughput remains (almost) the same as the failure-free case.

## Performance of Multi-leader Sailfish under fault-free





(b) n vs. end-to-end latency. Figure 8: Latency comparison of Multi-leader Sailfish in the absence of failures across different system sizes.

**case.** We also evaluated the performance of Multi-leader Sailfish in failure-free scenarios, exploring configurations with both f and 2f leaders in a round. To simplify implementation, we adopted the strategy where nodes wait for all leader vertices before advancing to the next round. The corresponding consensus and end-to-end latencies are presented in Figure 8. In Figure 8, MLSF-f represents Multi-leader Sailfish with f leaders, while MLSF-2f represents Multi-leader Sailfish with 2f leaders.

As depicted in Figure 8a, the latency to commit the leader vertex (and the non-leader vertices) increased slightly due to the necessity of waiting for all leader vertices in a round. Nonetheless, the average commit latency exhibits significant improvement as more vertices are committed with reduced latency. This improvement in consensus latencies also translates to improved end-to-end latency. As illustrated in Figure 8b, we observe improved end-to-end latencies as the number of leader vertices increases.

### 6. Related Work

Extensive body of research has aimed to enhance the performance of BFT consensus protocols. DAG-based BFT protocols has emerged as a means to enhance the throughput of BFT consensus protocols. We review the most recent and closely related works below (additional related work in Appendix A). Compared to these protocols, our protocols require one RBC plus  $1\delta$  to commit the leader vertex and an additional RBC to commit the non-leader vertices. Our protocol also supports multiple leaders in a round.

**Asynchronous DAG-based BFT.** Hashgraph [5] builds an unstructured DAG with each vertex referencing two previous ones and then runs an inefficient binary agreement protocol, resulting in expected exponential time complexity. Aleph [24] constructs a structured, round-based DAG where parties advance rounds after receiving 2f + 1 vertices from the same round. An asynchronous binary agreement protocol then decides vertex order, leading to higher commit latency.

DAG-Rider [28] is an asynchronous DAG-based BFT protocol that progresses in waves, each consisting of 4 rounds. There is a single leader in each wave and it requires an expected 6 rounds (i.e., 6 sequential RBCs) to commit the leader vertex, and an additional 4 rounds to commit non-leader vertices that share a round with the leader vertex. Tusk [15] is implementation based on DAG-Rider.

Recently, GradedDAG [13] and LightDAG [12] improve the latency of asynchronous DAG-based BFT protocols by using weaker primitives like consistent broadcast [40] instead of RBC. While these weaker primitives reduce latency in fault-free cases, they necessitate downloading missing vertices during failures, increasing overall latency.

Partially synchronous DAG-based BFT. Blockmania [14] uses a modified PBFT [10] for vertex dissemination and constructs a structured round-based DAG. Their protocol is specifically designed for owned objects [6], and it does not ensure the total ordering of these vertices. Bullshark [38], [39] builds upon DAG-Rider to improve the commit latency during the synchronous period. The partially synchronous version has one leader every two rounds. It requires 2 RBCs to commit a leader vertex and an additional 2 RBCs to commit the non-leader vertices that share a round with the leader vertex. Furthermore, Bullshark relies on an honest leader to synchronize all parties post the GST, committing a vertex only after such synchronization. Consequently, it demands two honest leaders to successfully commit a vertex after GST, leading to latency issues in case of frequent transitions between synchrony and asynchrony in the network. Our protocol supports commit with a single honest leader after GST through explicit round synchronization.

Shoal [37] proposed a pseudo-pipelining approach to reduce the latency of non-leader vertices in Bullshark by invoking multiple instance of Bullshark sequentially. When Bullshark fails to commit, Shoal requires an additional two rounds to commit some vertex and start a new Bullshark instance, compromising its ability to guarantee a leader vertex in each round. While Shoal introduced leader reputation

system to elect good leaders and ensure a leader vertex in each round, leader reputation takes time to build and can prevent slow honest nodes from becoming leader, introducing fairness concerns and are potentially inapplicable when parties change over time. Shoal also supports multiple leaders in a round. However, it again relies on executing multiple instances of Bullshark sequentially, each with a different leader. As Bullshark is inherently designed as a single-leader protocol which ensures the commitment of only one leader vertex per round (after GST), reinterpreting the existing DAG with a different leader does not guarantee the new leader will be committed, even if the new leader is honest.

Cordial Miners [29] introduced a DAG-based BFT protocol using BEB instead of RBC to propagate vertices, aiming to reduce latency. Their protocol achieves a commit latency of  $3\delta$  for the leader vertex but requires  $6\delta$  for nonleader vertices aligned with the leader round. Building on Cordial Miners, Mysticeti [4] introduces support for multiple leaders within the same round. However, their approach for committing multiple leaders is optimistic in nature with no guarantee that these multiple leader vertices be committed even after GST. It also does not detail necessary conditions to ensure these leaders are committed. In addition, when Byzantine parties selectively send their proposals to only some honest parties, extra latency is required to download missing vertices increasing overall latency. Both Cordial Miners and Mysticeti have a high communication complexity of  $O(n^4)$  per round and lack modularity. Both protocols also incur higher latency in the event of leader failure as they need to wait in each (non-RBC) round. In comparison, the protocols that rely on RBC can employ a single wait for multiple steps of RBC in a round, resulting in reduced latency.

BBCA-chain [31] relies on a traditional leader-heavy BFT protocol inherently capable of accommodating a leader in each round. At the end of each round, each party sends a block of transactions (via BEB) along with the commit status of the current round and the commit certificate for the highest round it have observed. The next leader aggregates a quorum of these messages in its proposal and uses singleshot PBFT [10] instance to propose its block. However, when the Byzantine parties "selectively" send their proposals only to the leader, additional latency is incurred to download the missing proposals and the leader is responsible for propagating O(n) proposals. When the size of each proposal is O(n) bits, (which is typically the case with DAG-based BFT), the leader is responsible to disseminate  $O(n^2)$  bits; placing a heavier burden on the leader. In comparison, in our protocol (and DAG-based BFT protocols in general), each party performs the same amount of work.

#### References

[1] Ittai Abraham, Kartik Nayak, Ling Ren, and Zhuolun Xiang. Goodcase latency of byzantine broadcast: A complete categorization. In *Proceedings of the 2021 ACM Symposium on Principles of Distributed Computing*, pages 331–341, 2021.

- [2] Nicolas Alhaddad, Sourav Das, Sisi Duan, Ling Ren, Mayank Varia, Zhuolun Xiang, and Haibin Zhang. Balanced byzantine reliable broadcast with near-optimal communication and improved computation. In Proceedings of the 2022 ACM Symposium on Principles of Distributed Computing, pages 399–417, 2022.
- [3] Balaji Aurn, Zekun Li, Florian Suri-Payer, Das Sourva, and Alexander Spiegelman. Shoal++: High throughput dag bft can be fast! arXiv preprint 2405.20488, 2024.
- [4] Kushal Babel, Andrey Chursin, George Danezis, Lefteris Kokoris-Kogias, and Alberto Sonnino. Mysticeti: Low-latency dag consensus with fast commit path. arXiv preprint arXiv:2310.14821, 2023.
- [5] Leemon Baird. The swirlds hashgraph consensus algorithm: Fair, fast, byzantine fault tolerance. Swirlds Tech Reports SWIRLDS-TR-2016-01, Tech. Rep, 34:9–11, 2016.
- [6] Mathieu Baudet, George Danezis, and Alberto Sonnino. Fastpay: High-performance byzantine fault tolerant settlement. In *Proceedings* of the 2nd ACM Conference on Advances in Financial Technologies, pages 163–177, 2020.
- [7] Dan Boneh, Ben Lynn, and Hovav Shacham. Short signatures from the weil pairing. *Journal of cryptology*, 17:297–319, 2004.
- [8] Gabriel Bracha. Asynchronous byzantine agreement protocols. *Information and Computation*, 75(2):130–143, 1987.
- [9] Jan Camenisch, Manu Drijvers, Timo Hanke, Yvonne-Anne Pignolet, Victor Shoup, and Dominic Williams. Internet computer consensus. In *Proceedings of the 2022 ACM Symposium on Principles of Distributed Computing*, pages 81–91, 2022.
- [10] Miguel Castro, Barbara Liskov, et al. Practical byzantine fault tolerance. In OSDI, number 1999 in 99, pages 173–186, 1999.
- [11] Shir Cohen, Rati Gelashvili, Lefteris Kokoris Kogias, Zekun Li, Dahlia Malkhi, Alberto Sonnino, and Alexander Spiegelman. Be aware of your leaders. In *International Conference on Financial Cryptography and Data Security*, pages 279–295. Springer, 2022.
- [12] Xiaohai Dai, Guanxiong Wang, Jiang Xiao, Zhengxuan Guo, Rui Hao, Xia Xie, and Hai Jin. Lightdag: A low-latency dag-based bft consensus through lightweight broadcast. *Cryptology ePrint Archive*, 2024.
- [13] Xiaohai Dai, Zhaonan Zhang, Jiang Xiao, Jingtao Yue, Xia Xie, and Hai Jin. Gradeddag: An asynchronous dag-based bft consensus with lower latency. *Cryptology ePrint Archive*, 2024.
- [14] George Danezis and David Hrycyszyn. Blockmania: from block dags to consensus. arXiv preprint arXiv:1809.01620, 2018.
- [15] George Danezis, Lefteris Kokoris-Kogias, Alberto Sonnino, and Alexander Spiegelman. Narwhal and tusk: a dag-based mempool and efficient bft consensus. In *Proceedings of the Seventeenth European Conference on Computer Systems*, pages 34–50, 2022.
- [16] Sourav Das, Sisi Duan, Shengqi Liu, Atsuki Momose, Ling Ren, and Victor Shoup. Asynchronous consensus without trusted setup or public-key cryptography. Cryptology ePrint Archive, 2024.
- [17] Sourav Das, Zhuolun Xiang, and Ling Ren. Asynchronous data dissemination and its applications. In *Proceedings of the 2021 ACM SIGSAC Conference on Computer and Communications Security*, pages 2705–2721, 2021.
- [18] Isaac Doidge, Raghavendra Ramesh, Nibesh Shrestha, and Joshua Tobkin. Moonshot: Optimizing chain-based rotating leader bft via optimistic proposals. In *International Conference on Dependable* Systems and Networks (DSN), 2024.
- [19] Sisi Duan, Michael K Reiter, and Haibin Zhang. Beat: Asynchronous bft made practical. In Proceedings of the 2018 ACM SIGSAC Conference on Computer and Communications Security, pages 2028– 2041, 2018.
- [20] Sisi Duan, Xin Wang, and Haibin Zhang. Fin: Practical signature-free asynchronous common subset in constant time. In *Proceedings of the* 2023 ACM SIGSAC Conference on Computer and Communications Security, pages 815–829, 2023.

- [21] Cynthia Dwork, Nancy Lynch, and Larry Stockmeyer. Consensus in the presence of partial synchrony. *Journal of the ACM (JACM)*, 35(2):288–323, 1988.
- [22] EigenLabs. Intro to eigenda: Hyperscale data availability for rollups, 2023. Accessed on March 20, 2024.
- [23] Ethereum. Data availability ethereum.org, 2024. Accessed on March 20, 2024.
- [24] Adam Gκagol, Damian Leśniak, Damian Straszak, and Michał Świκetek. Aleph: Efficient atomic broadcast in asynchronous networks with byzantine nodes. In Proceedings of the 1st ACM Conference on Advances in Financial Technologies, pages 214–228, 2019.
- [25] Rati Gelashvili, Lefteris Kokoris-Kogias, Alberto Sonnino, Alexander Spiegelman, and Zhuolun Xiang. Jolteon and ditto: Network-adaptive efficient consensus with asynchronous fallback. In *International Conference on Financial Cryptography and Data Security*, pages 296–315. Springer, 2022.
- [26] Bingyong Guo, Yuan Lu, Zhenliang Lu, Qiang Tang, Jing Xu, and Zhenfeng Zhang. Speeding dumbo: Pushing asynchronous bft closer to practice. In 29th Annual Network and Distributed System Security Symposium, NDSS 2022. The Internet Society, 2022.
- [27] Bingyong Guo, Zhenliang Lu, Qiang Tang, Jing Xu, and Zhenfeng Zhang. Dumbo: Faster asynchronous bft protocols. In *Proceedings* of the 2020 ACM SIGSAC Conference on Computer and Communications Security, pages 803–818, 2020.
- [28] Idit Keidar, Eleftherios Kokoris-Kogias, Oded Naor, and Alexander Spiegelman. All you need is dag. In *Proceedings of the 2021 ACM Symposium on Principles of Distributed Computing*, pages 165–175, 2021
- [29] Idit Keidar, Oded Naor, Ouri Poupko, and Ehud Shapiro. Cordial miners: Fast and efficient consensus for every eventuality. In 37th International Symposium on Distributed Computing (DISC 2023). Schloss-Dagstuhl-Leibniz Zentrum für Informatik, 2023.
- [30] Dahlia Malkhi and Kartik Nayak. Hotstuff-2: Optimal two-phase responsive bft. *Cryptology ePrint Archive*, 2023.
- [31] Dahlia Malkhi, Chrysoula Stathakopoulou, and Maofan Yin. Bbcachain: One-message, low latency bft consensus on a dag. In *Inter*national Conference on Financial Cryptography and Data Security, 2024.
- [32] Andrew Miller, Yu Xia, Kyle Croman, Elaine Shi, and Dawn Song. The honey badger of bft protocols. In *Proceedings of the 2016 ACM SIGSAC conference on computer and communications security*, pages 31–42, 2016.
- [33] Kartik Nayak, Ling Ren, Elaine Shi, Nitin H Vaidya, and Zhuolun Xiang. Improved extension protocols for byzantine broadcast and agreement. In 34th International Symposium on Distributed Computing, 2020.
- [34] [n.d.]. Google Cloud Platform general purpose machines. https:// cloud.google.com/compute/docs/general-purpose-machines. [Online; accessed 06/03/2024].
- [35] [n.d.]. Google Cloud Platform Network Bandwidth. https: //cloud.google.com/compute/docs/network-bandwidth. [Online; accessed 06/03/2024].
- [36] Facebook Research. Bullshark github repository. https://github.com/ facebookresearch/narwhal/tree/bullshark. [Online; accessed 20-April-20241.
- [37] Alexander Spiegelman, Balaji Aurn, Rati Gelashvili, and Zekun Li. Shoal: Improving dag-bft latency and robustness. In *International Conference on Financial Cryptography and Data Security*, 2024.
- [38] Alexander Spiegelman, Neil Giridharan, Alberto Sonnino, and Lefteris Kokoris-Kogias. Bullshark: Dag bft protocols made practical. In Proceedings of the 2022 ACM SIGSAC Conference on Computer and Communications Security, pages 2705–2718, 2022.

- [39] Alexander Spiegelman, Neil Giridharan, Alberto Sonnino, and Lefteris Kokoris-Kogias. Bullshark: The partially synchronous version. arXiv preprint arXiv:2209.05633, 2022.
- [40] TK Srikanth and Sam Toueg. Simulating authenticated broadcasts to derive simple fault-tolerant algorithms. *Distributed Computing*, 2:80–94, 1987.
- [41] Espresso Systems. Designing the espresso sequencer: Combining hotshot consensus with tiramisu da - hackmd, 2023. Accessed on March 20, 2024.
- [42] Giorgos Tsimos, Anastasios Kichidis, Alberto Sonnino, and Lefteris Kokoris-Kogias. Hammerhead: Leader reputation for dynamic scheduling. arXiv preprint arXiv:2309.12713, 2023.
- [43] Maofan Yin, Dahlia Malkhi, Michael K Reiter, Guy Golan Gueta, and Ittai Abraham. Hotstuff: Bft consensus with linearity and responsiveness. In *Proceedings of the 2019 ACM Symposium on Principles of Distributed Computing*, pages 347–356, 2019.

## Appendix A. Additional Related Work

Subsequent work. A day before the submission deadline, we noticed a new protocol Shoal++ [3] uploaded on arXiv, which extends Shoal to commit the leader vertex with one RBC plus  $1\delta$ . However, if Byzantine parties drive the protocol fast, this commit rule is not triggered, and it falls back to Shoal, committing the leader vertex with 2 RBCs. Like Shoal, it relies on a leader reputation system to support a leader in each round, inheriting the same limitations as Shoal. In comparison, our protocol prevents Byzantine parties from driving the protocol fast, ensuring our commit rule is always triggered with an honest leader after GST. Moreover, our protocol truly supports a leader vertex in each round.

**Asynchronous BFT protocols.** Fully asynchronous BFT protocols [19], [26], [27], [32] also utilize n parallel RBCs to propagate the proposals, followed by asynchronous common subset agreement [32] or multi-value validated Byzantine agreement [27] to agree on at least n-f proposals. These protocols are primarily designed for single shot consensus and terminate there after.

Fully asynchronous BFT protocols have been studied under various setup assumptions, such as without signatures [20] or without public key cryptography [16]. When using a signature-free RBC [17], Sailfish is also signature-free in common case executions without failures and after GST, as it requires signatures only for no-vote and timeout messages and certificates. It is an interesting direction to explore fully signature-free version of Sailfish.

# Appendix B. Security Analysis of Sailfish

We say that a leader vertex  $v_i$  is committed directly by party  $P_i$  if  $P_i$  invokes commit\_leader( $v_i$ ). Similarly, we say that a leader vertex  $v_j$  is committed indirectly if it is added to leader Stack in Line 84. In addition, we say party  $P_i$  consecutively directly commit leader vertices  $v_k$  and

 $v_{k'}$  if  $P_i$  directly commits  $v_k$  and  $v_{k'}$  in rounds r and r' respectively and does not directly commit any leader vertex between r and r'.

The following fact is immediate from using reliable broadcast to propagate a vertex v and waiting for the entire causal history of v to be added to the DAG before adding v.

**Fact 1.** For every two honest parties  $P_i$  and  $P_j$  (i) for every round r,  $\bigcup_{r' \leq r} DAG_i[r']$  is eventually equal to  $\bigcup_{r' \leq r} DAG_j[r']$ , (ii) at any given time t and round r, if  $v \in DAG_i[r] \land v' \in DAG_j[r]$  s.t. v.source = v'.source, then v = v'. Moreover, for every round r' < r, if  $v'' \in DAG_i[r']$  and there is a path from v to v'', then  $v'' \in DAG_j[r']$  and there is a path from v' to v''.

**Claim 1.** If an honest party  $P_i$  directly commits a leader vertex  $v_k$  in round r, then for every leader vertex  $v_\ell$  in round r' such that r' > r, there exists a strong path from  $v_\ell$  to  $v_k$ .

*Proof.* Since  $P_i$  directly committed  $v_k$  in round r, there exists a set  $\mathcal Q$  of 2f+1 round r+1 vertices that included a reference to vertex  $v_k$ . Let  $\mathcal H\subset \mathcal Q$  be the set of vertices proposed by honest parties in  $\mathcal Q$ . We complete the proof by showing the statement holds for any r'>r.

Case r'=r+1: If  $v_\ell\in\mathcal{H}$ , we are trivially done. Otherwise, the vertices in  $\mathcal{H}$  are from round r+1 honest non-leader parties. When a round r+1 honest non-leader party  $P_i$  includes a reference to vertex leader  $v_k$ , it does not send a round r no-vote message. Since  $|\mathcal{H}|\geq f+1$ , by standard quorum intersection argument,  $\mathcal{NVC}_r$  does not exist. Moreover, parties in  $\mathcal{H}$  have delivered  $v_k$ . By Fact 1,  $L_{r+1}$  will eventually deliver  $v_k$ . Thus, if  $v_\ell$  exists, it must include a reference to  $v_k$  and there exists a strong path from  $v_\ell$  to  $v_k$ .

Case r'>r+1: Note that all vertices in  $\mathcal H$  will eventually be delivered by all honest parties and included in DAG[r+1]. Additionally, a round r+2 vertex has a strong path to 2f+1 round r+1 vertices. By standard quorum intersection, this includes at least 1 vertex in  $\mathcal H$  which has a strong path to  $v_k$ . Thus, all-round r+2 vertices (including round r+2 leader vertex) have a strong path to  $v_k$ . Moreover, each round r''>r+2 vertex has strong paths to at least 2f+1 vertices in round r''-1. By transitivity, each vertex at round r'' has strong paths to at least 2f+1 vertices in round r'' as strong paths to at least r0 round r1. This implies r1 which have a strong path to r2. This implies r3 round have a strong path to r4.

**Claim 2.** If an honest party  $P_i$  directly commits a leader vertex  $v_k$  in round r and an honest party  $P_j$  directly commits a leader vertex  $v_\ell$  in round  $r' \geq r$ , then  $P_j$  (directly or indirectly) commits  $v_k$  in round r.

*Proof.* If r' = r, by Fact 1,  $v_k = v_\ell$  and we are trivially done. When r' > r, by Fact 1 and Claim 1, there exists a strong path from  $v_\ell$  to  $v_k$  in  $DAG_j$ . By the code of commit\_leader, after directly committing a leader vertex  $v_\ell$  in round r',  $P_i$  tries to indirectly commit leader vertices  $v_m$  in smaller rounds such that there exists a path from  $v_\ell$  to  $v_m$  until it reaches a round r'' < r' in which it previously

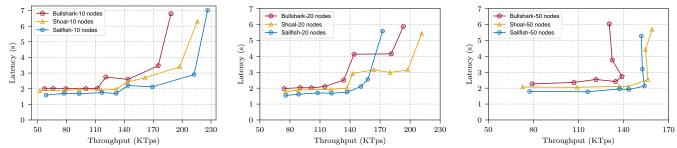


Figure 9: Throughput vs. end-to-end latency in the absence of failures at various system sizes and varying input

directly committed a leader vertex. If r'' < r < r', party  $P_j$  will indirectly commit  $v_k$  in round r. Otherwise, by inductive argument and Claim 1, party  $P_j$  must have indirectly committed  $v_k$  when directly committing round r'' leader vertex.

Claim 3. Let  $v_k$  and  $v_k'$  be two leader vertices consecutively directly committed by a party  $P_i$  in rounds  $r_i$  and  $r_i' > r_i$  respectively. Let  $v_\ell$  and  $v_\ell'$  be two leader vertices consecutively directly committed by party  $P_j$  in rounds  $r_j$  and  $r_j' > r_j$  respectively. Then,  $P_i$  and  $P_j$  commits the same leader vertices between rounds  $\max(r_i, r_j)$  and  $\min(r_i', r_j')$  and in the same order.

*Proof.* If  $r_i' < r_j$  or  $r_j' < r_i$ , then there are no rounds between  $\max(r_i, r_j)$  and  $\min(r_i', r_j')$  and we are trivially done. Otherwise, assume wlog that  $r_i \le r_j < r_i'$ . By Claim 2, both  $P_i$  and  $P_j$  will (directly or indirectly) commit the same leader vertex in the round  $\min(r_i', r_j')$ . Assume  $\min(r_i', r_j') = r_i'$ . By Fact 1, both  $DAG_i$  and  $DAG_j$  will contain  $v_k'$  and all vertices that have a path from  $v_k'$  in  $DAG_i$ .

By the code of commit\_leader, after (directly or indirectly) committing the leader vertex  $v_k'$ , parties try to indirectly commit leader vertices in smaller round numbers until they reach a round in which they previously directly committed a leader vertex. Therefore, both  $P_i$  and  $P_j$  will indirectly commit all leader vertices from  $\min(r_i', r_j')$  to  $\max(r_i, r_j)$ . Moreover, due to deterministic code of commit\_leader, both parties will commit the same leader vertices between rounds  $\min(r_i', r_j')$  to  $\max(r_i, r_j)$  in the same order.

By inductively applying Claim 3 between any two pairs of honest parties we obtain the following corollary.

**Corollary 1.** Honest parties commit the same leader vertices in the same order.

**Lemma 1** (Total order). The protocol in Figures 1 to 3 satisfies Total order.

*Proof.* By Corollary 1, honest parties commit the same leader vertices in the same order. By the code of order\_vertices, parties iterate on the committed leader vertices according to their order and a\_deliver all vertices in their causal history by a predefined deterministic rule. By Fact 1, all honest parties have the same causal history in their DAG for every committed leader. Thus, the lemma follows. □

**Lemma 2** (Agreement). The protocol in Figures 1 to 3 satisfies Agreement.

Proof. If an honest party  $P_i$  outputs a\_deliver $_i(v_i.block, v_i.round, v_i.source)$ ,  $v_i$  must be in the causal history of some leader vertex  $v_k$ .

When party  $P_j$  eventually directly commits a leader vertex  $v_\ell$  for round higher than  $v_k.round$ , by Lemma 1,  $P_j$  also commits  $v_k$ . By Fact 1, the causal histories of  $v_k$  in  $DAG_i$  and  $DAG_j$  are the same. Thus, when  $P_j$  orders the causal histories of  $v_k$ , it outputs a\_deliver $_i(v_i.block, v_i.round, v_i.source)$ .

**Lemma 3** (Integrity). *The protocol in Figures 1 to 3 satisfies Integrity.* 

*Proof.* An honest party  $P_i$  calls a\_deliver $_i(v.block, v.round, v.source)$  only when vertex v is in  $DAG_i$ . The vertices in  $DAG_i$  are added with event r\_deliver $_i(v, v.round, v.source)$ . Therefore, the proof follows from the Integrity property of reliable broadcast. □

**Validity.** We rely on GST to prove validity. For RBC, we use the protocol from Das et al. [17] for its (nearly) optimal communication complexity. Their protocol requires 4 communication steps and satisfies the RBC properties at all times. After GST, it provides the following stronger guarantees:

**Property 1.** Let t be a time after GST. If an honest party reliably broadcasts a message M at time t, all honest parties deliver M by time  $t + 4\Delta$ .

**Property 2.** Let  $t_g$  denote the GST. If an honest party delivers message M at time t, then all honest parties deliver M by time  $\max(t_g, t) + 2\Delta$ .

Claim 4. Let  $t_g$  denote the GST and  $P_i$  be the first honest party to enter round r. If  $P_i$  enters round r at time t via receiving round r-1 leader vertex, then all honest parties enter round r or higher by  $\max(t_g,t)+2\Delta$ .

*Proof.* Observe that  $P_i$  must have delivered 2f+1 round r-1 vertices along with round r-1 leader vertex by time t. By Property 2, all honest parties must have delivered 2f+1 round r-1 vertices along with round r-1 leader vertex by  $\max(t_g,t)+2\Delta$ . Thus, all honest parties will enter round r

by  $\max(t_g,t)+2\Delta$  if they have not already entered a higher round.  $\Box$ 

Claim 5. Let  $t_g$  denote the GST and  $P_i$  be the first honest party to enter round r. If  $P_i$  enters round r at time t via  $\mathcal{TC}_{r-1}$ , then (i) all honest parties (except  $L_r$  when  $P_i \neq L_r$ ) enter round r or higher by  $\max(t_g, t) + 2\Delta$ , and (ii)  $L_r$  (if honest and  $P_i \neq L_r$ ) enters round r or higher by  $\max(t_g, t) + 4\Delta$ .

*Proof.* Observe that  $P_i$  must have delivered 2f+1 round r-1 vertices and received  $\mathcal{TC}_{r-1}$  by time t. By Property 2, all honest parties must have delivered 2f+1 round r-1 vertices by  $\max(t_g,t)+2\Delta$ . In addition,  $P_i$  must have multicasted  $\mathcal{TC}_{r-1}$  which arrives all honest parties by  $\max(t_g,t)+\Delta$ . Thus, all honest parties (except  $L_r$  when  $P_i\neq L_r$ ) will enter round r by  $\max(t_g,t)+2\Delta$  if they have not already entered a higher round. This proves part (i) of the claim.

Observe that if no honest party delivered round r-1 leader vertex by  $\max(t_g,t)+2\Delta$ , all honest parties (including  $L_r$ ) will send (no-vote, r-1) to  $L_r$ . Thus,  $L_r$  will receive  $\mathcal{NVC}_{r-1}$  by time  $\max(t_g,t)+3\Delta$ . On the other hand, if at least one honest party delivered round r-1 leader vertex by  $\max(t_g,t)+2\Delta$ , by Property 2,  $L_r$  will deliver round r-1 leader vertex by  $\max(t_g,t)+4\Delta$ . Thus,  $L_r$  will enter round r by  $\max(t_g,t)+4\Delta$  if it has not already entered a higher round. This proves part (ii) of the claim.

Claim 6. All honest parties keep entering increasing rounds.

*Proof.* Suppose all honest parties are in round r or above. Let party  $P_i$  be in round r. If there exists an honest party  $P_j$  in round r' > r at any time, then by Claim 4 and Claim 5, all honest parties will enter round r' or higher. Otherwise, all honest parties are in round r. Observe that all honest parties will r\_broadcast round r vertex when entering round r. Thus, all honest parties will deliver 2f + 1 round r vertices

Observe that if no honest party delivered round r leader vertex, due to the timeout rule, all honest parties will multicast  $\langle \text{timeout}, r \rangle$  and receive  $\mathcal{TC}_r$ . In addition, all honest parties will also send  $\langle \text{no-vote}, r \rangle$  to  $L_{r+1}$  and  $L_{r+1}$  will receive  $\mathcal{NVC}_{r-1}$ . Thus, all honest parties will move to round r+1. On the other hand, if at least one honest party has delivered round r leader vertex, by Fact 1, all honest parties will deliver the round r leader vertex. Having delivered 2f+1 round r vertices and round r leader vertex, all honest parties will move to round r+1.

**Claim 7.** If an honest party enters round r then at least f + 1 honest parties must have already entered r - 1.

*Proof.* For an honest party to enter round r, it must have delivered 2f+1 round r-1 vertices. At least f+1 of those vertices are sent by honest parties while they were in round r-1. Thus, f+1 honest parties must have already entered r-1.

**Claim 8.** If the first honest party to enter round r does so after GST and  $L_r$  is honest, then there exists at least 2f + 1

round r + 1 vertices with strong paths to round r leader vertex.

*Proof.* Let t be the time when the first honest party (say  $P_i$ ) entered round r. Observe that no honest party sends  $\langle \mathsf{timeout}, r \rangle$  before  $t + 8\Delta$  due to its round timer expiring. Accordingly, no honest party sends  $\langle \mathsf{timeout}, r \rangle$  due to receiving f + 1  $\langle \mathsf{timeout}, r \rangle$  before  $t + 8\Delta$ . Thus,  $\mathcal{TC}_r$  does not exist before  $t + 8\Delta$ . In addition, by Claim 7, no honest party can enter a round greater than r until at least f + 1 honest parties have entered r. Thus, no honest party sends a timeout message for a round greater than r before  $t + 8\Delta$  and no honest party enters a round greater than r via a timeout certificate before  $t + 8\Delta$ .

Since,  $P_i$  entered round r at time t, by Claim 5, all honest parties (except  $L_r$ ) enter round r or higher by  $t+2\Delta$  and  $L_r$  enters round r or higher by  $t+4\Delta$ . Observe that if some honest party enters a round higher than r+1 before  $t+8\Delta$ , there exists at least 2f+1 round r+1 vertices with strong paths to round r leader vertex (say  $v_k$ ). This is because for an honest party to enter round r', it must have delivered 2f+1 round r'-1 vertices. By transitive argument, it must be that there exists 2f+1 round r+1 vertices. Since  $\mathcal{TC}_r$  does not exist before  $t+8\Delta$ , the round r+1 vertices must have a strong path to  $v_k$ .

Also, note that if an honest party enters round r+1 before  $t+8\Delta$ , it must have delivered 2f+1 round r vertices and vertex  $v_k$  (since  $\mathcal{TC}_r$  does not exist before  $t+8\Delta$ ). Thus, its round r+1 vertex must have a strong path to  $v_k$ .

In the rest of the proof, we consider the case when no honest party entered a round higher than r before  $t+8\Delta$ . Thus, by Claim 5, all honest parties (except  $L_r$ ) enter round r by  $t+2\Delta$  and  $L_r$  enters round r by  $t+4\Delta$ . Note that an honest party invokes r\_bcast on its round r vertex when it enters round r. By Property 1, round r vertices from all honest parties (except  $L_r$ ) will be delivered by  $t+6\Delta$ . In addition, by Property 1,  $v_k$  will be delivered by  $t+8\Delta$ . Thus, all honest parties will receive 2f+1 round r vertices by  $t+8\Delta$  along with  $v_k$  and send round r+1 vertex with a strong path to  $v_k$ .

The above claim uses  $\tau=8\Delta$ . When an honest party enters round r via receiving round r-1 leader vertex, by using Claim 4 (instead of Claim 5), we can show the above claim holds with  $\tau=6\Delta$ . By the commit rule and Claim 8, the following corollary follows.

**Corollary 2.** If the first honest party to enter round r does so after GST and  $L_r$  is honest, all honest parties will directly commit round r leader vertex.

**Lemma 4** (Validity). The protocol in Figures 1 to 3 satisfies Validity.

*Proof.* Let party  $P_i$  be an honest party that invokes a\_bcast(b, r). We show that all honest parties eventually output a\_deliver( $b, r, p_i$ ). Observe that  $P_i$  pushes b into the blocksToPropose queue. By Claim 6,  $P_i$  keeps increasing rounds and creating new vertices in those new rounds. Thus,  $P_i$  will eventually create a vertex  $v_i$  with b at some round r

and reliably broadcast it. By the Validity property of reliably broadcast, all honest parties will eventually add it to their DAG i.e.,  $v_i \in DAG[r]$  for every honest party. By the code of create\_new\_vertex, every vertex that  $P_j$  creates after  $v_i$  is added to  $DAG_j[r]$  has a path to  $v_i$ .

By Corollary 2, the leader vertex proposed by an honest leader is directly committed after GST. With a leader-election function that elects all parties with equal probability, there will be an honest leader who will propose a vertex with a path to  $v_i$  and the leader vertex will be committed. By the code of order\_vertices,  $P_j$  will eventually invoke a\_deliver $(b, r, p_i)$ . By Lemma 2, all honest parties will eventually invoke a\_deliver $(b, r, p_i)$ .

# Appendix C. Security Analysis of Multi-leader Sailfish

We say that a leader vertex  $v_i$  is committed directly by party  $P_i$  if  $P_i$  invokes commit\_leaders( $\mathcal{CLS}$ ) and  $v_i \in \mathcal{CLS}$ . Similarly, we say that a leader vertex  $v_j$  is committed indirectly if  $\mathcal{CMV}$  is added to leader Stack (in Line 140) and  $v_j \in \mathcal{CMV}$ . In addition, we say party  $P_i$  consecutively directly commit leader vertices in rounds r and r' > r and does not directly commit any leader vertex between r and r'

To commit a round r leader vertex in Multi-leader Sailfish, at least f+1 round-(r+1) vertices proposed by honest parties must have a strong path to the round r leader vertex, which is identical to that of Sailfish. Additionally, the main leader vertex of Multi-leader Sailfish is identical to the leader vertex of Sailfish. Thus, the proof of the following claim (Claim 9) remains identical to Claim 1.

**Claim 9.** If an honest party  $P_i$  directly commits a leader vertex  $v_k$  in round r, then for every main leader vertex  $v_\ell$  in round r' such that r' > r, there exists a strong path from  $v_\ell$  to  $v_k$ .

Similarly, the indirect commit rule of a main leader vertex in Multi-leader Sailfish is identical to the indirect commit rule of the leader vertex in Sailfish. Thus, the proof of the following claim (Claim 10) remains identical to Claim 2 except Claim 9 needs to be invoked (instead of Claim 1).

**Claim 10.** If an honest party  $P_i$  directly commits the main leader vertex  $v_k$  in round r and an honest party  $P_j$  directly commits the main leader vertex  $v_\ell$  in round  $r' \ge r$ , then  $P_j$  (directly or indirectly) commits  $v_k$  in round r.

Claim 11. If an honest party  $P_i$  directly commits all leader vertices corresponding to  $\mathcal{ML}_r[:x]$  (for some x > 0) and an honest party  $P_j$  directly commits the main leader vertex  $v_\ell$  in round r' > r, then  $P_j$  indirectly commits all leader vertices corresponding to  $\mathcal{ML}_r[:x]$  in round r.

*Proof.* Given that  $P_i$  directly committed all leader vertices in  $\mathcal{ML}_r[:x]$ , by Fact 1 and Claim 9, there are strong paths from the main leader vertex of any round higher than r to all leader vertices corresponding to  $\mathcal{ML}_r[:x]$  in  $DAG_j$ .

By the code of commit\_leaders(), after directly committing the main leader vertex  $v_{\ell}$  in round r',  $P_i$  tries to indirectly commit all leader vertices corresponding to  $\mathcal{ML}_{r''}[:y]$  (for some y > 0) in an earlier round r'' < r'such that there exists strong paths from  $v_{\ell}$  to all leader vertices corresponding to  $\mathcal{ML}_{r''}[:y]$ . This process of indirectly committing multiple leader vertices of an earlier round is repeated for leader vertices that have strong paths from the main leader vertex of round r'' (i.e.,  $\mathcal{ML}_{r''}[1]$ ), until it reaches a round  $r^* < r'$  in which it previously directly committed a leader vertex. If  $r^* < r < r'$ , party  $P_j$  will indirectly commit all leader vertices in  $\mathcal{ML}_r[:x]$  in round r. Otherwise, by inductive argument and Claim 9, party  $P_i$  must have indirectly committed all leader vertices in  $\mathcal{ML}_r[:x]$  when directly committing the main leader vertex of round  $r^*$ .

**Claim 12.** Let an honest party  $P_i$  consecutively directly committed in rounds  $r_i$  and  $r'_i$ . Also, let an honest party  $P_j$  consecutively directly committed in rounds  $r_j$  and  $r'_j$ . Then,  $P_i$  and  $P_j$  commits the same leader vertices between rounds  $\max(r_i, r_j)$  and  $\min(r'_i, r'_j)$  and in the same order.

*Proof.* If  $r_i' < r_j$  or  $r_j' < r_i$ , then there are no rounds between  $\max(r_i, r_j)$  and  $\min(r_i', r_j')$  and we are trivially done. Otherwise, assume wlog that  $r_i \leq r_j < r_i'$ . Also, assume  $\min(r_i', r_j') = r_i'$ . Let  $\mathcal{ML}_{r_i'}[:x]$  be the list of multiple leader vertices directly committed by party  $P_i$  in round  $r_i'$  for some x > 0. If  $r_i' = r_j'$ , by Claim 10, party  $P_j$  commits at least  $\mathcal{ML}_{r_i'}[1]$  in round  $r_i'$ . Otherwise, by Claim 11, party  $P_j$  indirectly commits all leader vertices in  $\mathcal{ML}_r[:x]$  in round  $r_i'$ .

Moreover, by Fact 1, both  $DAG_i$  and  $DAG_j$  will contain  $\mathcal{ML}_{r_i'}[1]$  (i.e., the main leader vertex in round  $r_i'$ ) and all vertices that have a path from  $\mathcal{ML}_{r_i'}[1]$  in  $DAG_i$ . By the code of commit\_leaders(), after (directly or indirectly) committing  $\mathcal{ML}_{r_i'}[1]$ , parties try to indirectly commit multiple leader vertices in a smaller round number  $r'' < r_i'$  that have strong paths from  $\mathcal{ML}_{r_i'}[1]$ . And, this process is repeated by indirectly committing leader vertices of earlier round with strong paths from  $\mathcal{ML}_{r_i'}[1]$  until it reaches a round  $r^* < r$  in which it previously directly committed a leader vertex. Therefore, both  $P_i$  and  $P_j$  will indirectly commit all leader vertices from  $\min(r_i', r_j')$  to  $\max(r_i, r_j)$ . Moreover, due to deterministic code of commit\_leaders, both parties will commit the same leader vertices between rounds  $\min(r_i', r_j')$  to  $\max(r_i, r_j)$  in the same order.

Due to space constraints, we present full proofs for Multi-leader Sailfish in the full version [?].

By inductively applying Claim 12 between any two pairs of honest parties we obtain the following corollary.

**Corollary 3.** Honest parties commit the same leaders in the same order.

The proof of the following total order lemma (Lemma 5) remains identical to Lemma 1 except Corollary 3 needs to be invoked (instead of Corollary 1).

**Lemma 5** (Total order). *Multi-leader Sailfish satisfies Total order.* 

**Agreement.** The agreement proof remains identical to Lemma 2 except Lemma 5 needs to be invoked (instead of Lemma 1).

**Integrity.** The integrity proof remains identical to Lemma 3.

**Validity.** We again rely on GST to prove validity and utilize the RBC protocol from Das et al. [17].

Claim 13. Let  $t_g$  denote the GST and  $P_i$  be the first honest party to enter round r. If  $P_i$  enters round r at time t, then (i) all honest parties (except  $L_r$  when  $P_i \neq L_r$ ) enter round r or higher by  $\max(t_g, t) + 2\Delta$ , and (ii)  $L_r$  (if honest and  $P_i \neq L_r$ ) enters round r or higher by  $\max(t_g, t) + 4\Delta$ .

*Proof.* Observe that  $P_i$  must have delivered either round r-1 main leader vertex (say  $v_k$ ) or received  $\mathcal{TC}_{r-1}$  along with 2f+1 round r-1 vertices. By Property 2, all honest parties must have delivered 2f+1 round r-1 vertices by  $\max(t_g,t)+2\Delta$ . If  $P_i$  delivered  $v_k$ , by Property 2, all honest parties must have delivered  $v_k$  by  $\max(t_g,t)+2\Delta$ . Otherwise,  $P_i$  must have multicasted  $\mathcal{TC}_{r-1}$  which arrives all honest parties by  $\max(t_g,t)+\Delta$ . Thus, all honest parties (except  $L_r$  when  $P_i\neq L_r$ ) will enter round r by  $\max(t_g,t)+2\Delta$  if they have not already entered a higher round. This proves part (i) of the claim.

Having delivered  $v_k$  or received  $\mathcal{TC}_{r-1}$  (along with 2f + 1 round r - 1 vertices), an honest party  $P_i$  sends  $\langle \mathsf{no\text{-}vote}, P_k, r-1 \rangle$  for all  $P_k \in \mathcal{ML}_{r-1}$  if  $P_j$  did not deliver its corresponding leader vertex by then. If no honest party delivered the leader vertex corresponding to  $P_k$  by  $\max(t_q, t) + 2\Delta$ , then all honest parties (including  $L_r$ ) will send (no-vote,  $P_k$ , r-1) to  $L_r$ . Thus,  $L_r$  will receive  $\mathcal{NVC}_{r-1}^{P_k}$  by time  $\max(t_q,t)+3\Delta$ . On the other hand, if at least one honest party delivered the leader vertex corresponding to  $P_k$  by  $\max(t_g, t) + 2\Delta$ , by Property 2,  $L_r$  will deliver the leader vertex corresponding to  $P_k$  by  $\max(t_q,t) + 4\Delta$ . Thus,  $L_r$  will either deliver a leader vertex corresponding to  $P_k$  or receive  $\mathcal{NVC}_{r-1}^{P_k}$  for all  $P_k \in$  $\mathcal{ML}_{r-1}$  by time  $\max(t_q, t) + 4\Delta$ . Since  $L_r$  waits for leader vertices corresponding to  $\mathcal{ML}_{r-1}[:x]$  and  $\mathcal{NVC}_{r-1}^p$  where  $p = \mathcal{ML}_{r-1}[x+1], L_r$  enters round r by  $\max(t_g, t) + 4\Delta$ if it has not already entered a higher round. This proves part (ii) of the claim.

**Claim 14.** All honest parties keep entering increasing rounds.

*Proof.* Suppose all honest parties are in round r or above. Let party  $P_i$  be in round r. If there exists an honest party  $P_j$  in round r'>r at any time, then by Claim 13, all honest parties will enter round r' or higher. Otherwise, all honest parties are in round r. Observe that all honest parties will r\_bcast round r vertex when entering round r. Thus, all honest parties will deliver 2f+1 round r vertices. Furthermore, if an honest party (except  $L_{r+1}$ ) delivers the round r main leader vertex (say  $v_k$ ), it will advance to round r+1.

Alternatively, if no honest party delivered  $v_k$  by the time their round r timer expires, due to the timeout rule, all honest parties will multicast  $\langle timeout, r \rangle$  and subsequently receive  $\mathcal{TC}_r$ . Having delivered  $v_k$  or received  $\mathcal{TC}_r$ , an honest party  $P_j$  send  $\langle \text{no-vote}, P_k, r \rangle$  for all  $P_k \in \mathcal{ML}_r$  if  $P_j$  did not deliver its corresponding leader vertex by then. If no honest party delivered the leader vertex corresponding to  $P_k$  by the time they delivered  $v_k$  or received  $\mathcal{TC}_r$ , then all honest parties will send (no-vote,  $P_k, r$ ) to  $L_{r+1}$ . Thus,  $L_{r+1}$  will receive  $\mathcal{NVC}_r^{P_k}$ . On the other hand, if at least one honest party delivered the leader vertex corresponding to  $P_k$ , by Property 2,  $L_{r+1}$  will deliver the leader vertex corresponding to  $P_k$ . Thus,  $L_r$  will either deliver a leader vertex corresponding to  $P_k$  or receive  $\mathcal{NVC}_r^{P_k}$  for all  $P_k \in \mathcal{ML}_r$ . Since  $L_{r+1}$  waits for leader vertices corresponding to  $\mathcal{ML}_r[:x]$  and  $\mathcal{NVC}_r^p$  where  $p = \mathcal{ML}_r[x+1]$ ,  $L_{r+1}$  will advance to round r+1.

The proof of the following claim (Claim 15) remains identical to Claim 8 except Claim 13 needs to be invoked (instead of Claim 4).

**Claim 15.** If the first honest party to enter round r does so after GST and  $L_r$  is honest, then there exists at least 2f + 1 round r + 1 vertices with strong paths to round r main leader vertex.

By the commit rule and Claim 15, the following corollary follows.

**Corollary 4.** If the first honest party to enter round r does so after GST and  $L_r$  is honest, all honest parties will directly commit the round r main leader vertex.

The proof of the following validity lemma (Lemma 6) remains identical to Lemma 4 except Corollary 4 needs to be invoked (instead of Corollary 2).

Lemma 6 (Validity). Multi-leader Sailfish satisfies Validity.

As demonstrated in Claim 15, a round r main leader vertex (proposed by an honest leader) is always committed by round r+1 (after GST). We now establish that the round r secondary leader vertices will receive votes from at least 2f+1 round r+1 vertices under an "optimistic condition" when at least 2f+1 parties (including Byzantine parties) vote for the proposed secondary leader vertices. Consequently, all leader vertices corresponding to  $\mathcal{ML}_r[:x]$  will be committed by round r+1 when all leaders in  $\mathcal{ML}_r[:x]$  are honest (after GST).

**Claim 16.** If the first honest party to enter round r does so after GST and  $\mathcal{HML}_r \subseteq \mathcal{ML}_r$  be the set of honest round r leaders, then under an optimistic condition where all parties vote for the proposed vertices, there exists at least 2f + 1 round r + 1 vertices with strong paths to round r leader vertices corresponding to parties in  $\mathcal{HML}_r$ .

*Proof.* Let t be the time when the first honest party (say  $P_i$ ) entered round r. Observe that no honest party sends  $\langle \mathsf{timeout}, r \rangle$  before  $t + 8\Delta$  due to its round timer expiring. Accordingly, no honest party sends  $\langle \mathsf{timeout}, r \rangle$  due to

receiving f+1 (timeout, r) before  $t+8\Delta$ . Thus,  $\mathcal{TC}_r$  does not exist before  $t+8\Delta$ . In addition, by Claim 7, no honest party can enter a round greater than r until at least f+1 honest parties have entered r. Thus, no honest party sends a timeout message for a round greater than r before  $t+8\Delta$  and no honest party enters a round greater than r via a timeout certificate before  $t+8\Delta$ .

Since,  $P_i$  entered round r at time t, by Claim 13, all honest parties (except  $L_r$ ) enter round r or higher by  $t+2\Delta$  and  $L_r$  enters round r or higher by  $t+4\Delta$ . Observe that if some honest party enters a round higher than r+1 before  $t+8\Delta$ , there exists at least 2f+1 round r+1 vertices with strong paths to the round r main leader vertex. This is because for an honest party to enter round r', it must have delivered 2f+1 round r'-1 vertices. By transitive argument, it must be that there exists 2f+1 round r+1 vertices. Since  $\mathcal{TC}_r$  does not exist before  $t+8\Delta$ , the round t+1 vertices must have a strong path to the round t+1 vertices must have a strong paths to all other round t+1 vertices must have strong paths to all other round t+1 vertices corresponding to parties in t+1.

Also, note that if an honest party enters round r+1 before  $t+8\Delta$ , it must have delivered 2f+1 round r vertices along with all round r leader vertices (since  $\mathcal{TC}_r$  does not exist before  $t+8\Delta$  and it waits for all round r leader vertices before entering round r+1). Thus, its round r+1 vertex must have a strong path to all round r leader vertices.

In the rest of the proof, we consider the case when no honest party entered a round higher than r before  $t+8\Delta$ . Thus, by Claim 13, all honest parties (except  $L_r$ ) enter round r by  $t+2\Delta$  and  $L_r$  enters round r by  $t+4\Delta$ . Note that an honest party r\_bcast its round r vertex when it enters round r. By Property 1, round r vertices from all honest parties (except  $L_r$ ) will be delivered by  $t+6\Delta$ . In addition, by Property 1, round r main leader vertex will be delivered by  $t+8\Delta$ . Thus, all honest parties will receive 2f+1 round r vertices along with leader vertices corresponding to parties in  $\mathcal{HML}_r$  by  $t+8\Delta$ . When honest parties advance to round r+1, their round r+1 vertex will have a strong path to  $v_k$ .