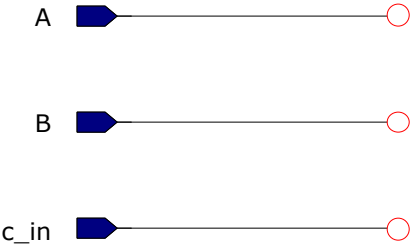
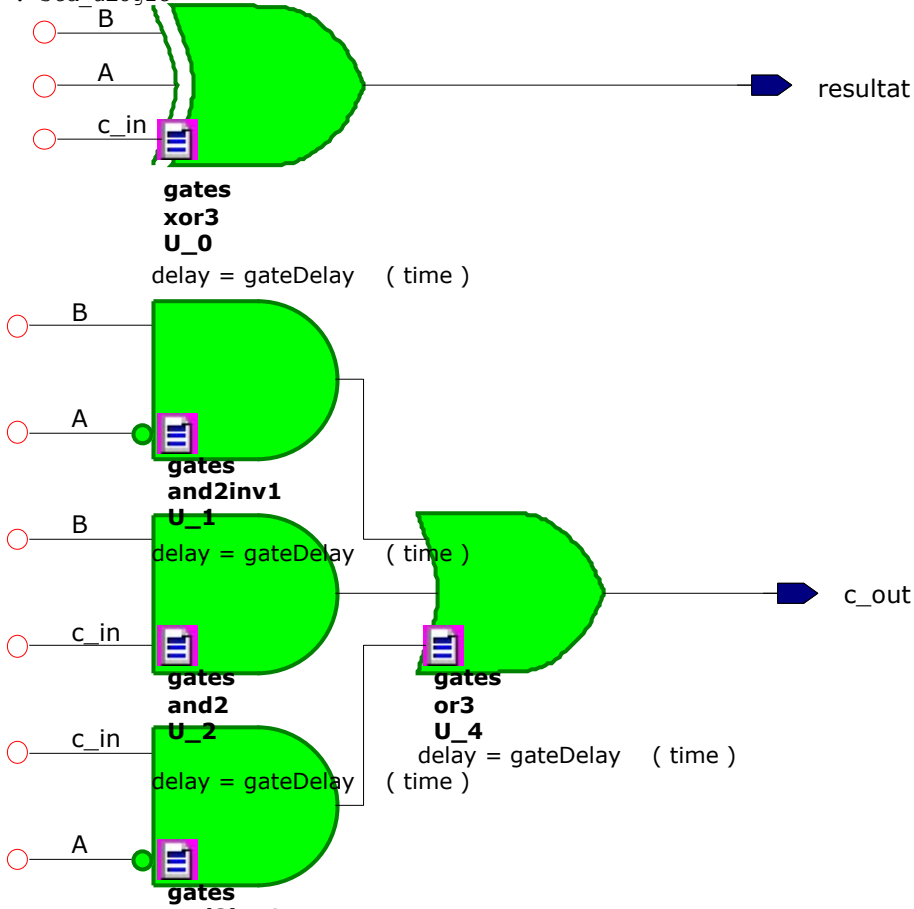


```
Package List
LIBRARY ieee;
USE ieee.std_logic_1164.all;
USE ieee.numeric_std.all;
LIBRARY gates;
USE gates.gates.all;
```



```
project_cursor/soustracteur_1bit/struct
Declarations
Ports:
    A      : std_ulogic
    B      : std_ulogic
    c_in   : std_ulogic
    c_out  : std_uLogic
    resultat : std_ulogic
```

```
Diagram Signals:
SIGNAL in3      : std_uLogic
SIGNAL out1     : std_uLogic
SIGNAL out2     : std_uLogic
```



<company name>		Project:	hds
Title: <enter diagram title here>		<enter comments here>	
Path: project_cursor/soustracteur_1bit/struct			
Edited: by Etienne on 02 déc. 2023			