

Package List

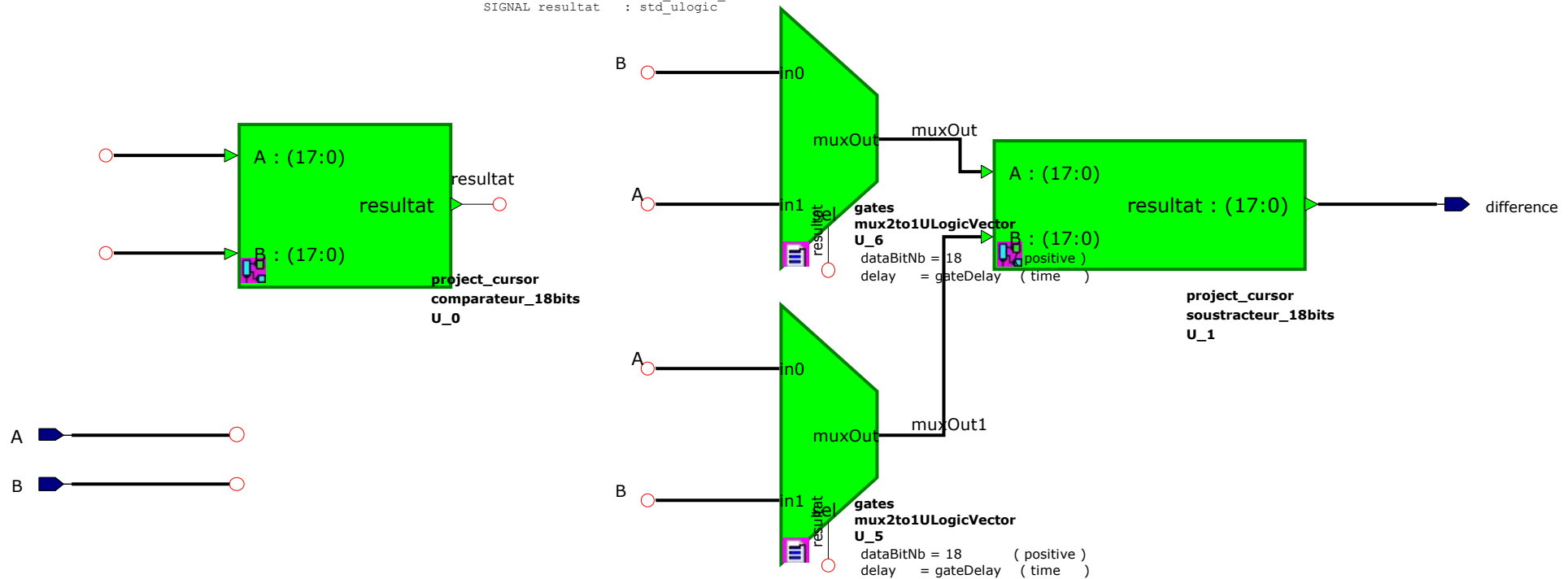
```
LIBRARY ieee;  
USE ieee.std_logic_1164.all;  
USE ieee.numeric_std.all;  
LIBRARY gates;  
USE gates.gates.all;
```

Declarations**Ports:**

```
A      : std_ulogic_vector(17 DOWNTO 0)  
B      : std_ulogic_vector(17 DOWNTO 0)  
difference : std_ulogic_vector(17 DOWNTO 0)
```

Diagram Signals:

```
SIGNAL muxOut      : std_ulogic_vector(17 DOWNTO 0)  
SIGNAL muxOut1     : std_ulogic_vector(17 DOWNTO 0)  
SIGNAL resultat    : std_ulogic_vector(17 DOWNTO 0)
```



<company name>		Project: hds
Title: <enter diagram title here>		<enter comments here>
Path: project_cursor/calcul_difference/struct		
Edited: by etienne.haingart on 18 déc. 2023		