

Package List

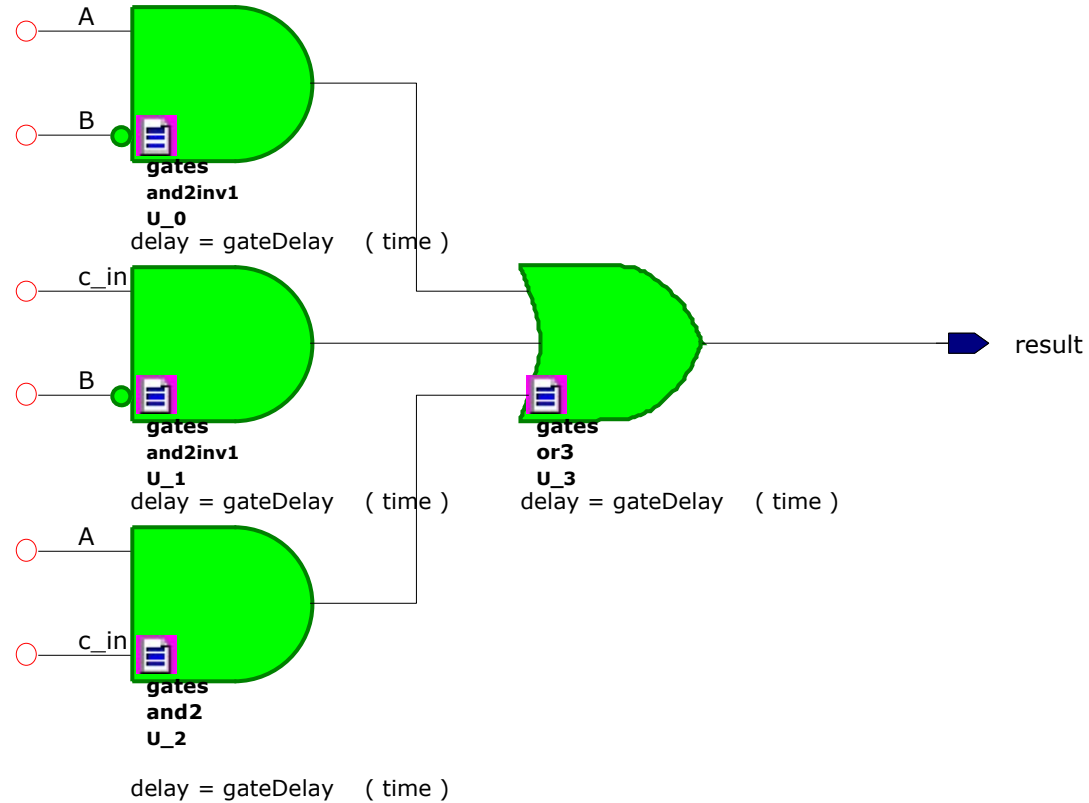
```
LIBRARY ieee;  
USE ieee.std_logic_1164.all;  
USE ieee.numeric_std.all;  
LIBRARY gates;  
USE gates.gates.all;
```

A B c_in **Declarations****Ports:**

```
A      : std_ulogic  
B      : std_ulogic  
c_in   : std_ulogic  
result : std_ulogic
```

Diagram Signals:

```
SIGNAL in3  : std_uLogic  
SIGNAL out1 : std_uLogic  
SIGNAL out2 : std_uLogic
```



<company name>		Project:	hds
Title:	<enter diagram title here>		
Path:	project_cursor/comparateur_1bit/struct		
Edited:	by etienne.haingart on 11 janv. 2024		