

# project\_cursor/gestion\_enable/struct

## Package List

```
LIBRARY ieee;
USE ieee.std_logic_1164.all;
USE ieee.numeric_std.all;
LIBRARY gates;
USE gates.gates.all;
```

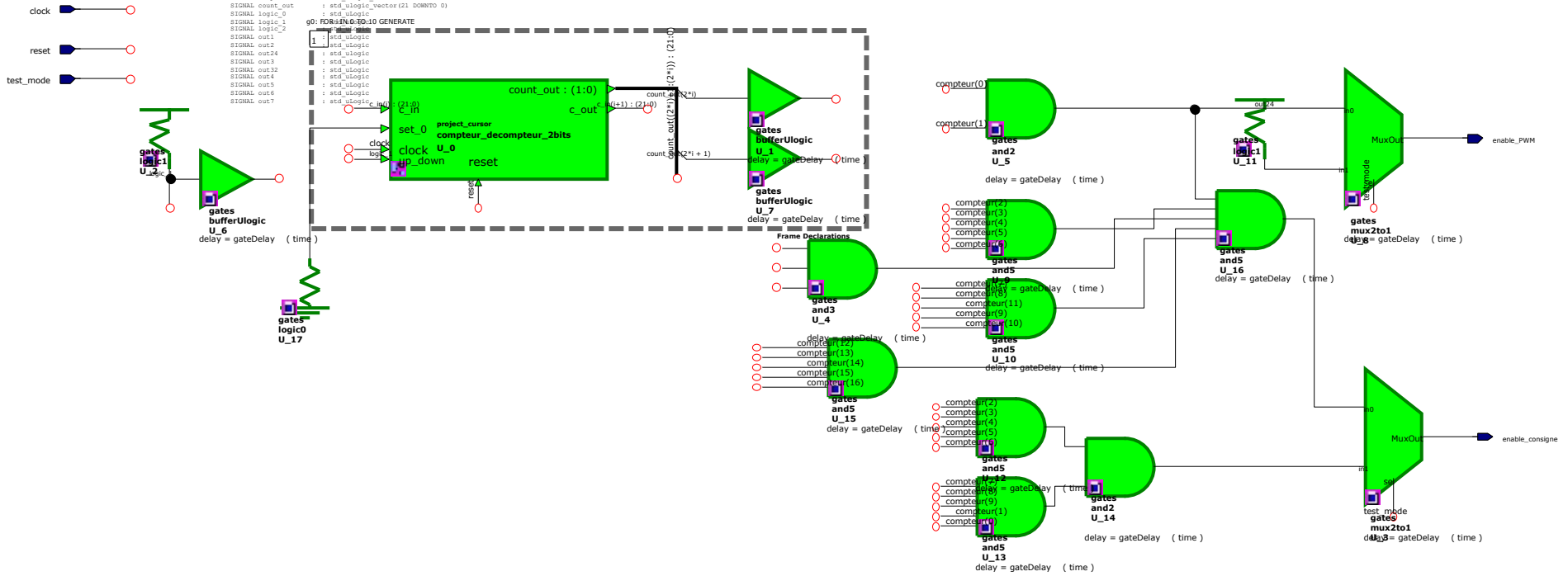
## Declarations

**Ports:**

```
clock      : std_logic
reset      : std_logic
test_mode  : std_logic
enable_PWM : std_logic
enable_consigne : std_logic
```

## Diagram Signals:

```
SIGNAL c_in      : std_logic_vector(21 downto 0)
SIGNAL compteur  : std_logic_vector(21 downto 0)
SIGNAL count_out : std_logic_vector(21 downto 0)
SIGNAL logic_0   : std_logic
SIGNAL logic_1   : std_logic
SIGNAL logic_2   : std_logic
SIGNAL out1      : std_logic
SIGNAL out2      : std_logic
SIGNAL out3      : std_logic
SIGNAL out32     : std_logic
SIGNAL out4      : std_logic
SIGNAL out5      : std_logic
SIGNAL out6      : std_logic
SIGNAL out7      : std_logic
```



<company name>		Project: hds
Title: <enter diagram title here>	Path: project_cursor/gestion_enable/struct	
Edited: lby Etienne on 21 janv. 2024		