

project_cursor/Gestion_moteur/struct

Package List

```
LIBRARY ieee;
USE ieee.std_logic_1164.all;
USE ieee.numeric_std.all;
LIBRARY gates;
USE gates.gates.all;
```

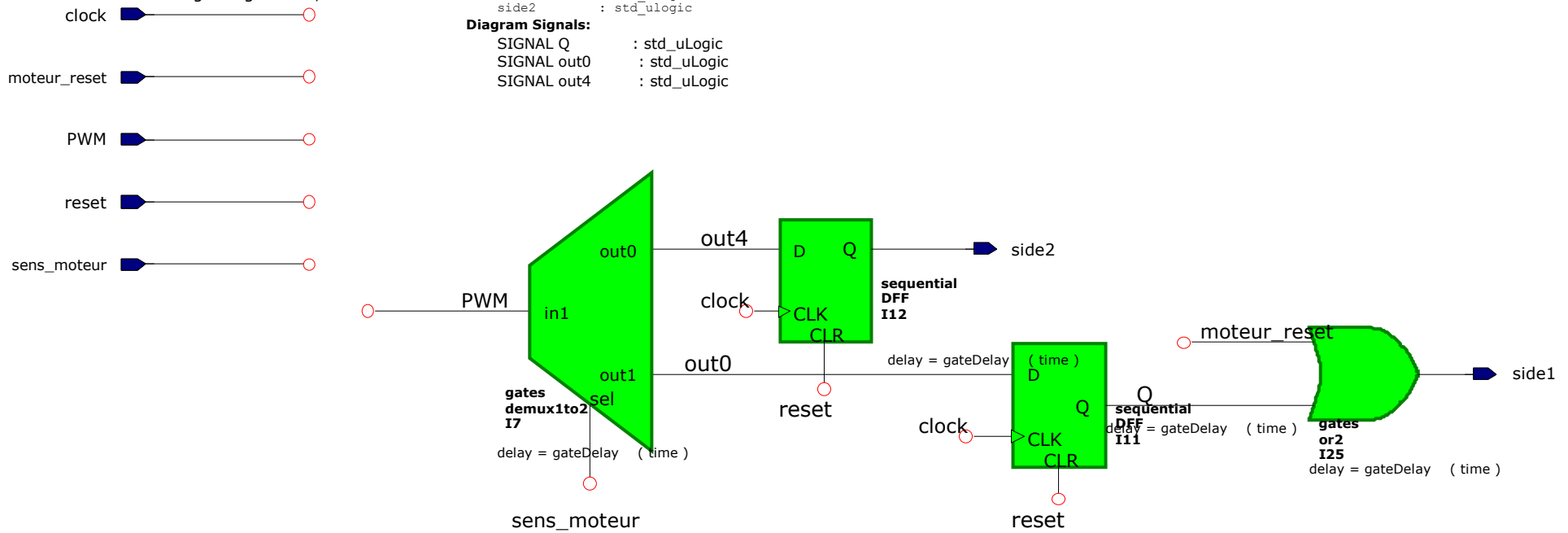
Declarations

Ports:

```
PWM      : std_ulogic
clock    : std_ulogic
moteur_reset : std_ulogic
reset    : std_ulogic
sens_moteur : std_ulogic
side1    : std_ulogic
side2    : std_ulogic
```

Diagram Signals:

```
SIGNAL Q      : std_uLogic
SIGNAL out0   : std_uLogic
SIGNAL out4   : std_uLogic
```



<company name>		Project:	hds
		<enter comments here>	
Title:	<enter diagram title here>		
Path:	project_cursor/Gestion_moteur/struct		
Edited:	by etienne.haignart on 08 janv. 2024		