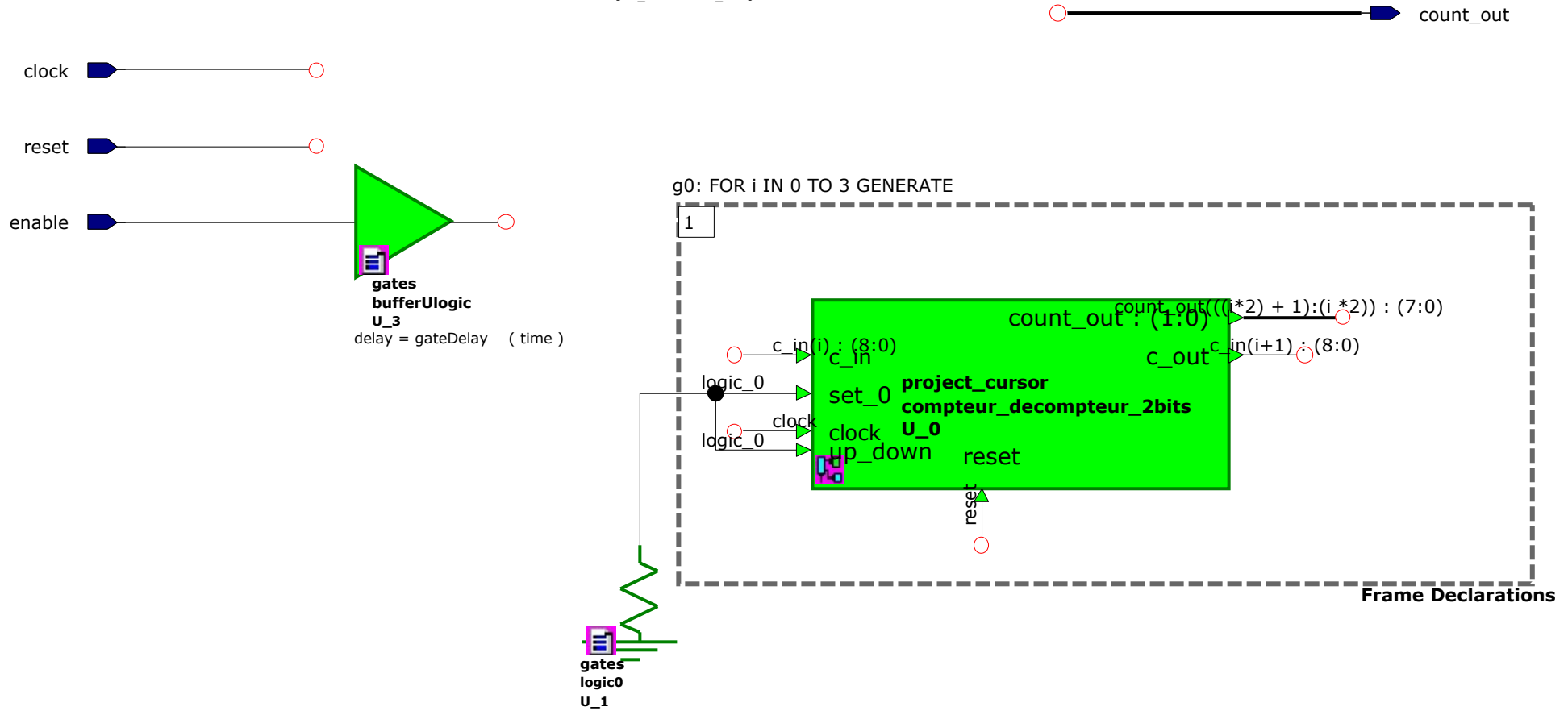


```
LIBRARY ieee;
USE ieee.std_logic_1164.all;
USE ieee.numeric_std.all;
LIBRARY gates;
USE gates.gates.all;
```

Declarations

```
clock      : std_uloic
enable     : std_uloic
reset      : std_uloic
count_out  : std_uloic_vector(7 DOWNT0 0)
```

```
SIGNAL c_in      : std_ulogic_vector(8 downto 0)
SIGNAL logic_0   : std_uLogic
```



<company name>		Project:	hds
		<enter comments here>	
Title:	<enter diagram title here>		
Path:	project_cursor/compteur_pwm_8bits/struct		
Edited:	by Etienne on 04 janv. 2024		