

Package List

LIBRARY ieee;

USE ieee.std\_logic\_1164.all;

USE ieee.numeric\_std.all;

LIBRARY gates;

USE gates.gates.all;

clock

up\_down

reset

set\_0

Declarations

**Ports:**

clock : std\_logic  
enable : std\_logic  
reset : std\_logic  
set\_0 : std\_logic  
up\_down : std\_logic  
count\_out : std\_logic\_vector(17 DOWNTO 0)

**Diagram Signals:**

SIGNAL enable\_0 : std\_logic  
SIGNAL c\_in : std\_logic\_vector(9 downto 0)  
SIGNAL in1 : std\_logic  
SIGNAL in5 : std\_logic  
SIGNAL out1 : std\_logic  
SIGNAL out2 : std\_logic  
SIGNAL out3 : std\_logic  
SIGNAL out4 : std\_logic  
SIGNAL out5 : std\_logic  
SIGNAL out7 : std\_logic

