

Global Actions
Pre Actions:
Post Actions:

Package List
LIBRARY ieee;
USE ieee.std_logic_1164.all;
USE ieee.numeric_std.all;

Concurrent Statements

Architecture Declarations
Pre Decs
Post Decs

Signal Status
SIGNAL MODE
up_down OUT
enable OUT

State Register Statements
DEFAULT RESET SCHEME COMMENT
'0' '0'
'0' '0'

Process Declarations

Clocked Process:

Output Process:

clk'EVENT AND clk = '1'

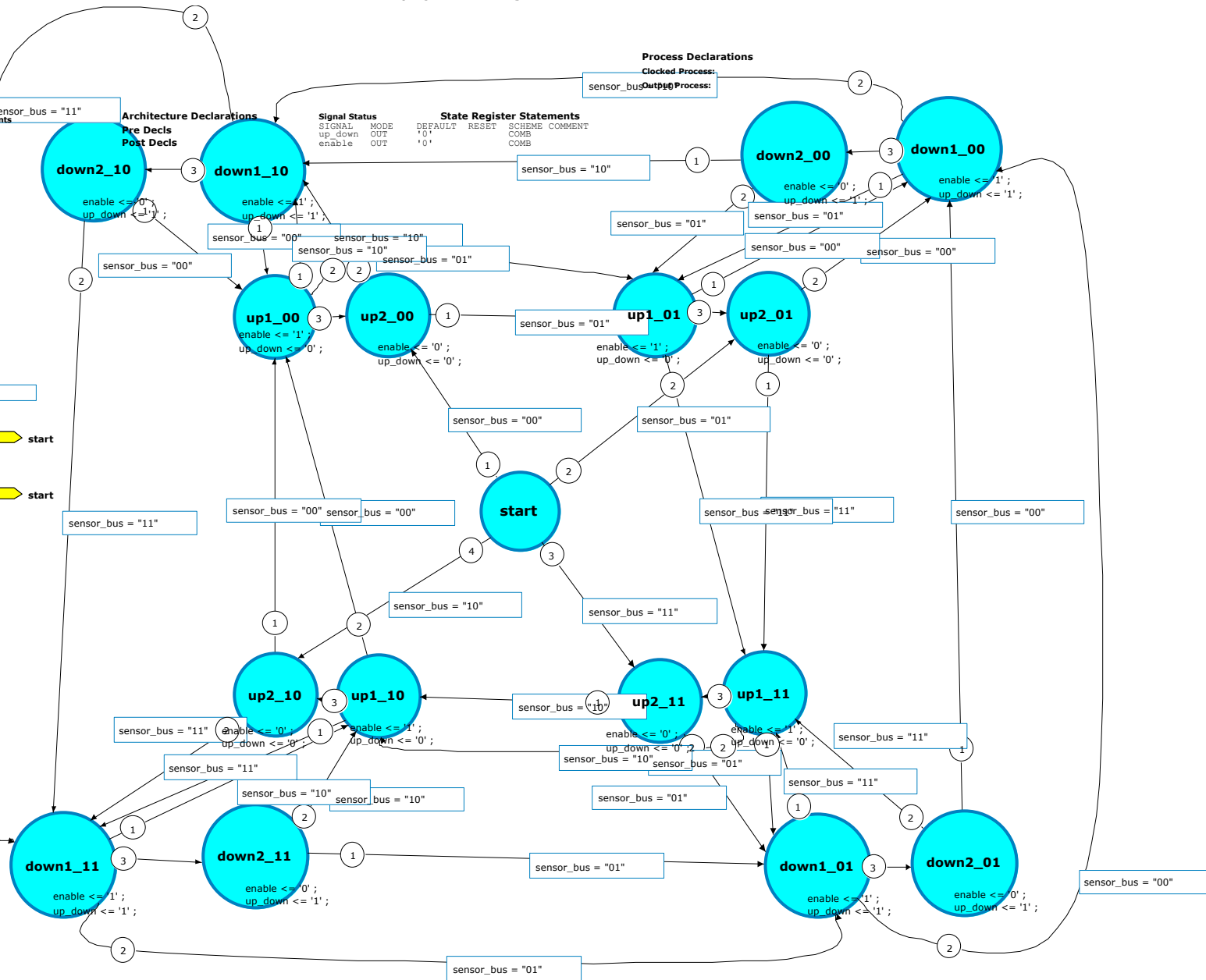


start

rst

rst = '1'

start



<company name>		Project:	hds
		<enter comments here>	
Title:	<enter diagram title here>		
Path:	project_cursor/gestion_encodeur/fsm		
Edited:	by etienne.haingart on 18 d�c. 2023		