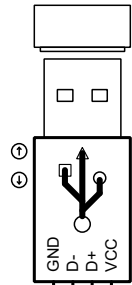


J3
AU-Y1007-R



GND
D-
D+
VCC

4
3
2
1

1k

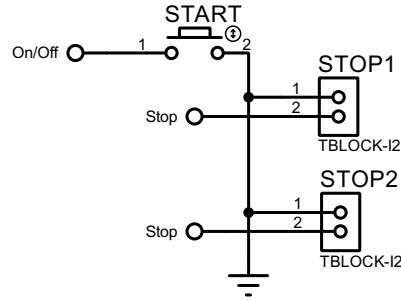
FU1
1A

D2
1N5338BRL

C2
100nF

C6
100nF

5V

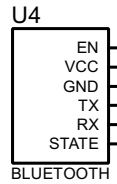
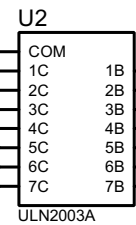


J1
CONN-SIL5

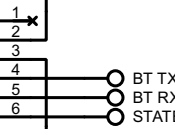


5V

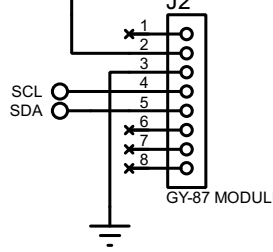
C1
330nF



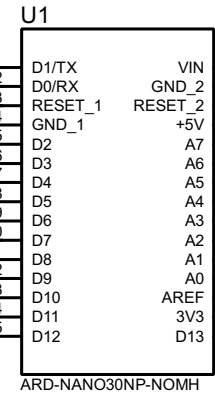
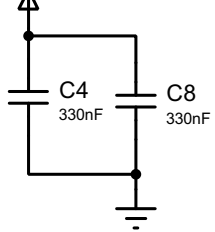
5V



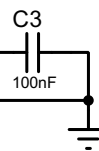
5V



5V



5V



SCL

SDA

BT TX

BT RX

On/Off

STATE

DESIGN TITLE: Barn Door Tracker	DATE: 01/02/2021
Author: Eugênio Pozzobon	TIME: 18:50:15
Advisor: Rafael Beltrame	REV: 1.1