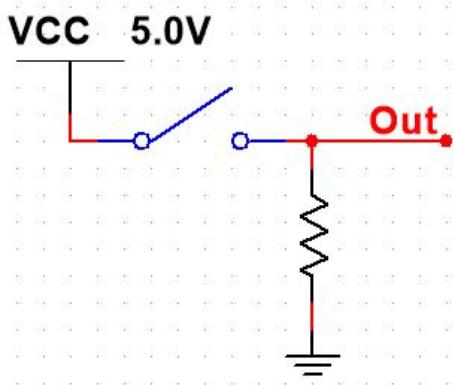


Question 1

Not yet answered

Marked out of 1.0

Which type of logic switch is shown in this schematic diagram?



- a. Active Low
- b. SPDT
- c. Active High

Question 2

Not yet answered

Marked out of 1.0

A particular logic component is new out of the box. When powered up and tested, the output is always low, regardless of the inputs. The LEAST likely explanation for this result is

- a. The IC has been damaged and is non-functional.
- b. The output for this device is open collector or open drain.
- c. The output for this device is tristate.

Question 3**Not yet answered**

Marked out of 1.0

A particular logic component is new out of the box. Which of the following troubleshooting steps would determine if the device has an open collector/open drain output?

- a. Place a 10 kΩ resistor between the output and ground.
- b. Place a 10 kΩ resistor between the output and VCC.
- c. Replace the IC with another, identical part.

Question 4**Not yet answered**

Marked out of 3.0

Select all of the following statements that are TRUE. (Correct minus incorrect)

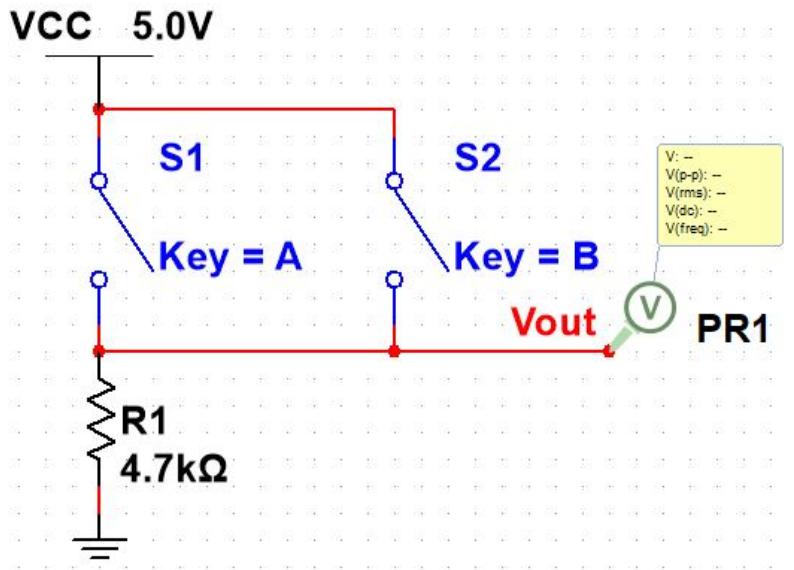
- a. Tristate outputs are typically used in a Wired-OR serial bus.
- b. Tristate outputs do not require pullup resistors.
- c. Multiple standard inputs may be connected together in a circuit.
- d. Multiple standard outputs may be connected together in a circuit.
- e. Wired-OR output devices do not require an enable control line.

Question 5

Not yet answered

Marked out of 5.0

Using Multisim or similar electronic simulation software, build and test the circuit below using the questions that follow.

**Truth Table**

A	B	Output
open	open	Choose...
open	closed	Choose...
closed	open	Choose...
closed	closed	Choose...

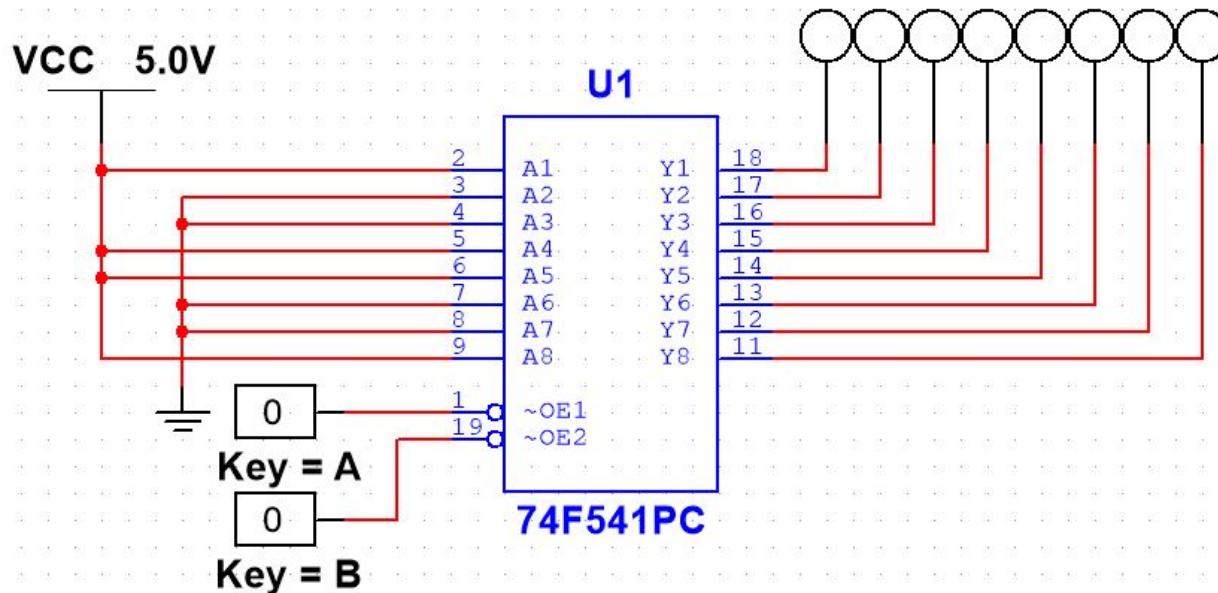
From the Truth Table, this circuit is best described as a Choose...

Question 6

Not yet answered

Marked out of 4.0

Using Multisim or similar electronic simulation software, build the circuit below and answer the questions that follow. ('A' and 'B' are INTERACTIVE_DIGITAL_CONSTANT digital sources; the eight indicators are PROBE_DIG_RED in Multisim.)



The outputs on this device are enabled when the logic levels on the inputs (A1 through A8) are displayed at the outputs (Y1 through Y8).

Fill in the following table:

Truth Table

A B Outputs Enabled?

- | | |
|-----|-----------|
| 0 0 | Choose... |
| 0 1 | Choose... |
| 1 0 | Choose... |
| 1 1 | Choose... |

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