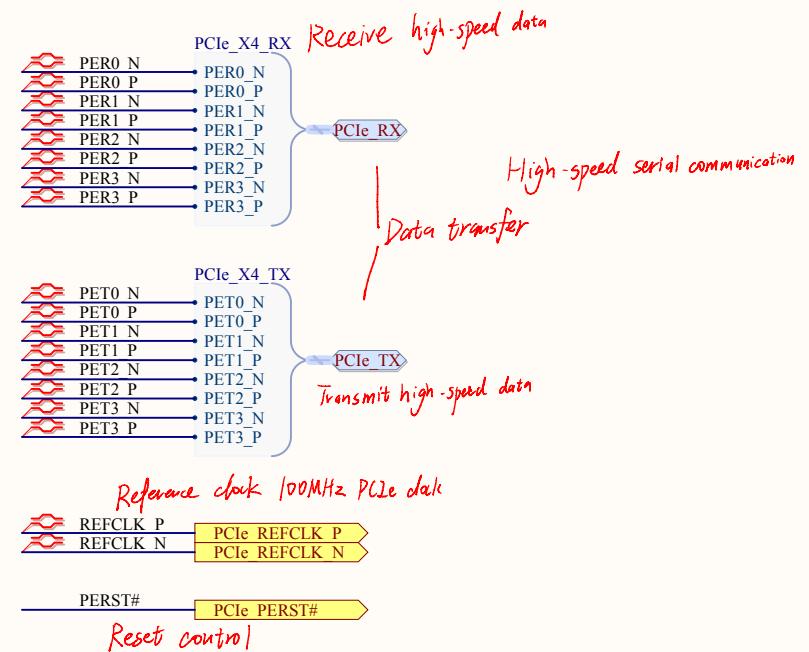
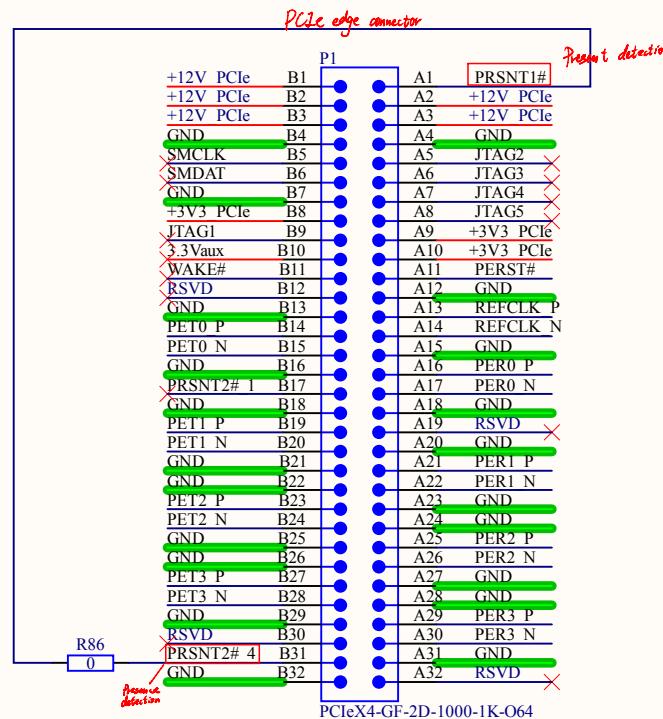


- Nominal values used, dimensions in mm
- The mounting holes and keep-out areas around them are only required when the I/O bracket is mounted on the card directly
- Component height rule and clearance rule derived from PCI_Express_CEM_r2.0.pdf, Page 84.
- Stackup is not specified in PCI_Express_CEM_r2.0.pdf, nor implemented in this template.

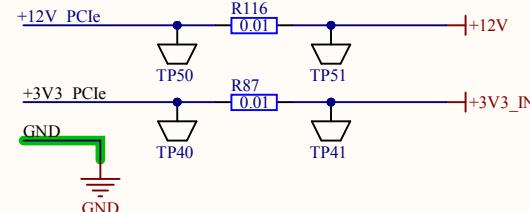
A



B

C

D

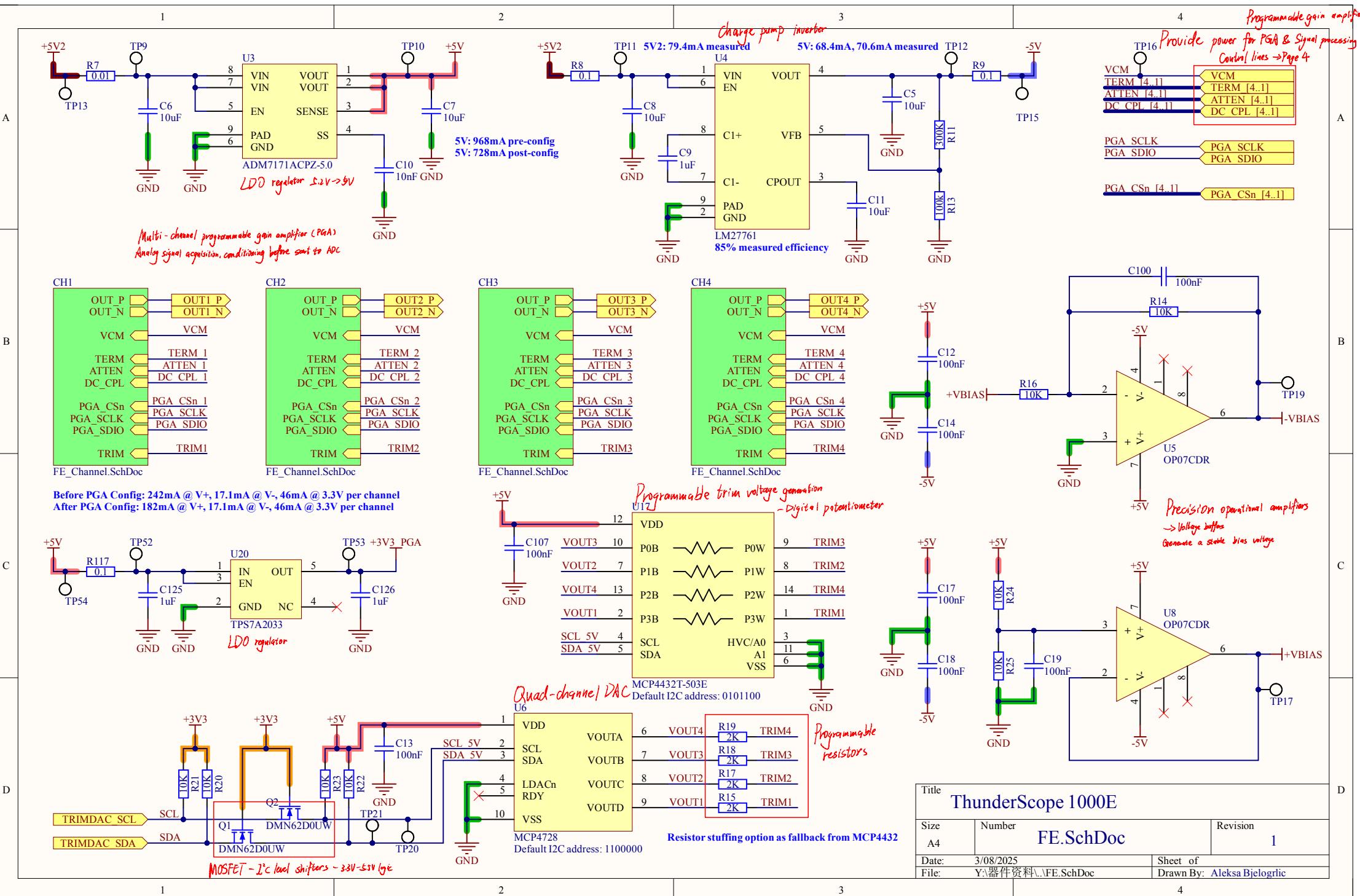
Title **ThunderScope 1000E**Size: **A4**

Number: * Revision: 1

Date: 2025/3/8 Time: 22:36:49 Sheet * of *

File: Y:\器件资料\示波器\ThunderScope-master\Hardware\Altium\Thunderscope_Rev4.1\Managed\Sheets\FC1D1A13-1.DS

Altium



1

2

3

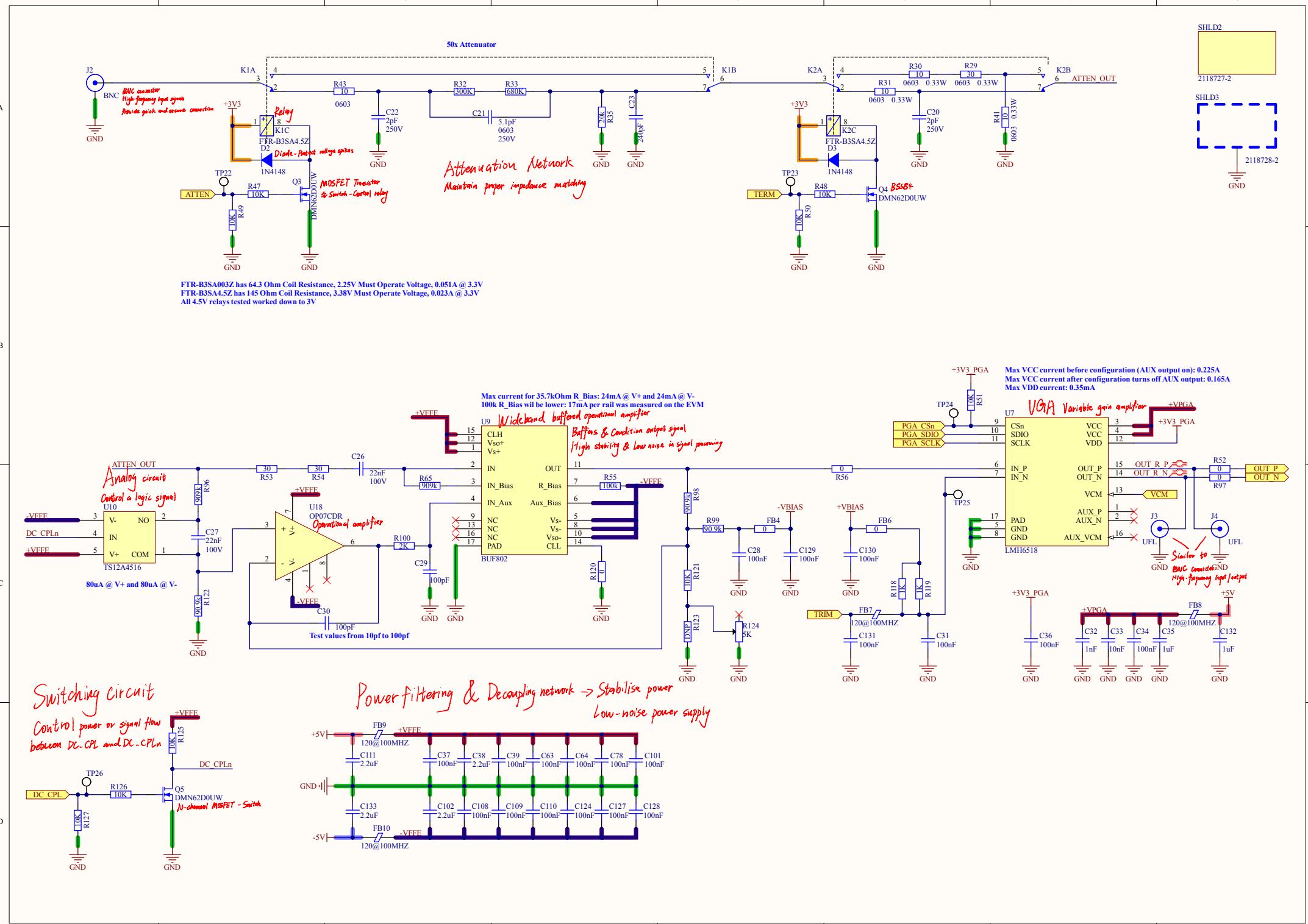
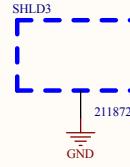
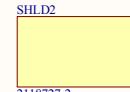
4

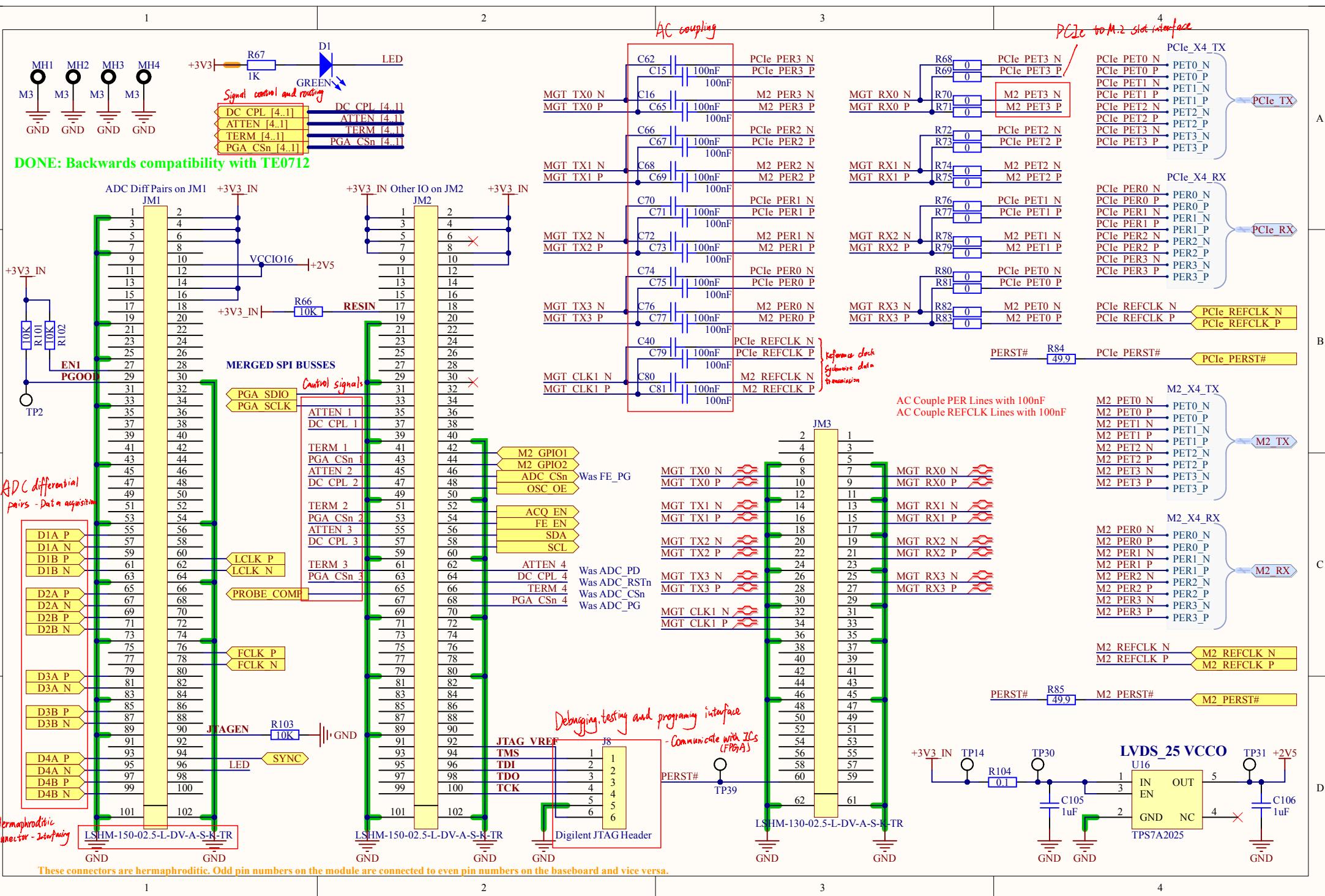
5

6

7

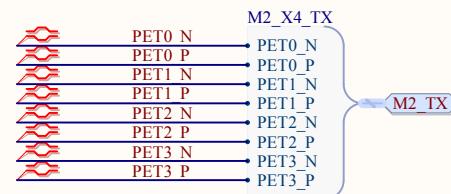
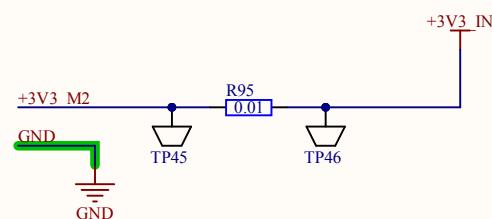
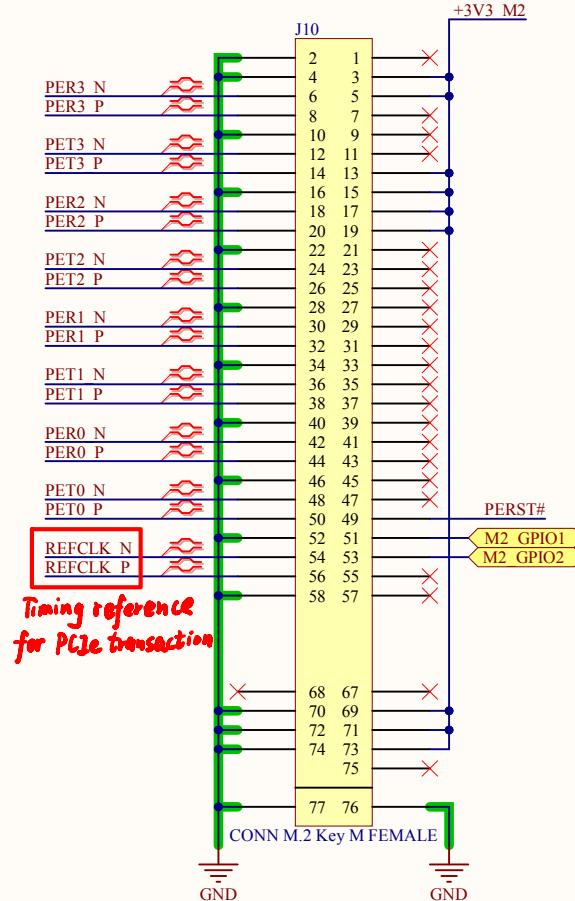
8



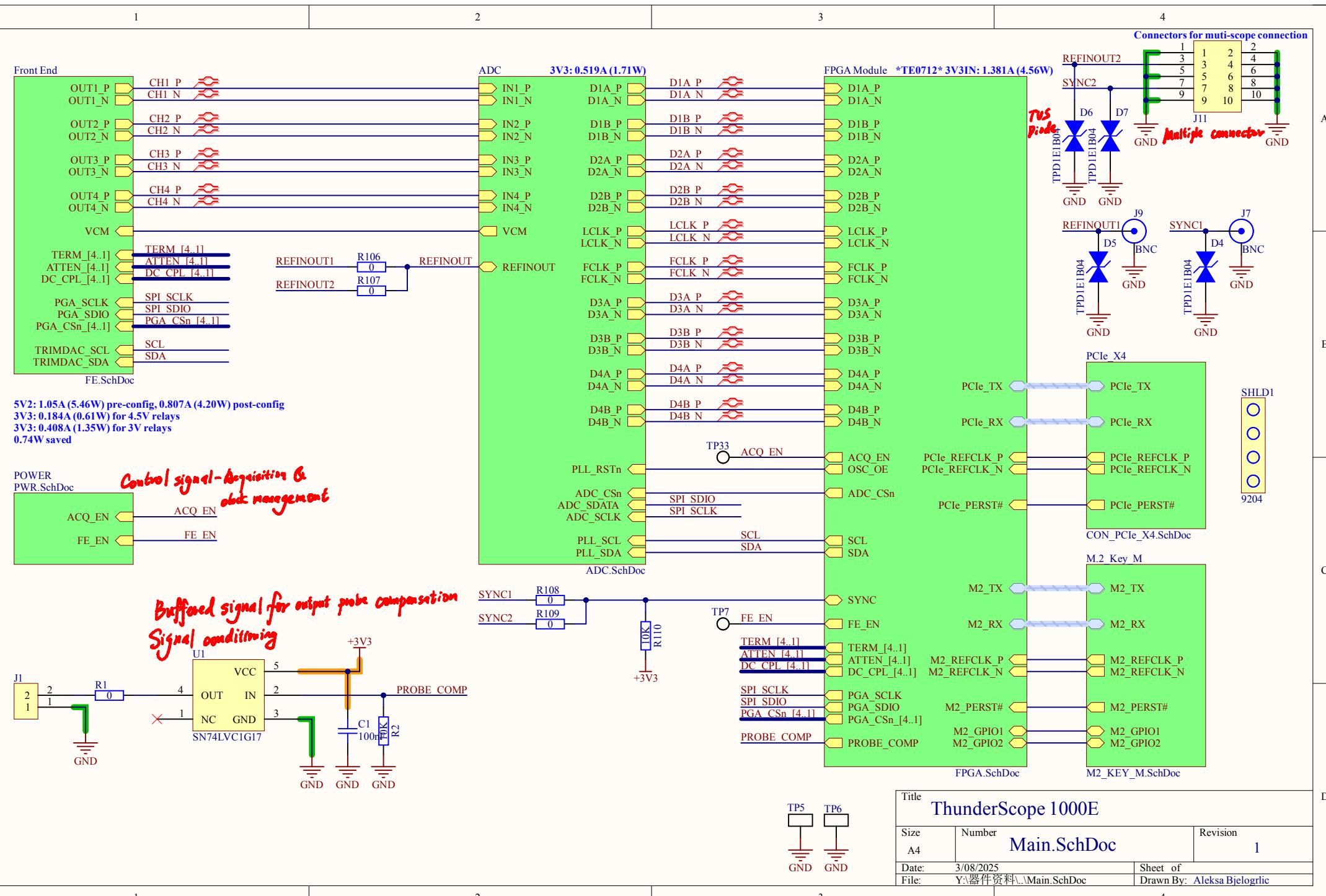


M.2 Key M interface

Main Board
Custom Pinout



Title		
Size	Number	Revision
A4		
Date: 3/08/2025	Sheet of	
File: Y:\器件资料\.\M2_KEY_M.SchDoc		Drawn By:

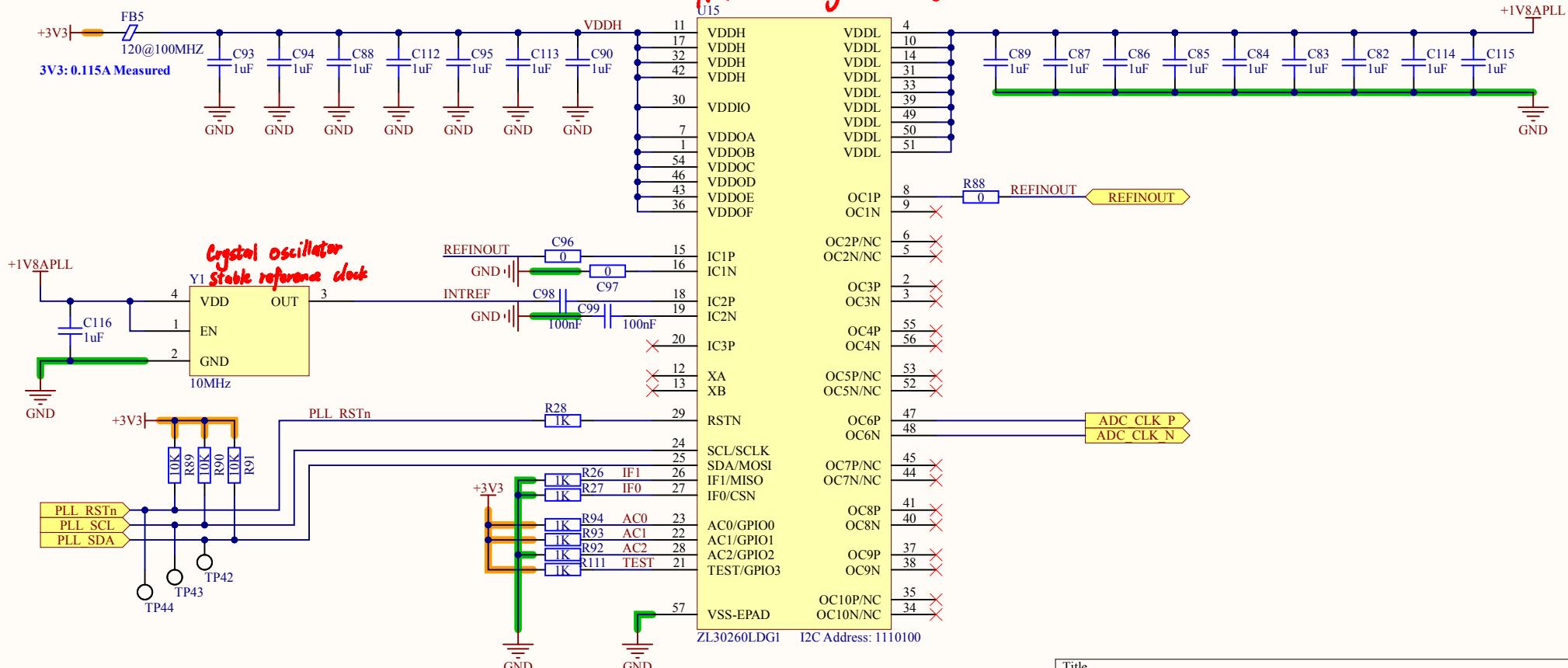


IF1	IF0	Processor Interface	Configuration Memory to Use
0	0	I ² C, slave address 11101 00	Internal ROM
0	1	I ² C, slave address 11101 01	Internal ROM
1	0	SPI Slave	Internal ROM
1	1	SPI Master during auto-configuration then SPI Slave	External SPI EEPROM

To configure the device as specified in the first three rows above but *without* auto-configuring from internal ROM, wire devices pins as follows: TEST=1 and AC[2:0]=011, as described in section 5.2.

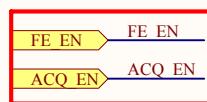
AC2	AC1	AC0	Auto Configuration
0	0	0	Configuration 0
0	0	1	Configuration 1
0	1	0	Configuration 2
0	1	1	Configuration 3
1	0	0	Configuration 4
1	0	1	Configuration 5
1	1	0	Configuration 6
1	1	1	Configuration 7

clock management 2C
precise clock signal. Clock synchronization

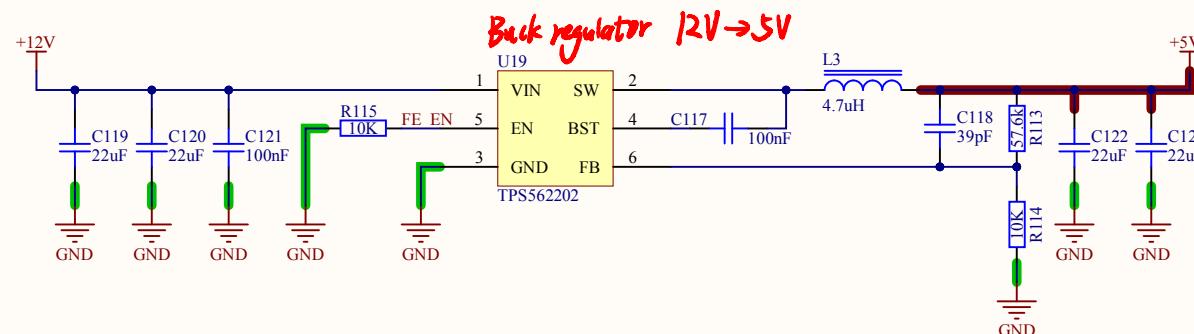
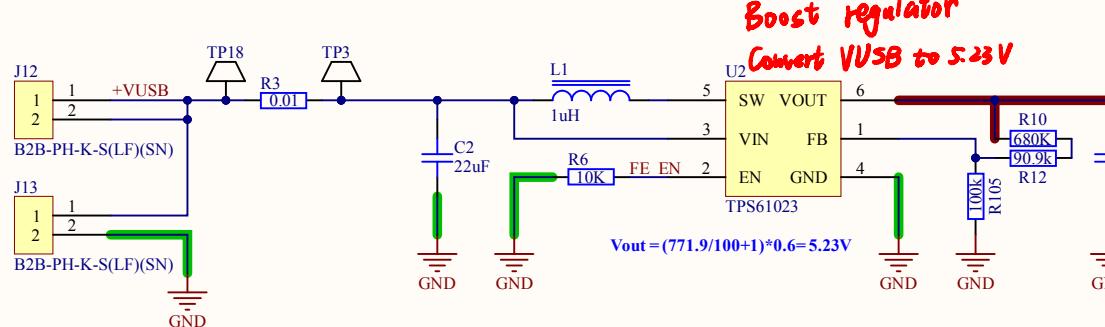
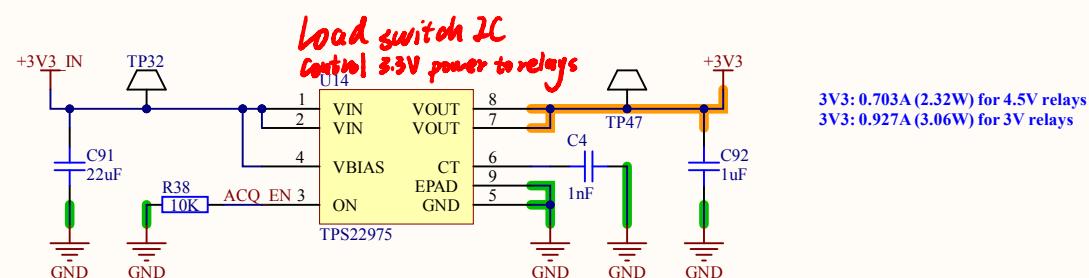


3.3 + 1.8V operation w/ one input one output: 0.67W Expected
0.62W Measured

Title		
Size	Number	Revision
A4		
Date: 3/08/2025	Sheet of	
File: Y:\器件资料\PLL.SchDoc		Drawn By:



*Enable Signal for
Controlling power sections*



Title		
Size	Number	Revision
A4		
Date: 3/08/2025	Sheet of	
File: Y:\器件资料\.\PWR.SchDoc		Drawn By:

