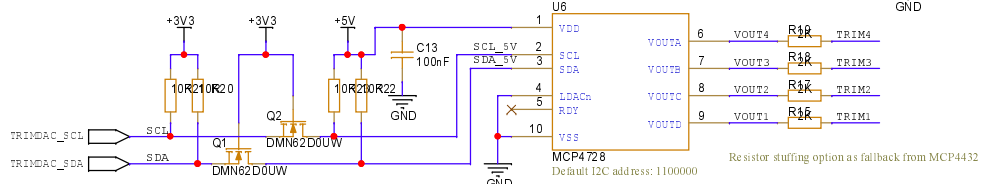
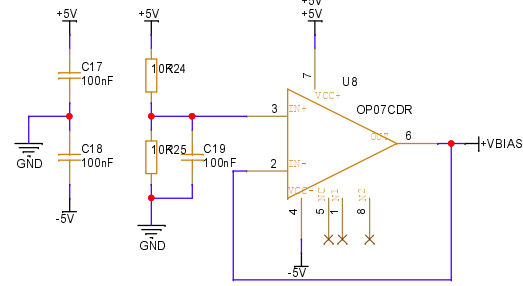
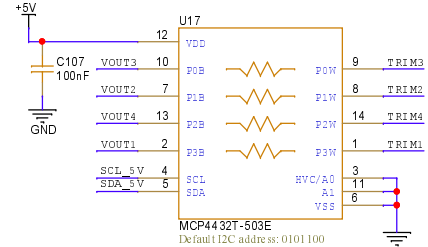
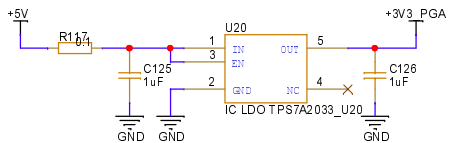
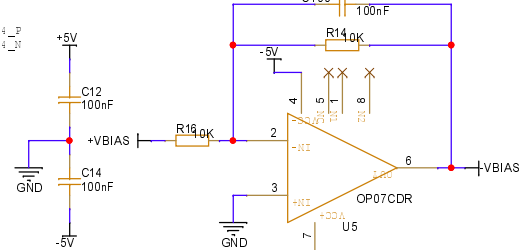
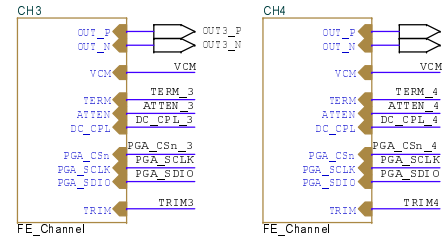
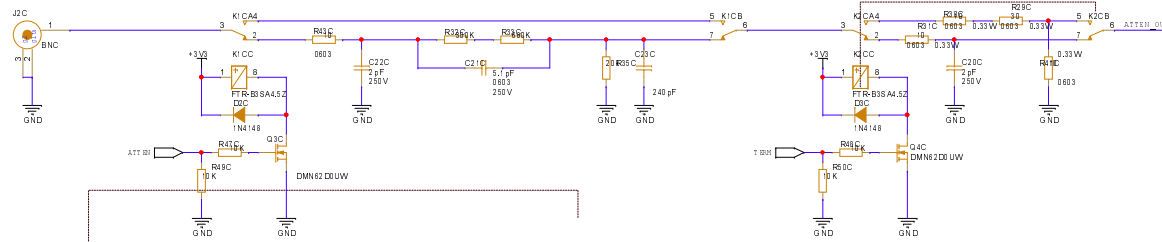


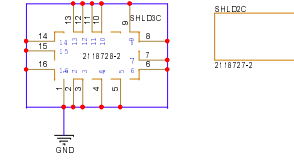
Before PGA Config: 242mA @ V+, 17.1mA @ V-, 46mA @ 3.3V per channel
After PGA Config: 182mA @ V+, 17.1mA @ V-, 46mA @ 3.3V per channel



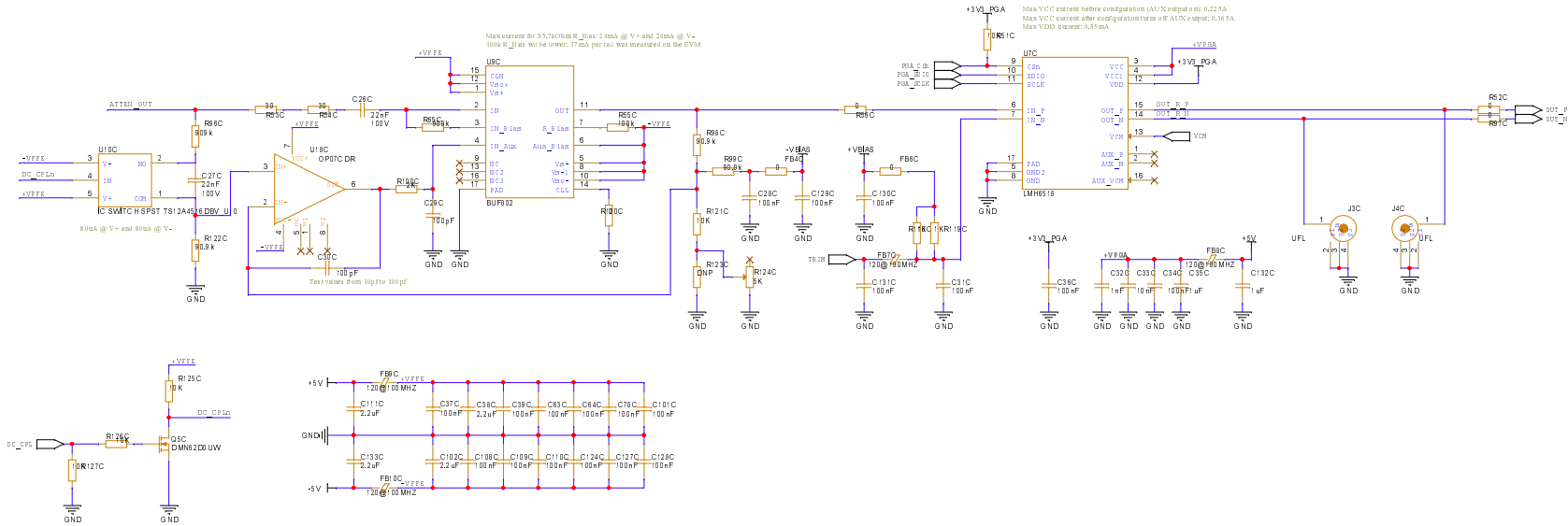
Resistor stuffing option as fallback from MCP4432

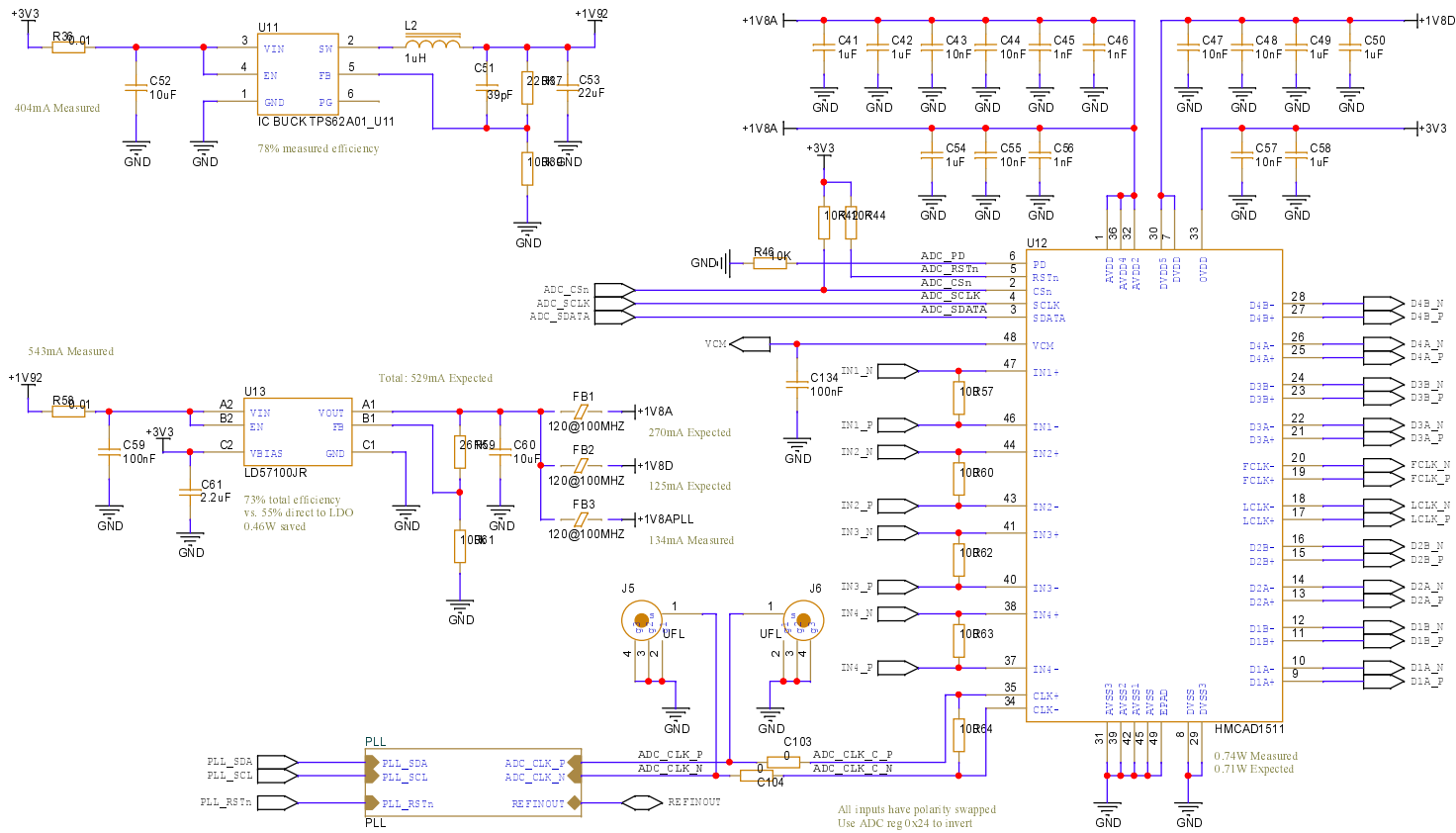


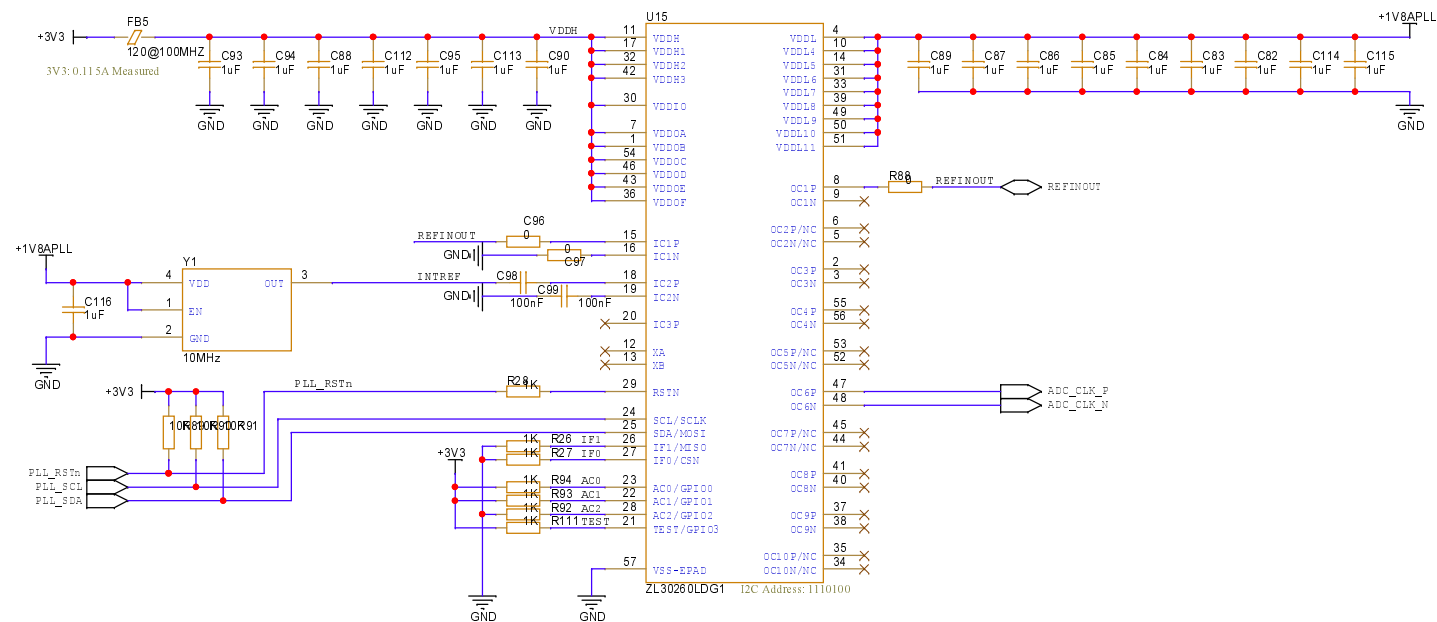
PT849 35A03 2.5k 64.3 Ohm Co-1 Resistor, 2.25V Max Operate Voltage, 0.011 A @ 3.3V
 PT849 35A03 2.5k 145 Ohm Co-1 Resistor, 2.25V Max Operate Voltage, 0.022 A @ 3.3V
 All 4.5V Teigs tested worked down to 3V



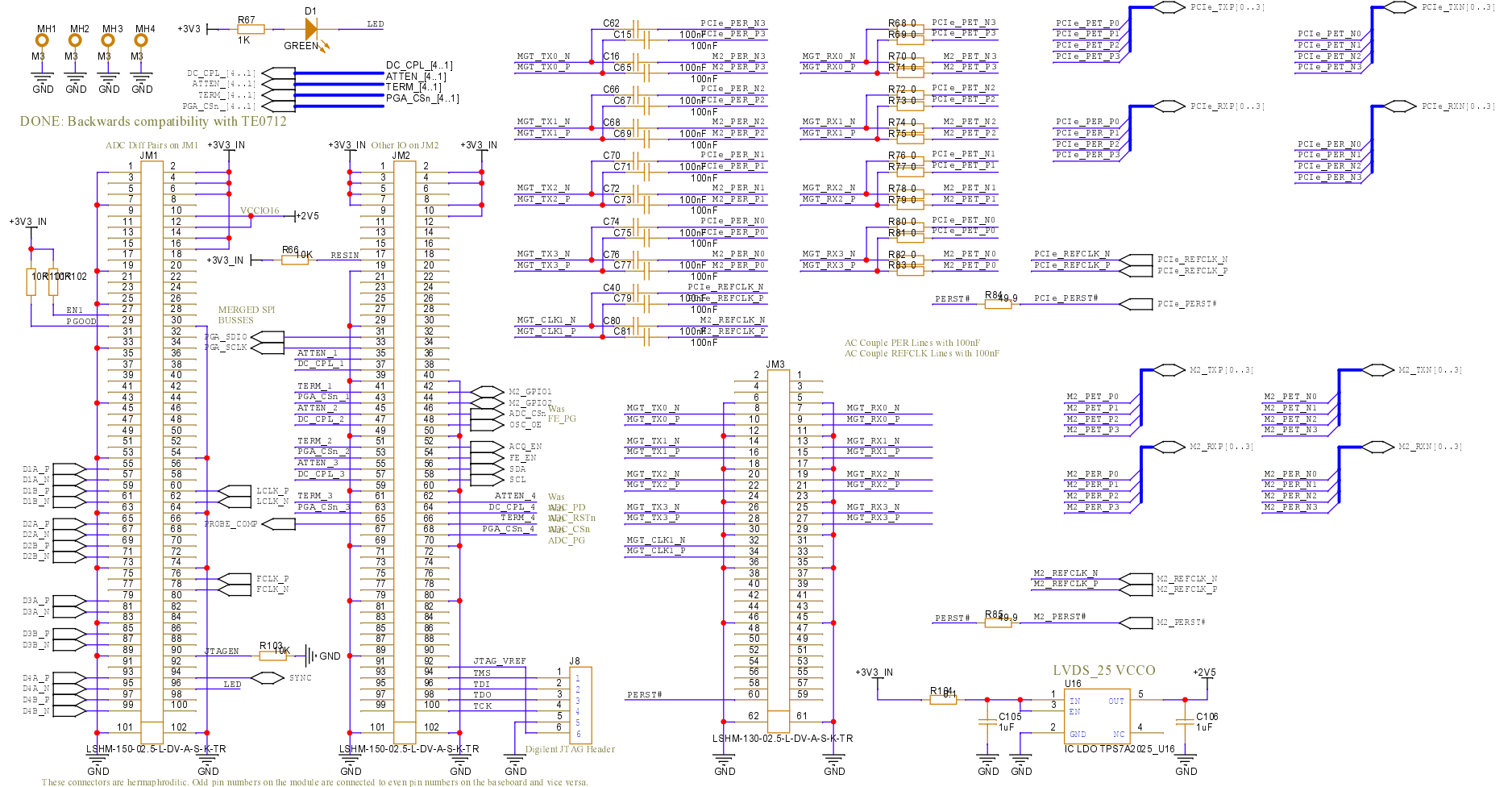
Max current for 3.576MHz R_glar: 2.4mA @ V+ and 2.4mA @ V-
 100k R_glar will be lower: 17mA per rail was measured on the EVM

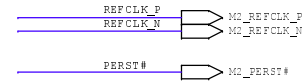
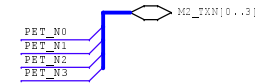
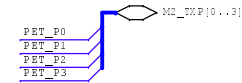
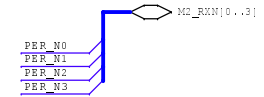
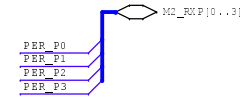
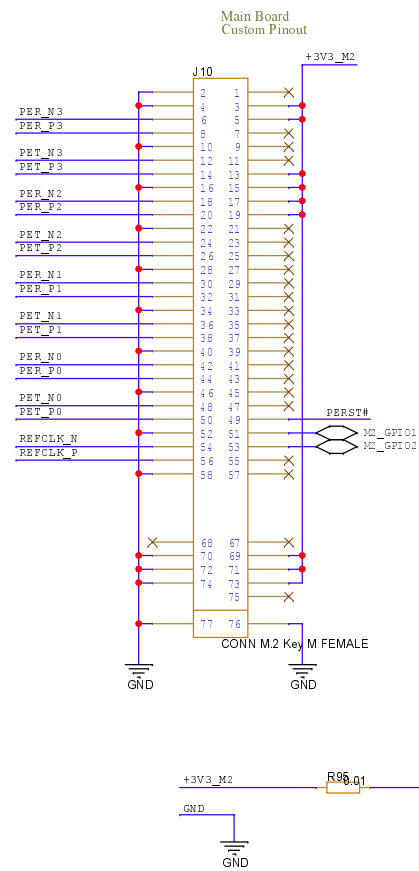


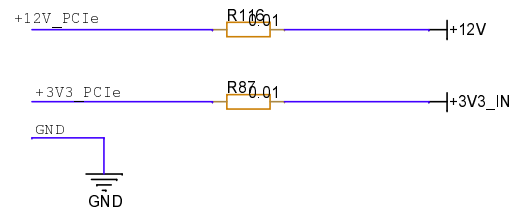
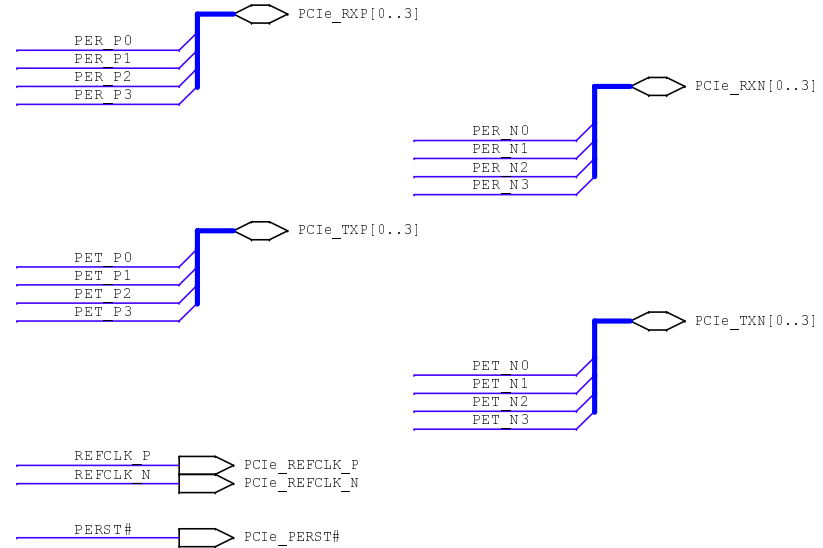
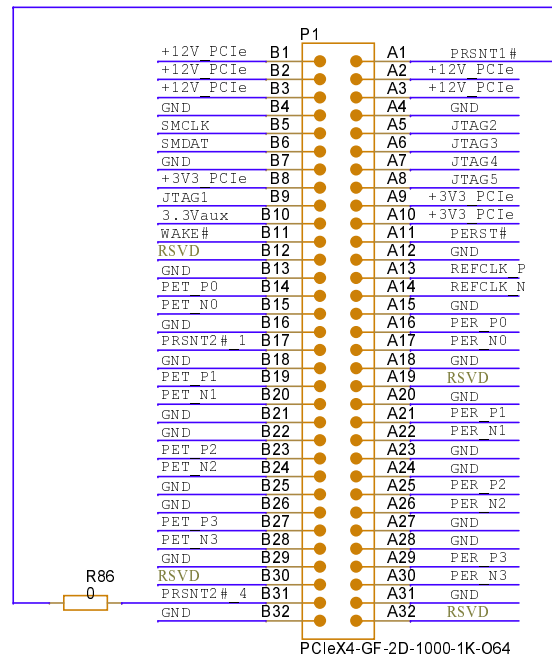




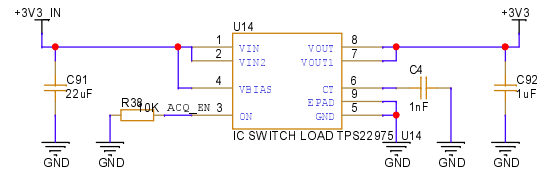
3.3V±6%V_{exp} input/output:
0.62W Measured



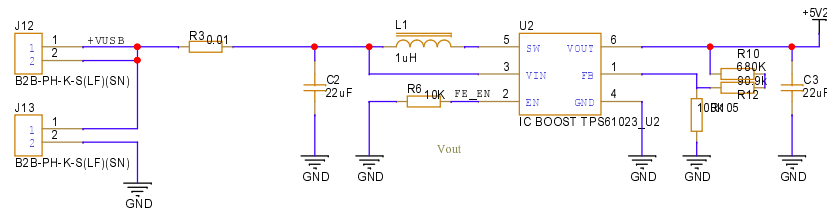




FE_EN
ACQ_EN



3V3: 0.703A (2.32W) for 4.5V relays
3V3: 0.927A (3.06W) for 3V relays



5V2: 1.05A pre-config, 0.807A post-config

