

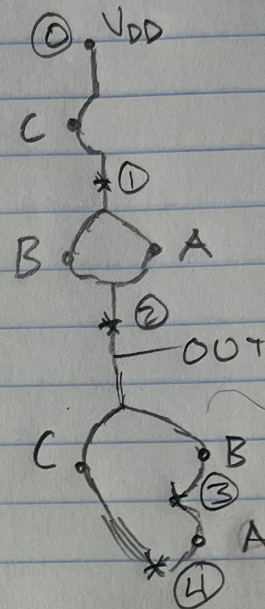
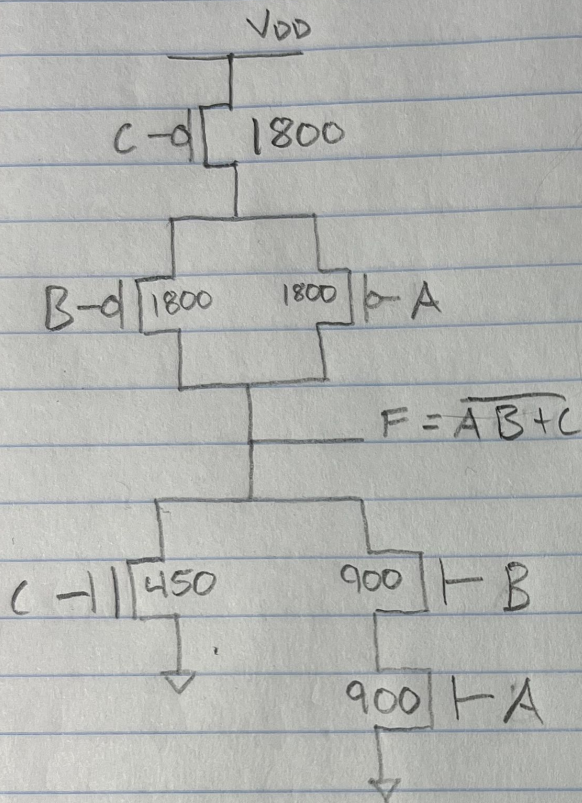
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10/10/23
Lab 06

The screenshots below taken for the lab exercise represent the following. Each number pertains to a screenshot in the order they are shown.

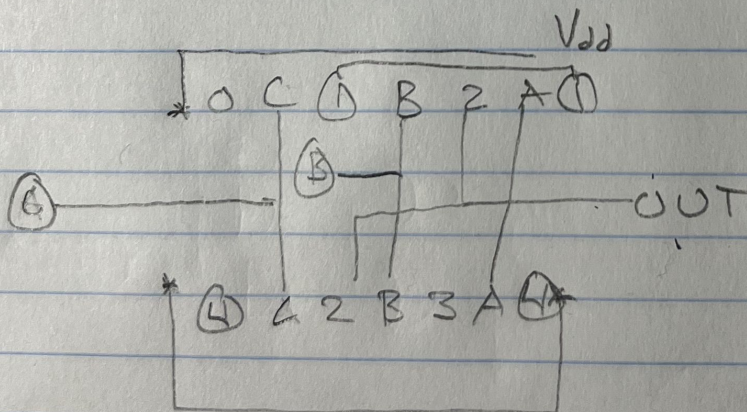
1. The transistor diagram with widths, Euler diagram, and stick diagram of $F = \text{NOT}(AB+C)$
2. The stick diagram recreated in the MicroWind Editor
3. The voltage vs. time graph of screenshot #2. The green line represents input A, the white line represents input B, the yellow line represents input C, and the red line represents the output.

Lab 6

$$F = \overline{AB+C} \quad W_n = 450\text{nm} \quad W_p = 900\text{nm} \quad L = 90\text{nm}$$



Euler order
 PMOS: 0 C 1 B 2 A 1
 NMOS: 4 C 2 B 3 A 4



5 λ
0.250 μm

