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lze4
10/19/23
Lab 07

The screenshots below taken for the lab exercise represent the following. Each number pertains to a screenshot in the order they are shown. All diagrams were created in the MicroWind editor.

1. The transistor diagram of INV_1 with $W_p = 600\text{nm}$, $W_n = 300\text{nm}$.
2. The transistor diagram of NAND2_1 with $W_p = 600\text{nm}$, $W_n = 600\text{nm}$.
3. The transistor diagram of NOR2_1 with $W_p = 1200\text{nm}$, $W_n = 300\text{nm}$.
4. The transistor diagram of XOR2_1 with $W_p = 1200\text{nm}$, $W_n = 600\text{nm}$.
5. The transistor diagram of XNOR2_1 with $W_p = 1200\text{nm}$, $W_n = 600\text{nm}$.









