Asymmetric Multicore Processor Scheduling

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November 22

1 Introduction

1.1 Why We Chose Asymmetric Multicore Processors

It has been hypothesised that ARM processors will begin to be used outside of mobile phones, and when this becomes popular, it would be nice to have a scheduler that is both efficient *and* fast. Current implementations are extremely focused on power management, leaving significant room for improvements in speed (throughput).

1.2 Current ARM Implementation

2 Types of Asymmetric Scheduling

3 Optimization Targets

4 WASH Implementation

5 Alternatives