

# UNIVERSAL ISM BAND FSK TRANSCEIVER MODULE RFM12B

(the purpose of this spec covers mainly for the physical characteristic of the module, for register configure and its related command info please refer to <a href="RF12B">RF12B</a> data sheets</a>)

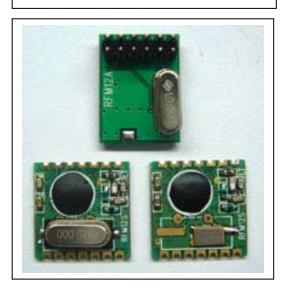
#### **General Introduction**

RFM12B is a low costing ISM band transceiver module implemented with unique PLL. It works signal ranges from 433/868/915MHZ bands, comply with FCC, ETSI regulation. The SPI interface is used to communicate with microcontroller for parameter setting.

#### Features:

- · Low costing, high performance and price ratio
- Tuning free during production
- PLL and zero IF technology
- Fast PLL lock time
- High resolution PLL with 2.5 KHz step
- High data rate (up to 115.2 kbps with internal demodulator, with external RC filter highest data rate is 256 kbps)
- Differential antenna input/output
- · Automatic antenna tuning
- Programmable TX frequency deviation (from 15 to 240 KHz)
- Programmable receiver bandwidth (from 67 to 400 kHz)
- Analog and digital signal strength indicator (ARSSI/DRSSI)
- Automatic frequency control (AFC)
- Data quality detection (DQD)
- Internal data filtering and clock recovery
- RX synchron pattern recognition
- SPI compatible serial control interface
- Clock and reset signal output for external MCU use
- 16 bit RX Data FIFO
- Two 8 bit TX data registers
- Standard 10 MHz crystal reference
- Wakeup timer
- 2.2V 3.8V power supply
- Low power consumption
- Standby current less than 0.3uA
- · Supports very short packets (down to 3 bytes)

#### RFM12B

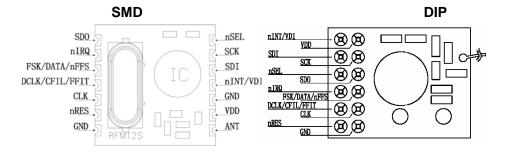




## **Typical Application:**

- Remote control
- Remote sensor
- · Wireless data collection
- Home security system
- Toys
- Tire pressure monitoring system

## **Pin Definition:**



definition	Туре	Function
nINT/VDI	DI/ DO	Interrupt input (active low)/Valid data indicator
VDD	S	Positive power supply
SDI	DI	SPI data input
SCK	DI	SPI clock input
nSEL	DI	Chip select (active low)
SDO	DO	Serial data output with bus hold
nIRQ	DO	Interrupts request output (active low)
FSK/DATA/nFFS	DI/DO/DI	Transmit FSK data input/ Received data output (FIFO not used)/ FIFO
		select
DCLK/CFIL/FFIT	DO/AIO/DO	Clock output (no FIFO )/ external filter capacitor(analog mode)/ FIFO
		interrupts(active high)when FIFO level set to 1, FIFO empty
		interruption can be achieved
CLK	DO	Clock output for external microcontroller
nRES	DIO	Reset output (active low)
GND	S	Power ground

## **Electrical Parameter:**

#### Maximum (not at working mode)

symbol	parameter	minimum	maximum	Unit
$V_{dd}$	Positive power supply	-0.5	6.0	V
V <sub>in</sub>	All pin input level	-0.5	Vdd+0.5	V

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## RFM12B

Datasheet REV1.2

I <sub>in</sub>	Input current except power	-25	25	mA
ESD	Human body model		1000	V
T <sub>st</sub>	Storage temperature	-55	125	$^{\circ}$ C
T <sub>Id</sub>	Soldering temperature(10s)		260	$^{\circ}$ C

Recommended working range

symbol	parameter	minimum	maximum	Unit
$V_{dd}$	Positive power supply	2.2	3.8	V
T <sub>op</sub>	Working temperature	-40	85	$^{\circ}$

#### **DC** characteristic

symbol	parameter	Remark	minimum	typical	maximum	Unit
I <sub>dd_TX_0</sub>	Supply current	315,433MHz band		15	17	mA
	(TX mode, $P_{out} = 0dBm$ )	868MHz band		16	18	
		915MHz band		17	19	
I <sub>dd_TX_PMAX</sub>	Supply current	315,433MHz band		22	24	mA
	(TX mode, $P_{out} = P_{max}$ )	868MHz band		23	25	
		915MHz band		24	26	
I <sub>dd_RX</sub>	Supply current	315,433MHz band		11	13	mA
	(RX mode)	868MHz band		12	14	
		915MHz band		13	15	
I <sub>x</sub>	Idle current	Crystal oscillator on		0.62	1.2	mA
I <sub>pd</sub>	Sleep mode current	All blocks off		0.3		uA
I <sub>lb</sub>	Low battery detection			0.5		uA
V <sub>Ib</sub>	Low battery detect	0.1V per step	2.2		3.7	V
	threshold					
V <sub>lba</sub>	Low battery detection accuracy		0		5	%
V <sub>il</sub>	Low level input				0.3*V <sub>dd</sub>	V
V <sub>ih</sub>	High level input		0.7*V <sub>dd</sub>			V
I <sub>ii</sub>	Leakage current	V <sub>il</sub> =0V	-1		1	uA
I <sub>ih</sub>	Leakage current	$V_{ih}=V_{dd}, V_{dd}=5.4V$	-1		1	uA
V <sub>ol</sub>	Low level output	I <sub>ol</sub> =2mA			0.4	V
V <sub>oh</sub>	High level output	I <sub>oh</sub> =-2mA	V <sub>dd</sub> -0.4			V

#### **AC** characteristic

symbol	parameter	remark	min	typical	max	Unit
f <sub>ref</sub>	PLL frequency		9	10	11	MHz
	frequency	433 MHz band,2.5KHz step	430.24		439.7	
f <sub>LO</sub>	(10MHz crystal	868 MHz band,5KHz step	860.48		5	MHz
	used)	915 MHz band,7.5KHz step	900.72		879.5	
					1	
					929.2	
					7	

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Datasheet REV1.2

		_			,	
	frequency	433 MHz band,2.5KHz step	387.22		395.7	
$f_{\text{LO}}$	(9MHZ crystal	868 MHz band,5KHz step	774.43		6	MHz
	used)	915 MHz band,7.5KHz step	810.65		791.5	
					6	
					836.3	
					4	
	frequency	433 MHz band,2.5KHz step	473.26		483.7	
$f_{LO}$	(11MHZ crystal	868 MHz band,5KHz step	946.53		3	MHz
	used)	915 MHz band,7.5KHz step	990.79		967.4	
					6	
					1022.	
					2	
BW	Receiver	mode 0	60	67	75	
	bandwidth	mode 1	120	134	150	
		mode 2	180	200	225	KHz
		mode 3	240	270	300	
		mode 4	300	350	375	
		mode 5	360	400	450	
$t_{\text{lock}}$	PLL lock time	After 10MHz step hopping, frequency		30		us
		error <10 kHz				
	PLL startup time	With a running crystal		200	300	us
tst, P	1 LE diartap iiino	oscillator		200	000	do
BR	Data rate	With internal digital demodulator	0.6		115.2	kbps
BR <sub>A</sub>	Data rate	With external RC filter			256	kbps
		BER 10 <sup>-3</sup> , BW=134KHz,BR=1.2kbps, 433MHz band		-109	-100	
$P_{min}$	sensitivity	BER 10 <sup>-3</sup> , BW=134KHz,BR=1.2kbps, 868MHz band		-105	-100	dBm
		BER 10 <sup>-3</sup> , BW=134KHz,BR=1.2kbps, 915MHz band		-105	-100	
AFC <sub>range</sub>	AFC working	df <sub>FSK</sub> : FSK deviation in the received		0.8*		
	range	signal		df <sub>FSK</sub>		
RSA	RSSI accuracy			±5		dB
RS <sub>R</sub>	RSSI range			46		dB
C <sub>ARSSI</sub>	ARSSI filter			1		nF
RS <sub>STEP</sub>	RSSI			6		dB
	programmable					
	step					
RS <sub>RESP</sub>	DRSSI response	RSSI output high after valid ,		500		us
	time	CARRSI=5nF				



# RFM12B

Datasheet REV1.2

AC characteristic(Transmitter)

symbol	parameter	remark	min	typical	max	Unit
		433MHz band	3	5		
P <sub>max</sub>	Max. available output power	868MHz band	2	4		dBm
		915MHz band	2	4		
P <sub>out</sub>	Typical output power	Selectable in 3 dB	P <sub>max</sub> -21		P <sub>max</sub>	dbm
		steps				
Co	Output capacitance	In low bands	2	2.6	3.2	pf
	(set by the automatic antenna	In high bands	2.1	2.7	3.3	
	tuning circuit)					
Q <sub>o</sub>	Quality factor of the output	In low bands	13	15	17	
	capacitance	In high bands	8	10	12	
Lout	Output phase noise	100 kHz from carrier			-80	dbc/HZ
		1 MHz from carrier			-103	
BR <sub>TX</sub>	FSK bit rate	Via internal TX data			172	kbps
		register				
BRA <sub>TX</sub>	FSK bit rate	TX data connected to the			256	kbps
		FSK input				
df <sub>fsk</sub>	FSK frequency deviation	Programmable in 15	15		240	kHZ
		kHz steps				

AC characteristic(Turn-on/Turnaround timings)

symbol	parameter	remark	min	typical	max	Unit
T <sub>st</sub>	Crystal oscillator	Crystal ESR < 100		1	5	ms
	startup time					
_	Transmitter turn-on	Synthesizer off, crystal		250		us
$T_{tx\_XTAL\_ON}$	time	oscillator on with 10 MHz step		250		us
_	Receiver turn-on time	Synthesizer off, crystal		250		us
T <sub>rx_XTAL_ON</sub>		oscillator on with 10 MHz step		250		us
	Transmitter –	Synthesizer and crystal				
T <sub>tx_rx_SYNT_ON</sub>	Receiver turnover time	oscillator on during TX/RX		150		us
		change with 10 MHz step				
	Receiver -	Synthesizer and crystal				
$T_{rx\_tx\_SYNT\_ON}$	Transmitter turnover	oscillator on during RX/TX		150		us
	time	change with 10 MHz step				
C <sub>xl</sub>	Crystal load	Programmable in 0.5 pF steps,	8.5		16	pf
	capacitance	tolerance+/- 10%				
t <sub>POR</sub>	Internal POR timeout	After V <sub>dd</sub> has reached 90% of			100	ms
		final value				
t <sub>PBt</sub>	Wake-up timer clock	Calibrated every 30 seconds	0.96		1.05	ms
	period					
C <sub>in, D</sub>	Digital input apacitance				2	pf
t <sub>r, f</sub>	Digital output rise/fall time	15pF pure capacitive load			10	ns

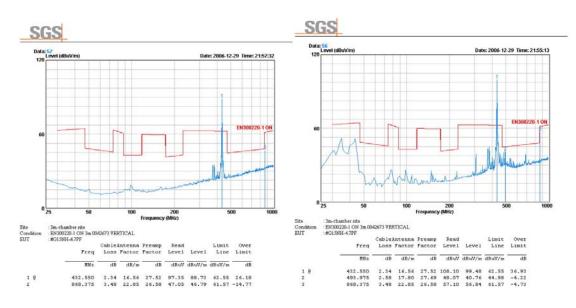
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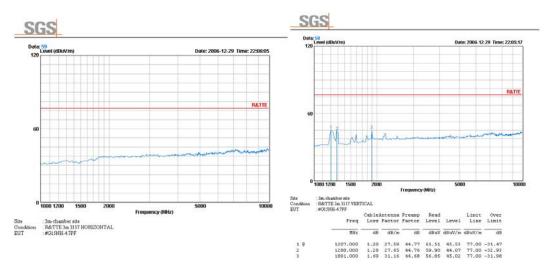


Field testing range

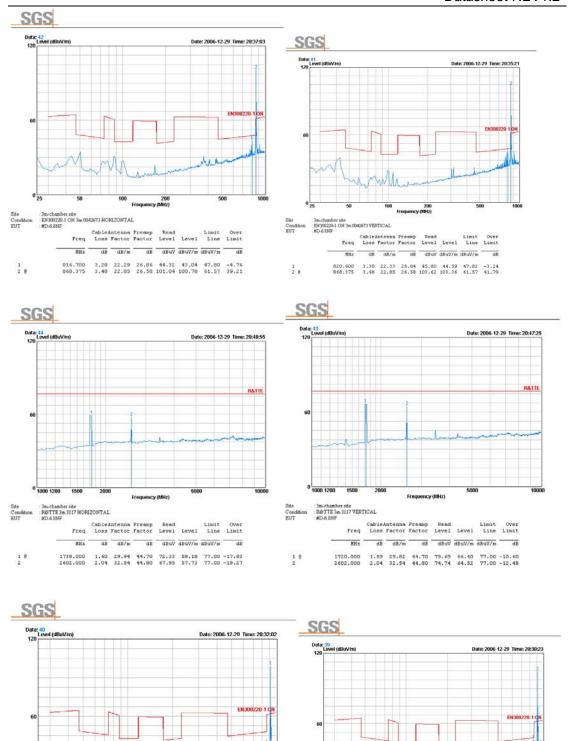
Band	Test condition	
433MHz band	Receiver bandwidth =67KHz, data rate=1.2kbps, transmitter frequency	
	deviation =45KHZ (matches with RFM12) In free open area	
868MHz band	Receiver bandwidth=67KHz,data rate =1.2kbps,Transmitter frequency	>200M
	deviation =45KHZ (matches with RFM12) in free open area	
915MHz band	Receiver bandwidth=67KHz,data rate =1.2kbps,Transmitter frequency	>200M
	deviation =45KHZ (matches with RFM12) in free open area	

## **SGS** Reports



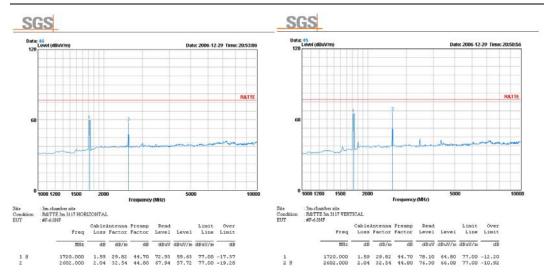








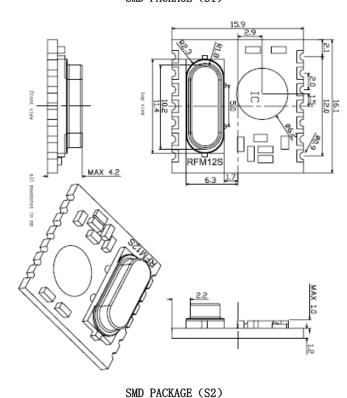




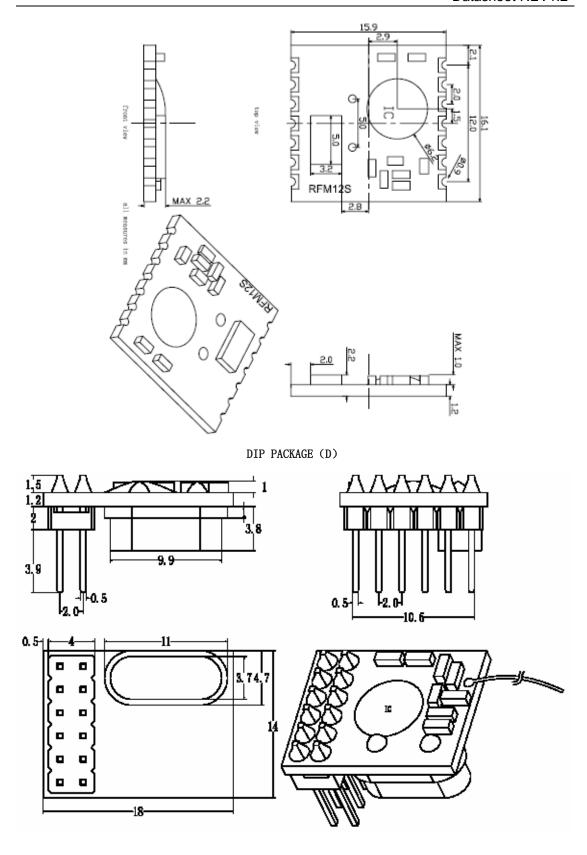
### **Mechanical Dimension**

(units in mm)

#### SMD PACKAGE (S1)



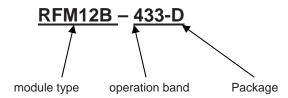






#### **Module Model Definition**

model=module-operation\_band-package\_type



example: 1, RFM12B module at 433MHz band, DIP: RFM12B-433-D.

2, RFM12B module at 868MHZ band, SMD, thickness at 4.2mm: RFM12B-868-S1.

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