DM74LS573 Octal D Latch with TRI-STATE Outputs

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General Description

The 'LS573 is a high speed octal latch with buffered common Latch Enable (LE) and buffered common Output Enable (\overline{OE}) inputs.

This device is functionally identical to the 'LS373, but has different pinouts. For truth tables, discussion of operations and AC and DC specifications, please refer to the 'LS373 data sheet

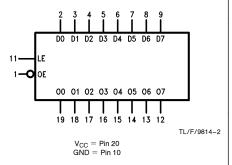
Features

- Inputs and outputs on opposite sides of package allowing easy interface with microprocessors
- Useful as input or output port for microprocessors
- Functionally identical to 'LS373
- Input clamp diodes limit high speed termination effects
- Fully TTL and CMOS compatible

Connection Diagram

Dual-In-Line Package D0 --00 -01 D1· -02 D2 D3 -03 -04 D5 -05 D6 -06 D7 -07

Logic Symbol



TL/F/9814-1

Order Number DM74LS573WM or DM74LS573N See NS Package Number M20B or N20A

Pin Names	Description
D0-D7	Data Inputs
LE OE	Latch Enable Input (Active HIGH)
ŌĒ	TRI-STATE Output Enable Input (Active LOW)
00-07	TRI-STATE Latch Outputs

Function Table

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OUTPUT Enable	Latch Enable	D	Output O
L	Н	Н	Н
L	Н	L	L
L	L	X	Qo
Н	X	Χ	Z

L = Low State, H = High State, X = Don't Care

Z = High Impedance State

 $Q_O =$ Previous Condition of O

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RRD-B30M115/Printed in U. S. A.

ww DataSheet4U c

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V
Input Voltage 7V

Operating Free Air Temperature Range

DM74LS 0° C to $+70^{\circ}$ C Storage Temperature Range -65° C to $+150^{\circ}$ C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter		Units		
	Tarameter	Min	Nom	Max	Jillis
4U.col¥cc	Supply Voltage	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			V
V _{IL}	Low Level Input Voltage			0.8	V
ІОН	High Level Input Current			-2.6	mA
l _{OL}	Low Level Output Current			24	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

Over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
V_{I}	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$			-1.5	V
V _{OH}	High Level Output Voltage	$V_{CC} = Min, I_{OH} = Max,$ $V_{IL} = Max$	2.7	3.4		٧
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max,$ $V_{IH} = Min$		0.35	0.5	٧
		I _{OL} = 4 mA, V _{CC} = Min		0.25	0.4	
I _I	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$			1	mA
I _{IH}	High Level Input Current	$V_{CC} = Max, V_{I} = 2.7V$			20	μΑ
I _{IL}	Low Level Input Current	$V_{CC} = Max, V_1 = 0.4V$			-0.4	mA
los	Short Circuit Output Current	V _{CC} = Max (Note 2)	-30		-130	mA
Icc	Supply Current	V _{CC} = Max			50	mA
lozh	TRI-STATE Output off Current High	$V_{CC} = V_{CCH}$ $V_{OZH} = 2.7V$			20	μΑ
I _{OZL}	TRI-STATE Output off Current Low	$V_{CC} = V_{CCH}$ $V_{OZL} = 0.4V$			-20	μΑ

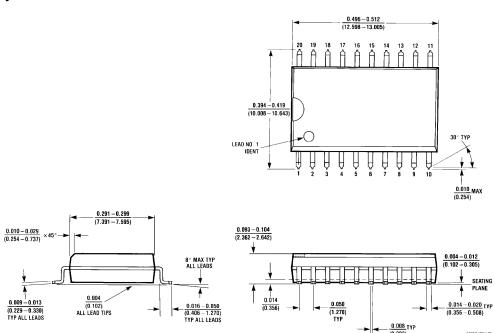
Note 1: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.

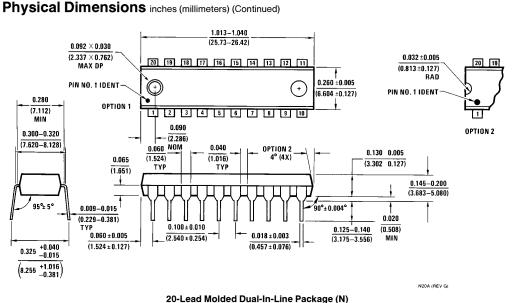
 $\begin{tabular}{lll} \textbf{Switching Characteristics} \\ at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$ (see Section 1 for Test Waveforms and output loading) \\ \end{tabular}$

Symbol	Parameter	$egin{aligned} \mathbf{R_L} &= 2\mathbf{k}\Omega, \ \mathbf{C_L} &= 50\mathbf{pF} \end{aligned}$		Units
		Min	Max	
t _{PLH} t _{PHL}	Propagation Delay Data to Q		27 18	ns
t _{PLH} t _{PHL}	Propagation Delay LE to Q		36 25	ns
t _{PZH} t _{PZL}	TRI-STATE Enable Time OE to Q		20 25	ns
t _{PHZ} t _{PLZ}	TRI-STATE Enable Time OE to Q		20 25	ns
$t_{s}(H)$ $t_{s}(L)$	Setup Time (High/Low) Data to LE	3 7		ns
t _h (H) t _h (L)	Hold Time (High/Low) Data to LE	10 10		ns
t _w (H)	Pulse Width (High) Data to LE	15		ns

Physical Dimensions inches (millimeters)



20-Lead Wide Small Outline Molded Package (M) Order Number DM74LS573WM NS Package Number M20B



20-Lead Molded Dual-In-Line Package (N) Order Number DM74LS573N NS Package Number N20A

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National Semiconductor Corporation 1111 West Bardin Road Arlington, TX 76017 Tel: 1(800) 272-9959 Fax: 1(800) 737-7018 National Semiconductor Europe

Fax: (+49) 0-180-530 85 86 Email: cnjwge@tevm2.nsc.com Deutsch Tel: (+49) 0-180-530 85 85 English Tel: (+49) 0-180-532 78 32 Français Tel: (+49) 0-180-532 93 58 Italiano Tel: (+49) 0-180-534 16 80 National Semiconductor Hong Kong Ltd. 13th Floor, Straight Block, Ocean Centre, 5 Canton Rd. Tsimshatsui, Kowloon Hong Kong Tel: (652) 2737-1600 Fax: (652) 2736-9960 National Semiconductor Japan Ltd. Tel: 81-043-299-2309 Fax: 81-043-299-2408

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