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DPU EEE FINAL TAKE ON EXAM

Due: June 15th, 2020, until 20:00

(50p) Please write a Visual C++ program for the computations of the formula sets ①,
②, ③, ④ given below.

Decibel Formulas Capacitive Reactance Resistors in Series $R_{eq} = R_1 + R_2 + R_2$ $X_C = \frac{1}{2\pi fC}$ $A_{V(dB)} = 20 \log A_{V(ratio)}$ $A_{I(dB)} = 20 \log A_{I(ratio)}$ Resistors in Parallel $X_c = Capacitive reactance$ f = Frequency $A_{P(dB)} = 10 \log A_{P(ratio)}$ C = Capacitance in Farads $A_{V(ratio)} = 10^{-20}$ Kirchhoff's Voltage Law Ohm's Law for AC Sum of all voltages in a E = IZloop is equal to zero. $\bigvee Voltages = 0$ True Power (P) $P = I^2R$ Kirchhoff's Current Law Sum of all currents Measure in watts entering and leaving a E = Voltage Reactive Power (Q) node is equal to zero. I = Current in Ampere $Q = I^2 X$ Z = Impedance in ohms Inductive Reactance Impedance in Series Measure in Volt-Amps- $X_L = 2\pi f L$ Reactive $Z_{series} = Z_1 + Z_2 + Z_3$ X_L = Inductive reactance Apparent Power (S) Impedance in Parallel $S = I^2 Z$ f = Frequency in hertz $Z_{parallel} = \frac{1}{\frac{1}{Z_1} + \frac{1}{Z_2} + \frac{1}{Z_3}}$ L = Inductance in henry Measure in Volt-Amps

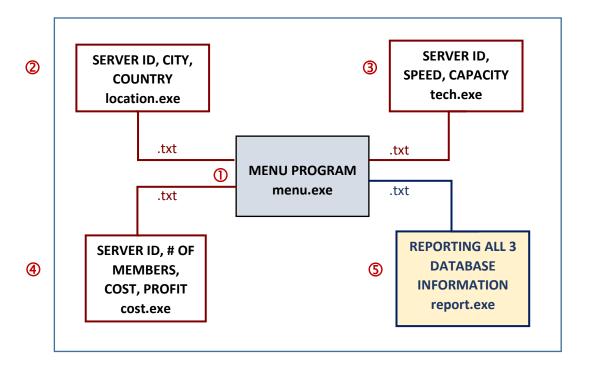
Below properties are <u>certainly</u> requested in the program* designed by you:

- Your program will be <u>menu driven</u> type. So you should use <u>switch-case-default</u> model.
- ii. Your program will have **#include <eee.h>** inclusion. So, you will design a header file with type definitions and member functions.
- iii. Your **eee.h** header file should have 4 **class** for each of 4 formula groups and their **member functions** for computations.

- iv. Notice that formula ① requires loops. You are free to choose for, while, do-while loop models.
- v. Notice that set 4 has 3 formulas

[TR: Özetle; switch-case-default yapısı ile menü kontrollü bir program yazınız. Programda tablodaki 4 formül seti kodlanacak. Formüller eee.h isimli bir header dosyası içinde tasarlanacak. eee.h dosyasında her formül seti için 4 class oluşturulacak. Programınızın çalıştığını kontrol ettikten sonra, sadece cpp ve h dosyalarını zip formatında eys.dpu.edu.tr adresine yükleyiniz.]

2. (50p) Please write a <u>package program</u> including 5 executable (.exe) files managing 3 database text files (.txt) and 1 report text file (.txt) whose contents are given in the figure below:



The menu program (menu.exe) controls all other 4 executable (.exe) files. Each and every files are individual Visual C++ projects. There are 3 databases at 3 text (.txt) files including;

- ② Server locations
- 3 Server technical properties
- Server cost and profits

^{*}Please pack .CPP file & .h file in .ZIP file, upload to eys.dpu.edu.tr and cplusplusproje@gmail.com

⑤ The other text (.TXT) file is Servers' Report File which is an output file combining data from 3 data files of other modules.

Executable files will enable generating and reading related text (.txt) files. Fields in the data files (.txt) are shown in the graph above. Either **spawnl** or **execlp** commands can be used.

*Please pack your .CPP files in .ZIP file, upload to eys.dpu.edu.tr and cplusplus@gmail.com

[TR: Özetle; veri taban yönetimi yapan bir paket program geliştirilecek. Paket programda 5 adet exe dosya yer alacak; 1 menü, 3 veri okuma/yazma işlemi yapan ve 1 raporlama programı. 3 veri dosyasındaki (.txt) alanlar grafikte yer almaktadır. Raporlama modülü ile 3 veri tabanından da yararlanarak çıktılar üretilebilecek. Her program ayrı bir Visual C++ projesi olacak ve menu.exe ile kontrol edilecek. spawnl veya execlp komutları kullanılabilir. 5 programa ait 5 cpp kod dosyasını zip olarak sıkıştırarak eys.dpu.edu.tr'ye yükleyiniz.]