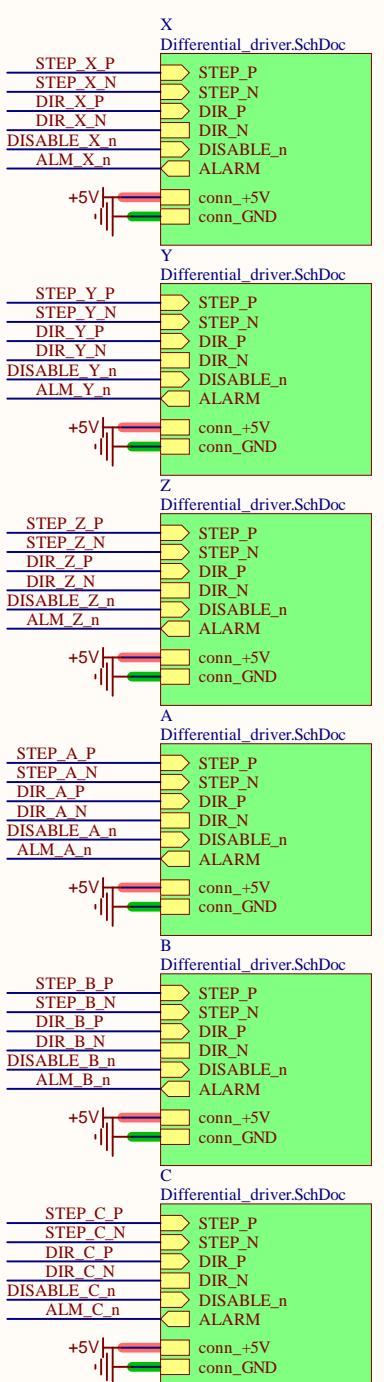
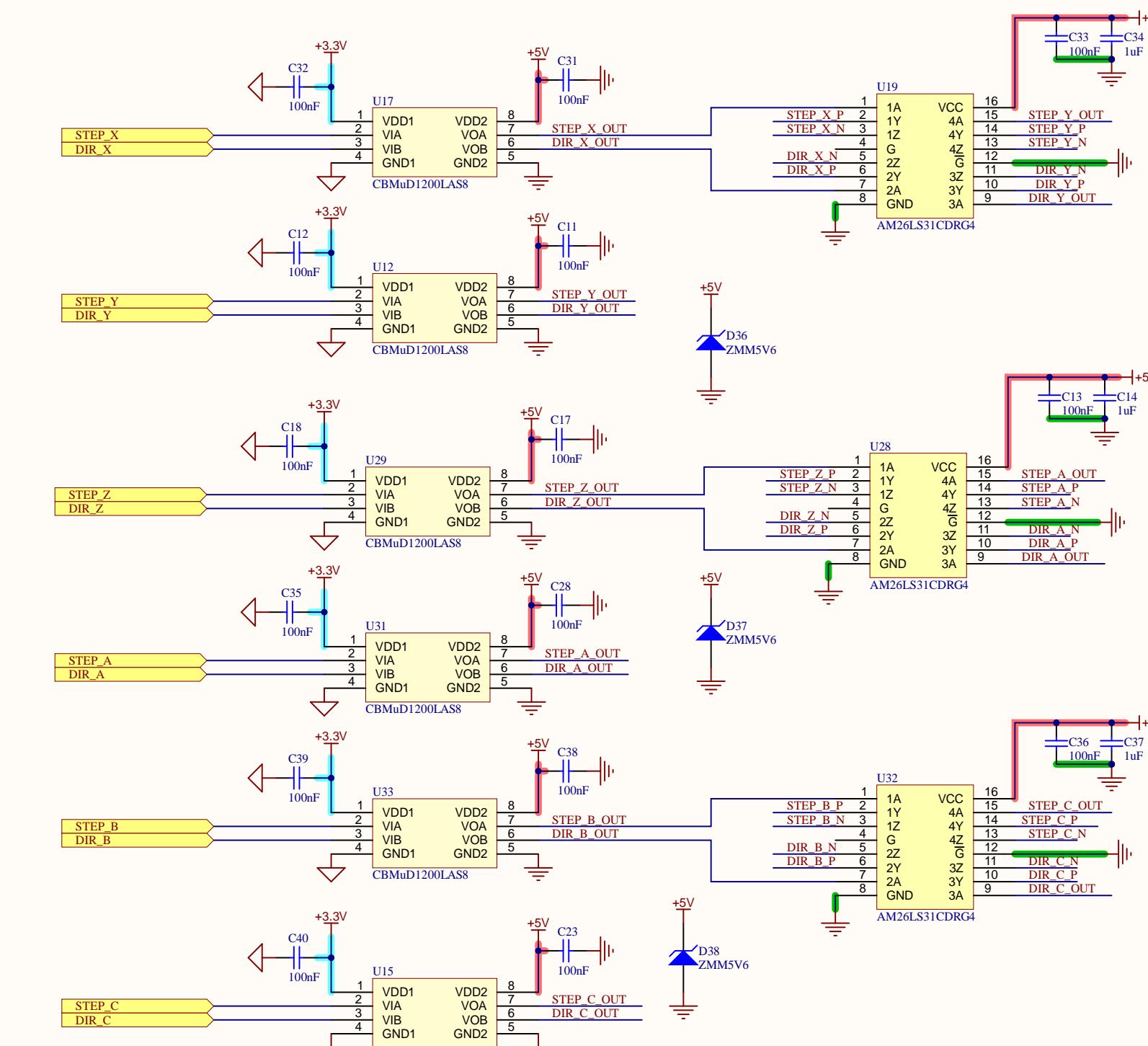


RP2350 MCU	
PROJECT	FLEXI_HAL_PRO.PjPCB
REV	A3
ENGR	AM
DOCUMENT	MCU_RP2350.SchDoc
SIZE	B
MODIFIED	2025-04-08 5:52:29 PM
SHEET	3 / 14

A



B

ALARM\_X  
DISABLE\_X\_n

ALARM\_Y  
DISABLE\_Y\_n

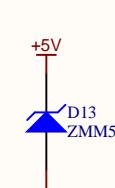
ALARM\_Z  
DISABLE\_Z\_n

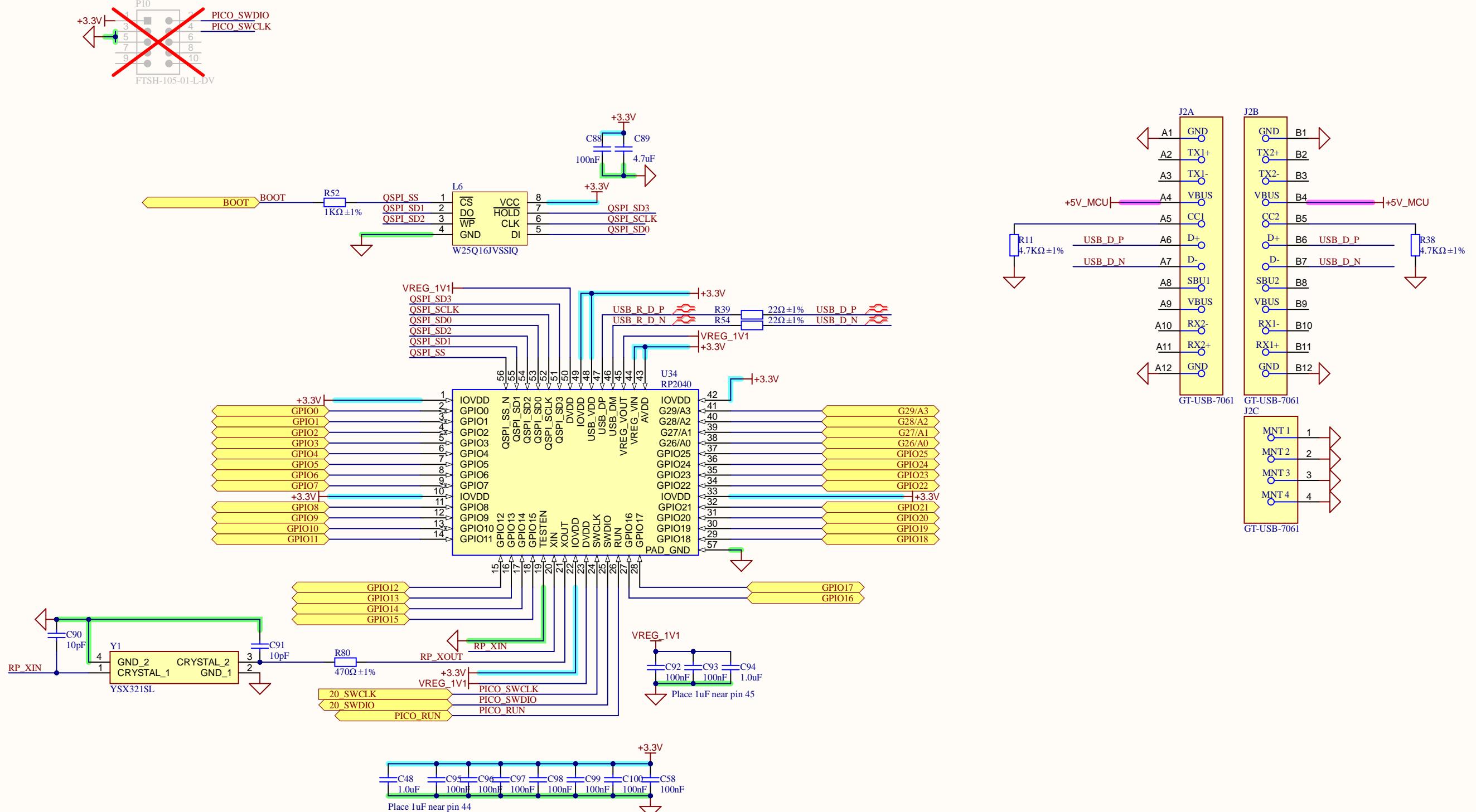
ALARM\_A  
DISABLE\_A\_n

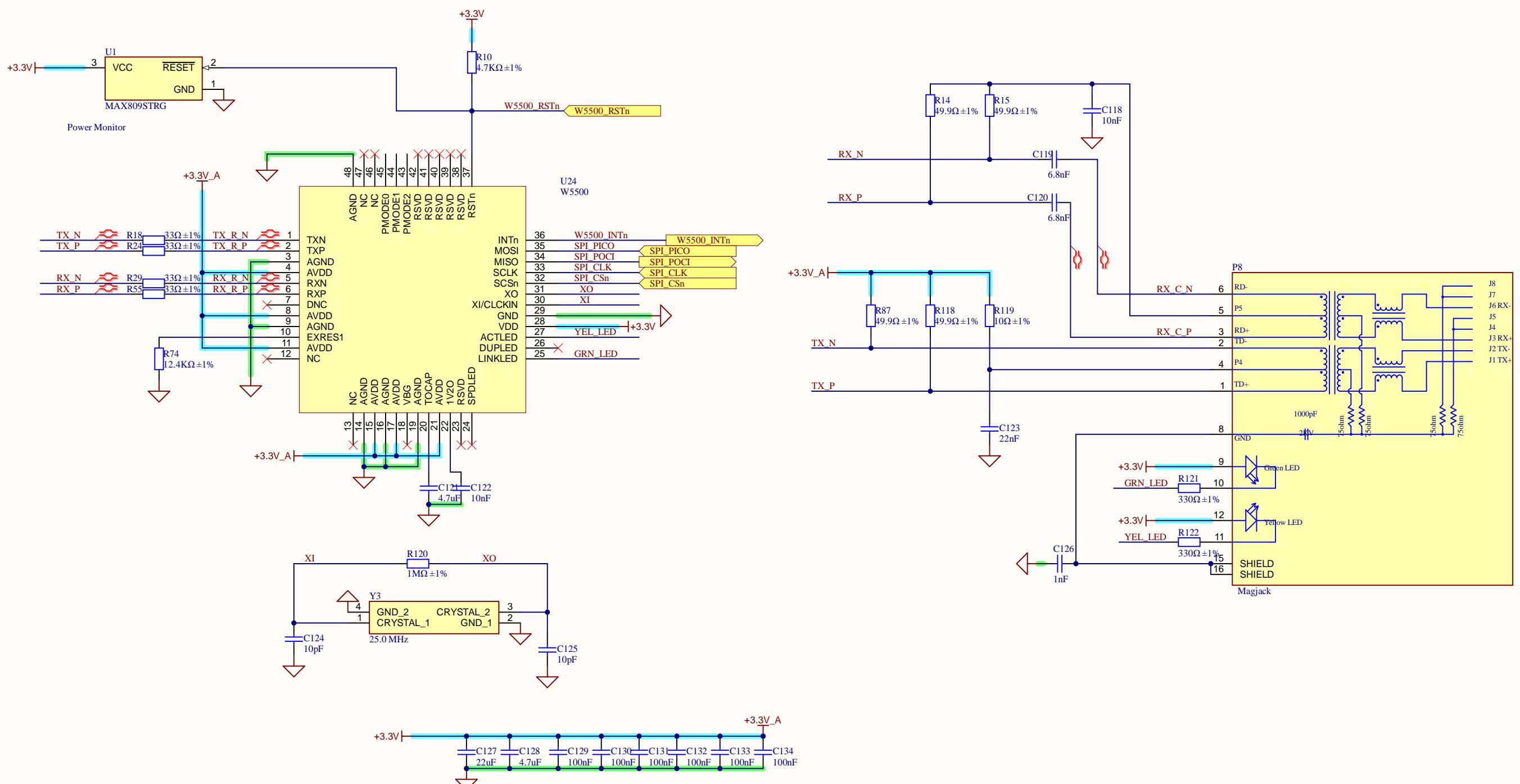
ALARM\_B  
DISABLE\_B\_n

ALARM\_C  
DISABLE\_C\_n

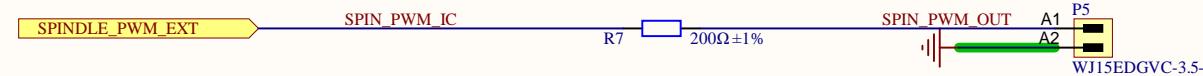
C



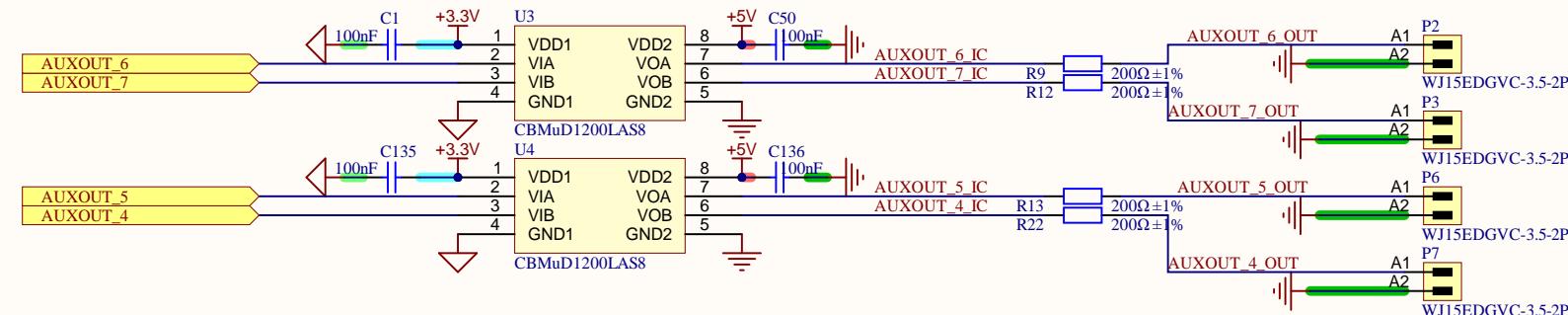




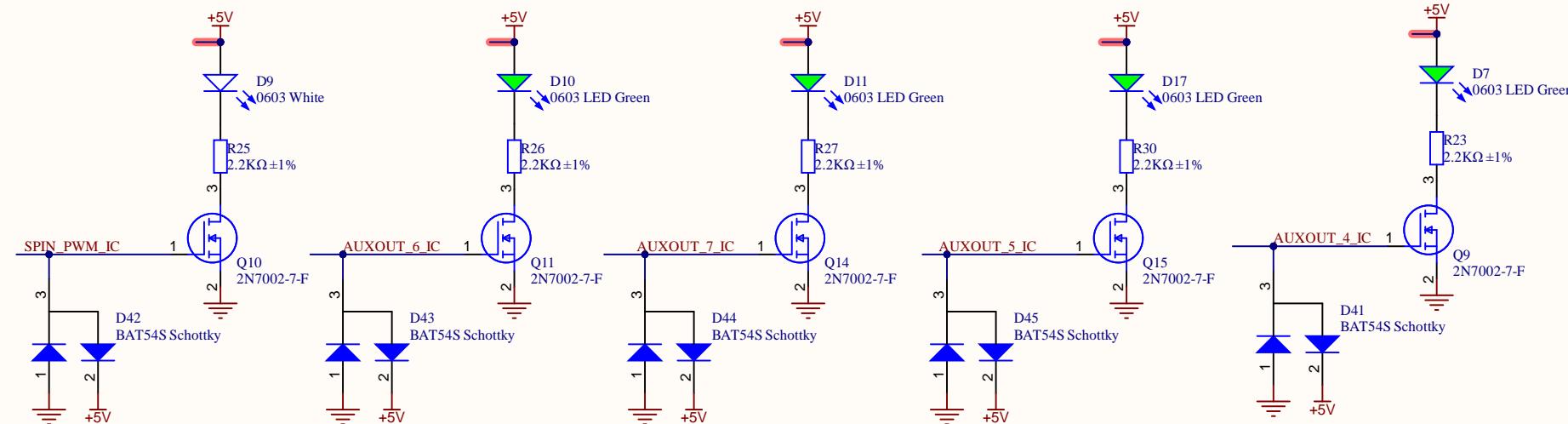
A



B



C



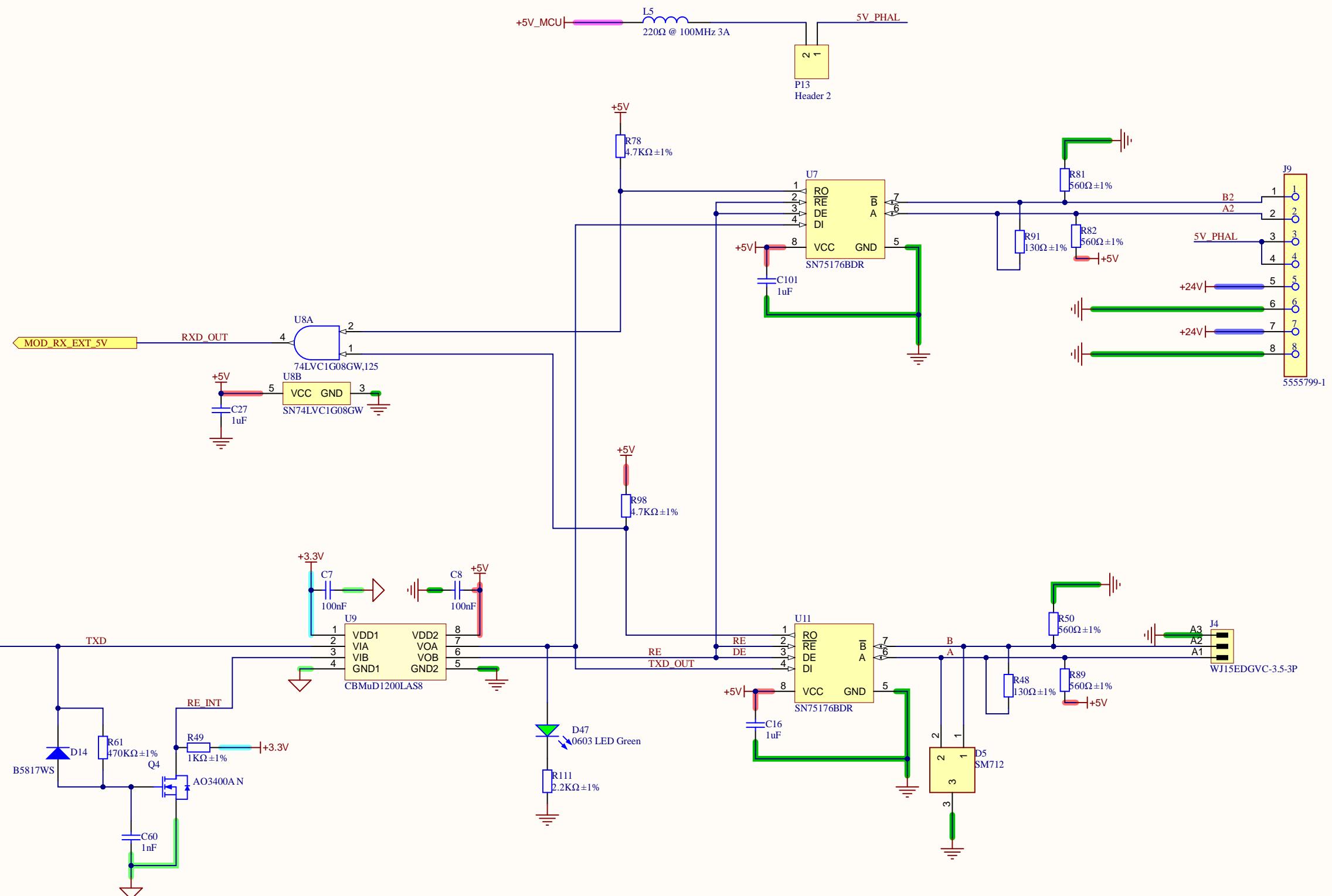
D

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Spindle Control	
PROJECT	REV
ENGR	DOCUMENT
FLEXI_HAL_PRO.PnjPCB	A3
AM	PWM_OUT.SchDoc
SIZE B	MODIFIED 2025-04-08 5:52:30 PM
	SHEET 5 / 14



A

A

B

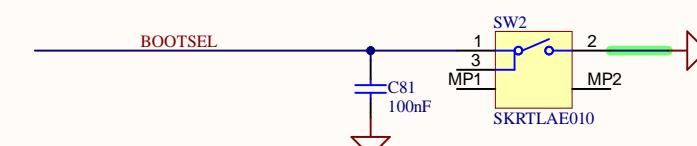
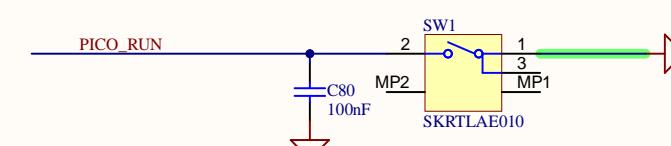
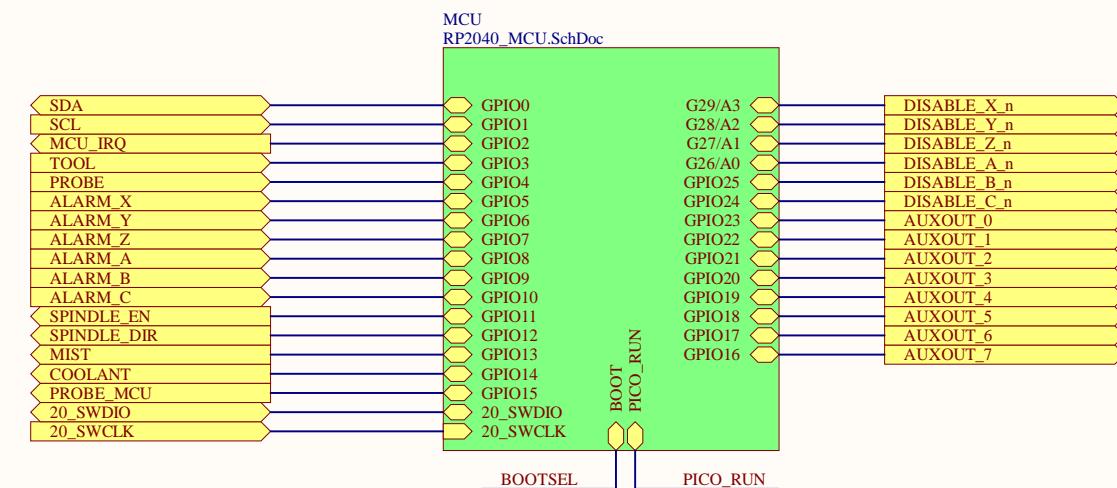
B

C

C

D

D

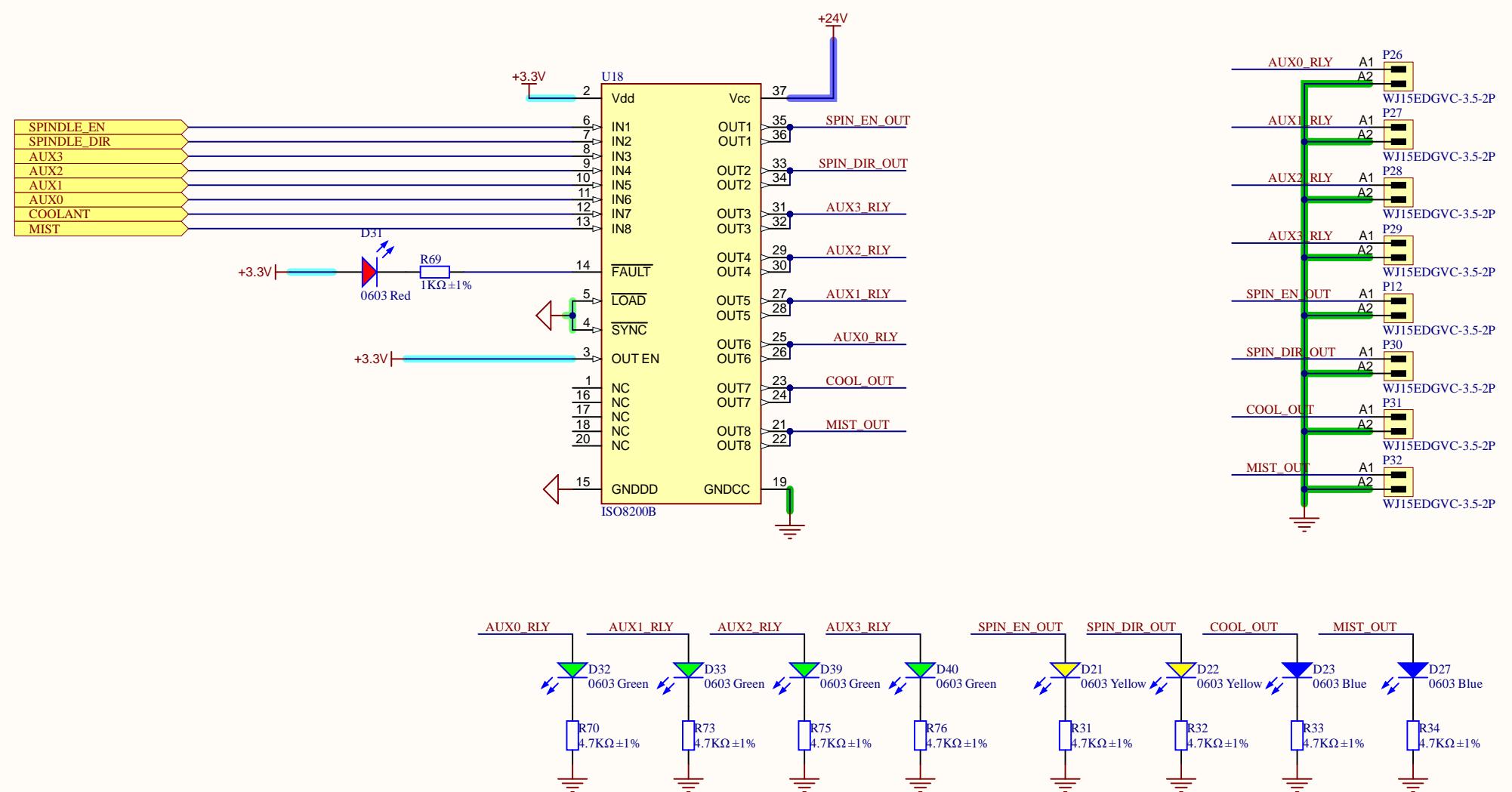


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SHEET External MCU		
PROJECT	REV	
FLEXI_HAL_PRO.PriPCB	A3	
ENGR	DOCUMENT	
AM	external_MCU.SchDoc	
SIZE	MODIFIED	SHEET
B	2025-04-08 5:52:30 PM	7 / 14



A

A

B

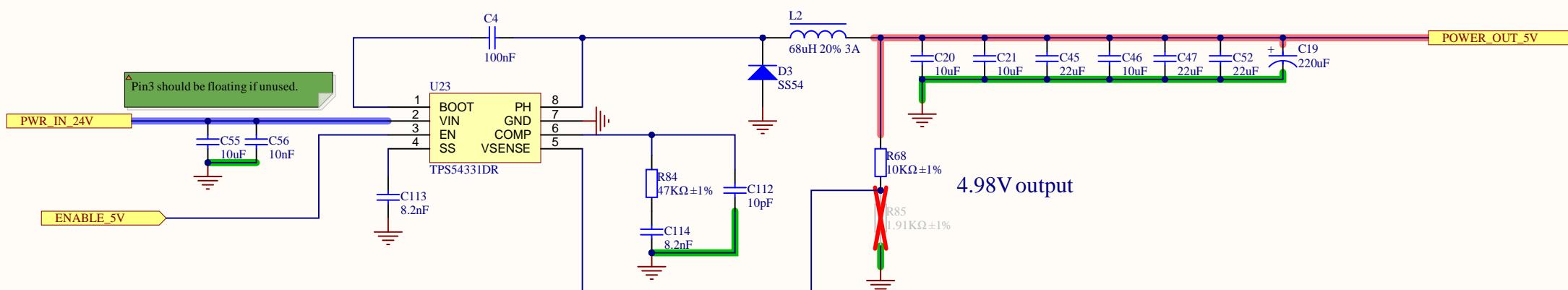
B

C

C

D

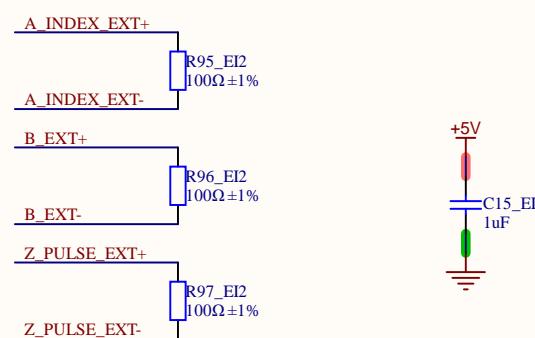
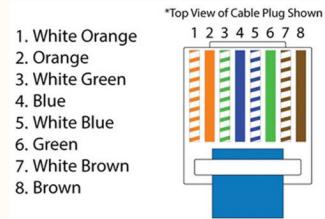
D



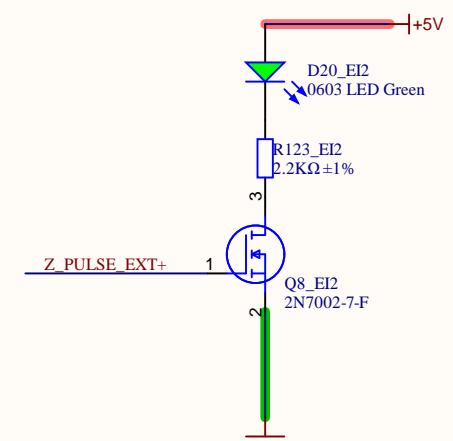
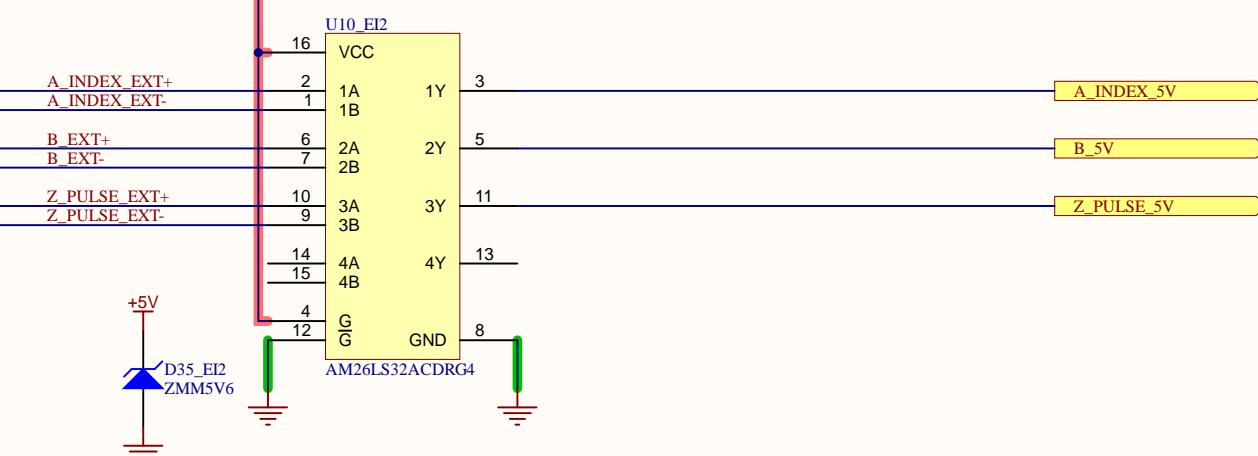
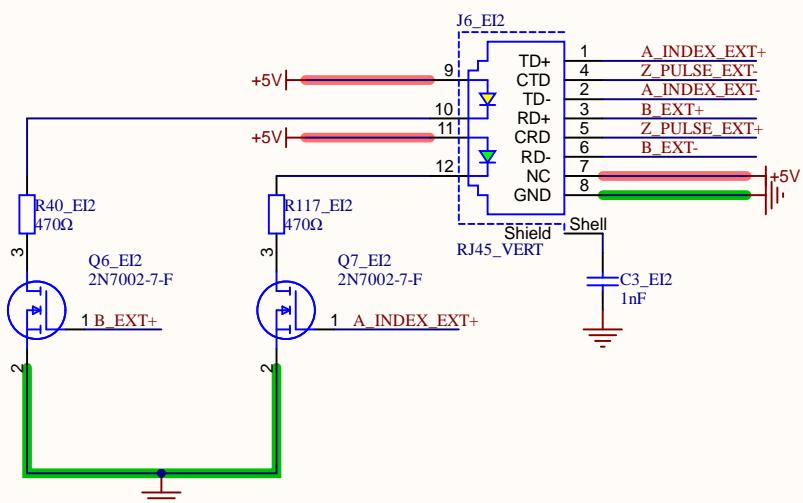
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PROJECT FLEXI\_HAL\_PRO.PjPCB REV A3  
ENGR AM DOCUMENT 5V\_Power\_Supply.SchDoc  
SIZE B MODIFIED 2025-04-08 5:52:30 PM SHEET 9 / 14  
EMAIL: INFO@EXPATRIA.CA



+5V  
C15\_EI2 1uF  
GND

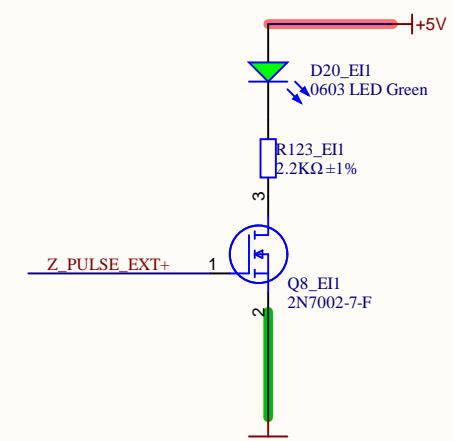
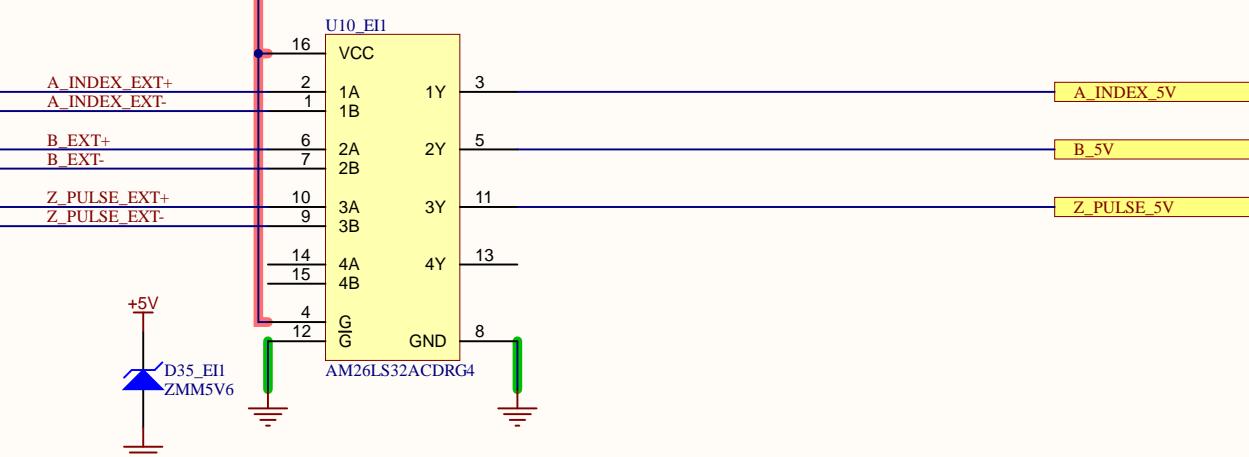
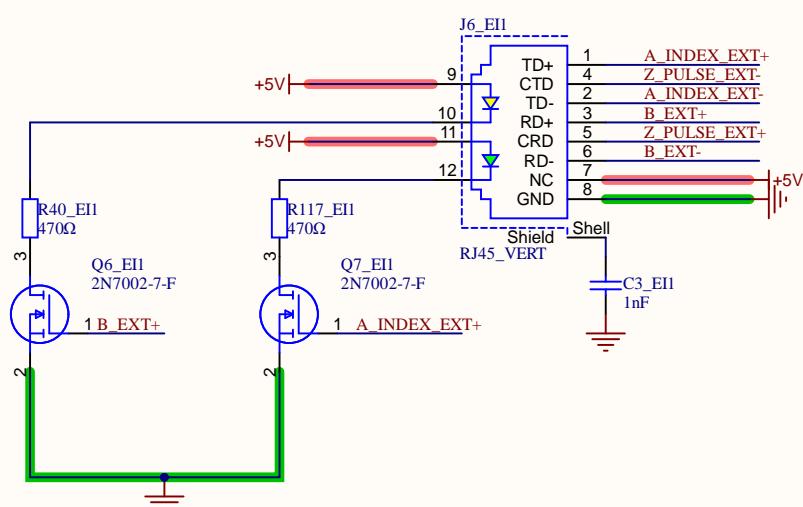
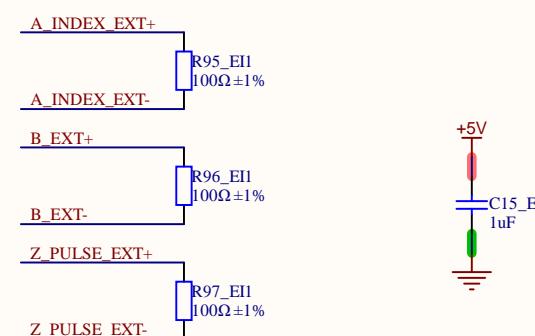
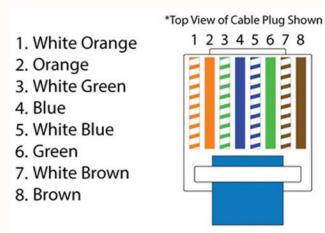


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Spindle_Sync	
PROJECT	REV
FLEXI_HAL_PRO.PriPCB	A1
ENGR	DOCUMENT
AM	Spindle_Sync.SchDoc
SIZE	MODIFIED
B	2025-04-08 5:52:30 PM
	11/ 14



A

A

B

B

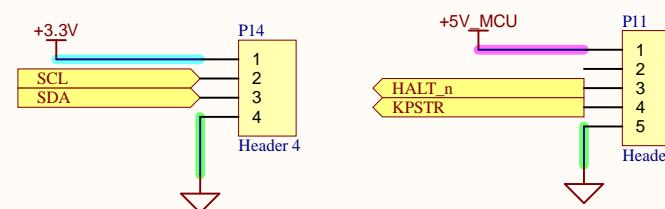
C

C

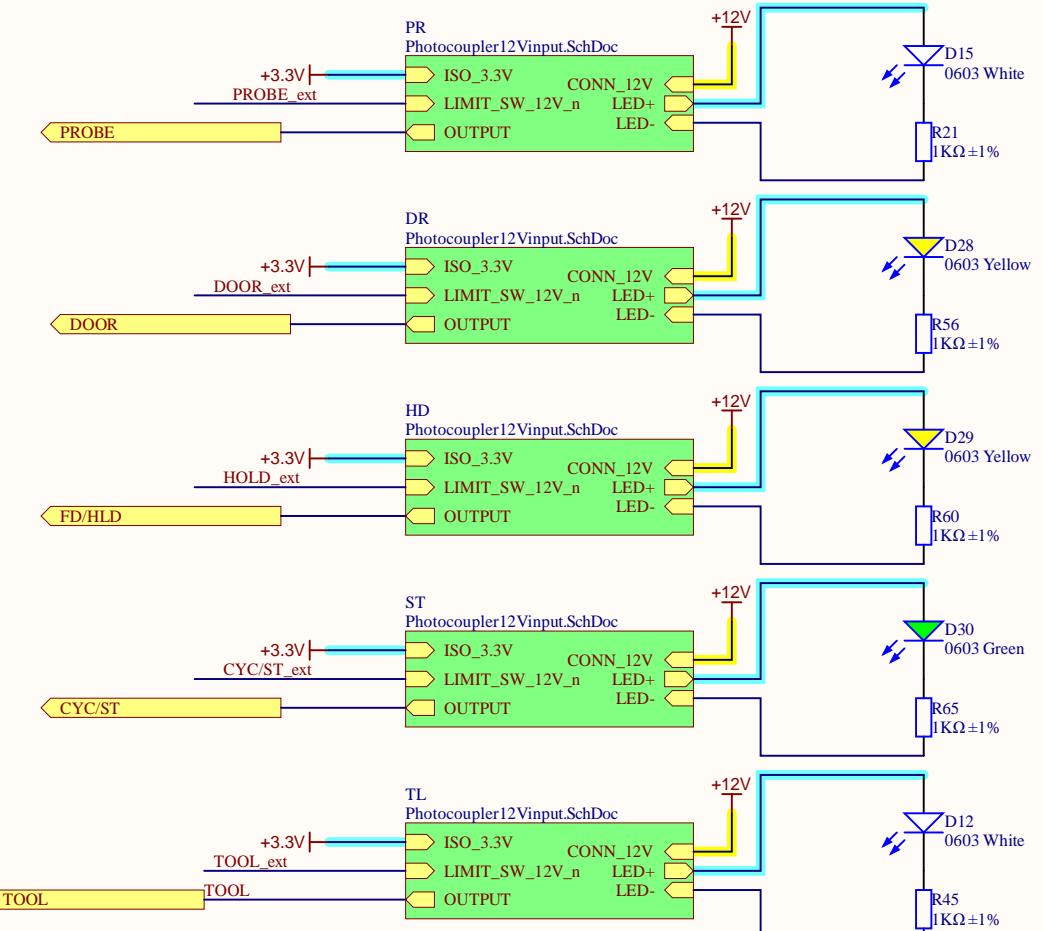
D

D

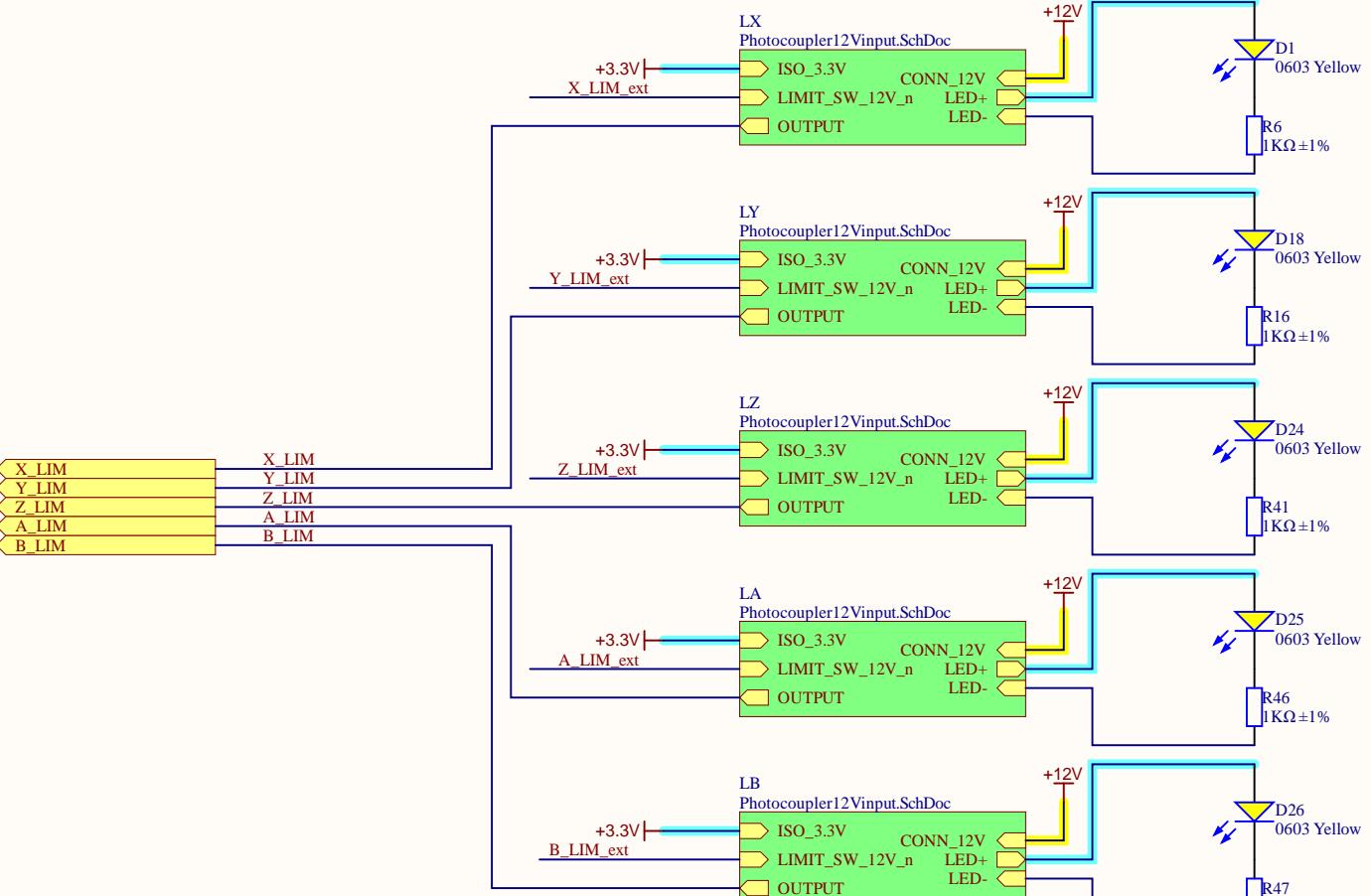
△ Pendant is in MCU domain  
HALT is active low.



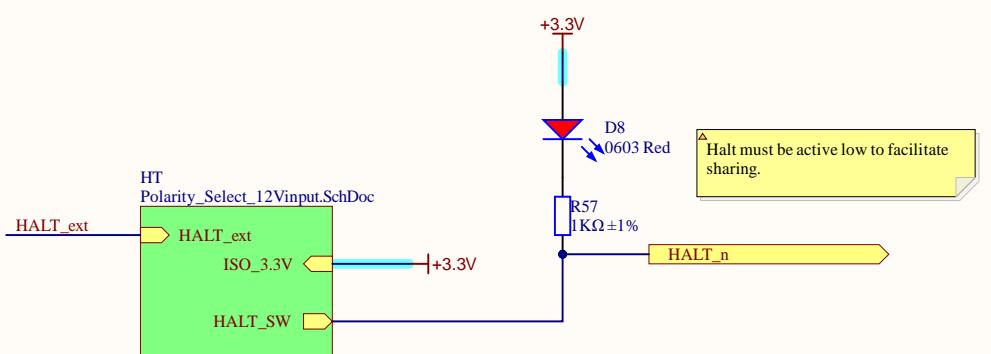
A



B



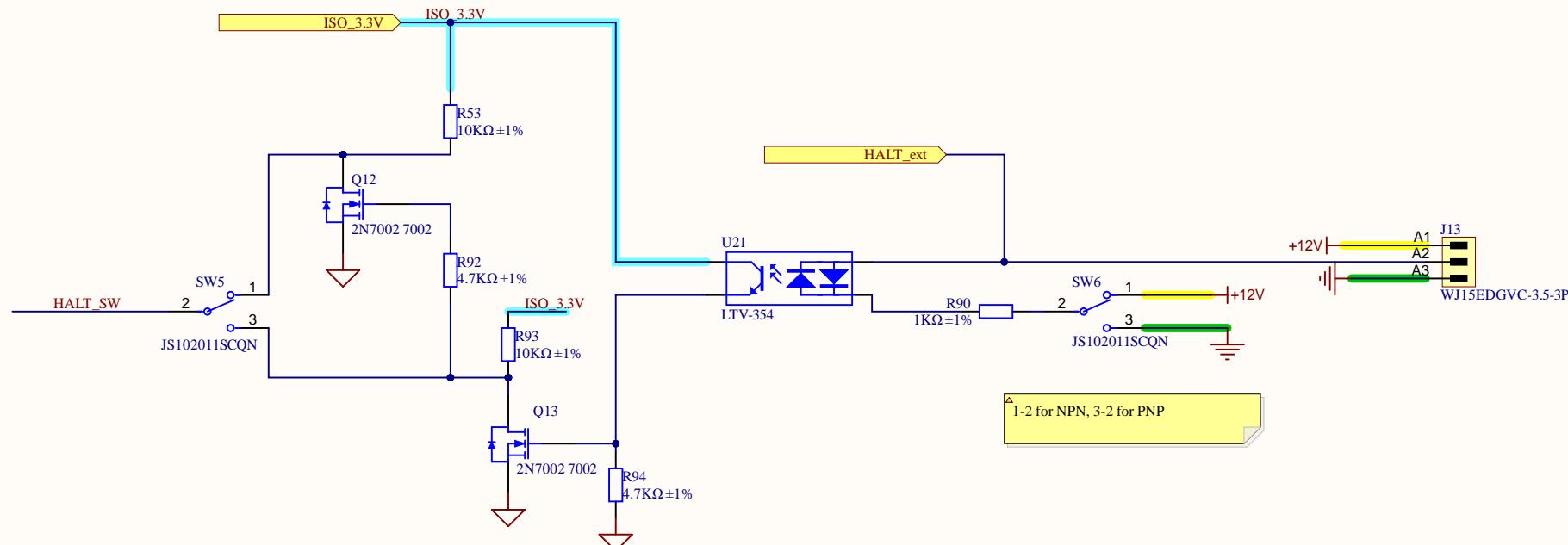
C



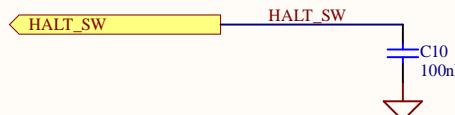
D

A

A



Signal pulled up here.



A

A

B

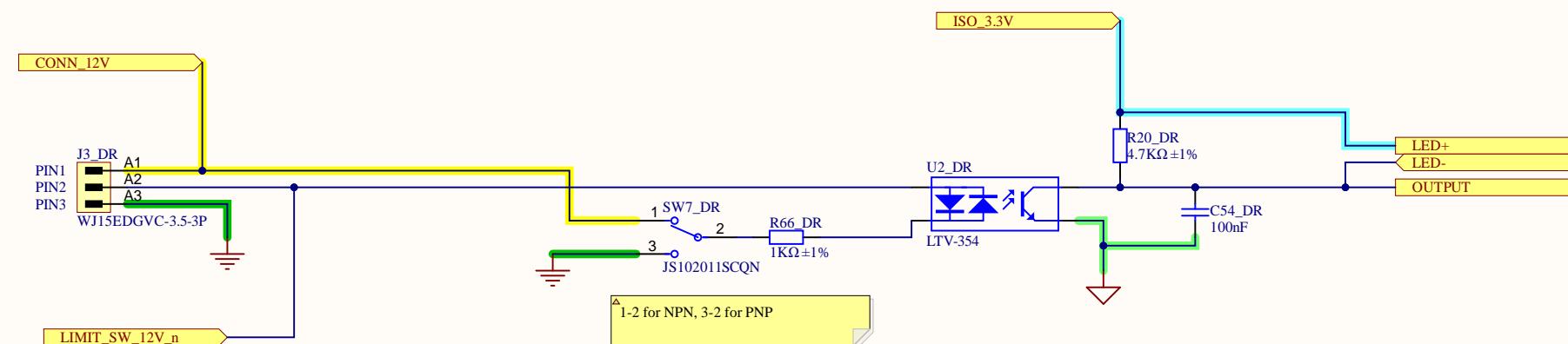
B

C

C

D

D



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SHEET Coupler 12V input		
PROJECT	REV	
FLEXI_HAL_PRO.PnjPCB	A3	
ENGR	DOCUMENT	
AM	Photocoupler12Vinput.SchDoc	
SIZE	MODIFIED	SHEET
B	2025-04-08 5:52:31 PM	14/ 14

A

A

B

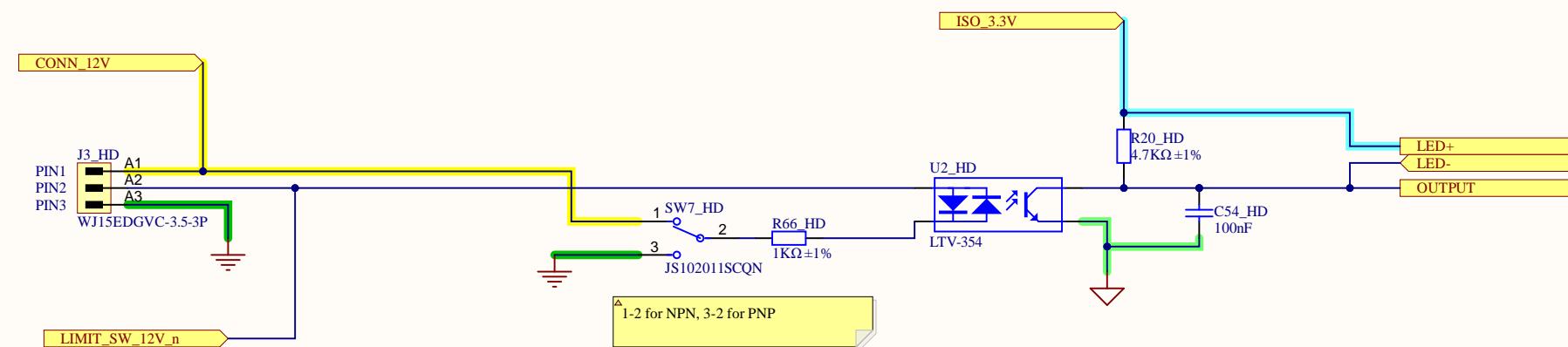
B

C

C

D

D



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SHEET Coupler 12V input		
PROJECT	REV	
FLEXI_HAL_PRO.PnjPCB	A3	
ENGR	DOCUMENT	
AM	Photocoupler12Vinput.SchDoc	
SIZE	MODIFIED	SHEET
B	2025-04-08 5:52:31 PM	14/ 14

A

A

B

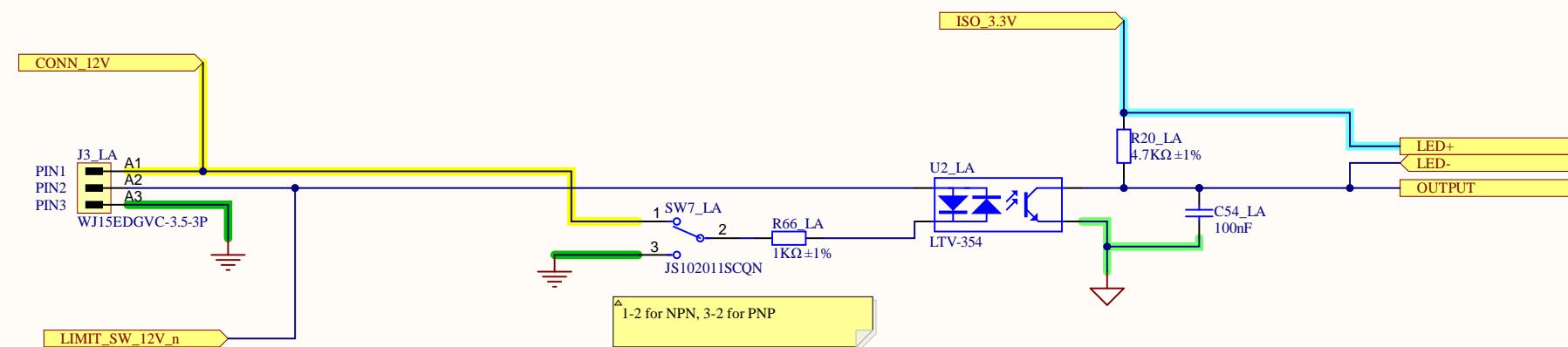
B

C

C

D

D



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SHEET Coupler 12V input		
PROJECT	REV	
FLEXI_HAL_PRO.PnjPCB	A3	
ENGR	DOCUMENT	
AM	Photocoupler12Vinput.SchDoc	
SIZE	MODIFIED	SHEET
B	2025-04-08 5:52:31 PM	14/ 14

A

A

B

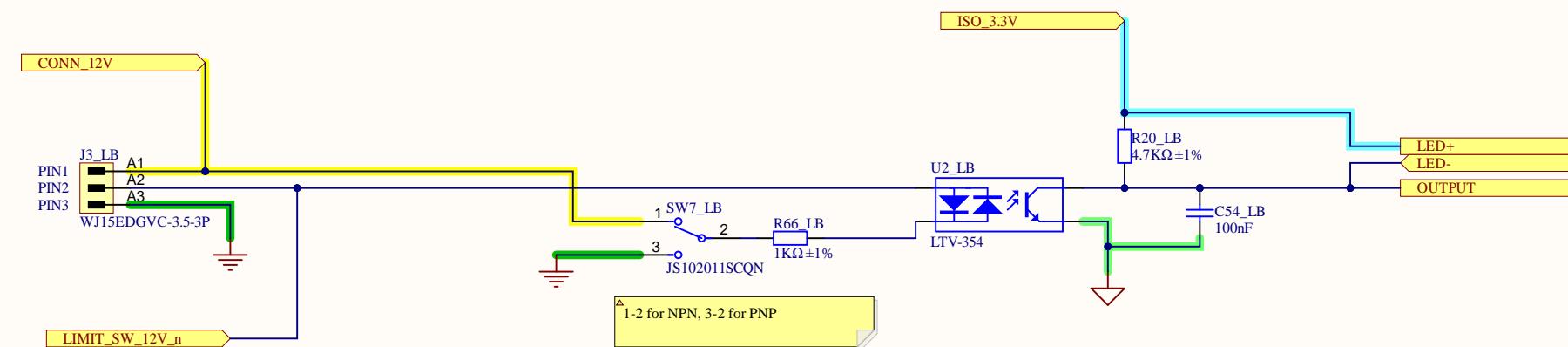
B

C

C

D

D

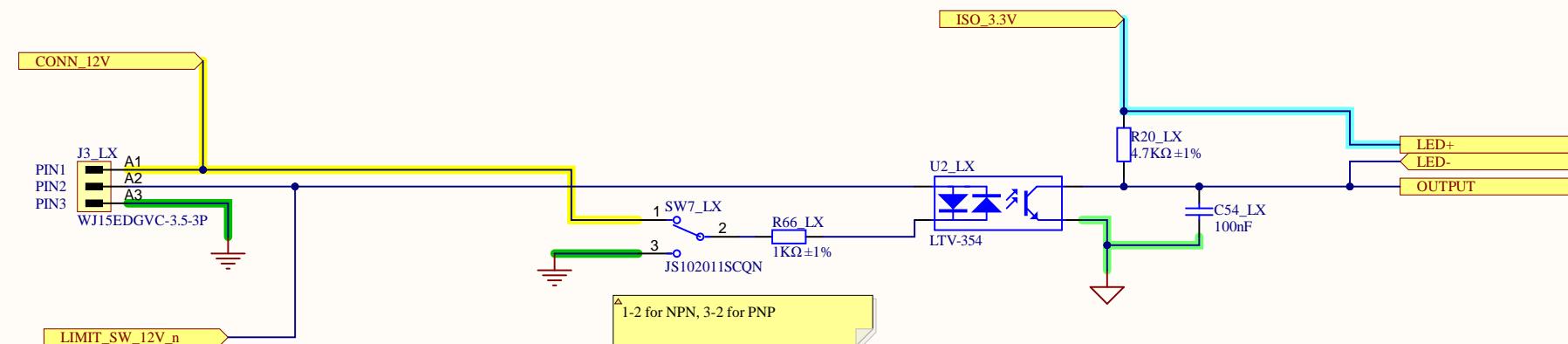


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SHEET Coupler 12V input		
PROJECT	REV	
FLEXI_HAL_PRO.PnjPCB	A3	
ENGR	DOCUMENT	
AM	Photocoupler12Vinput.SchDoc	
SIZE	MODIFIED	SHEET
B	2025-04-08 5:52:31 PM	14/ 14



A

A

B

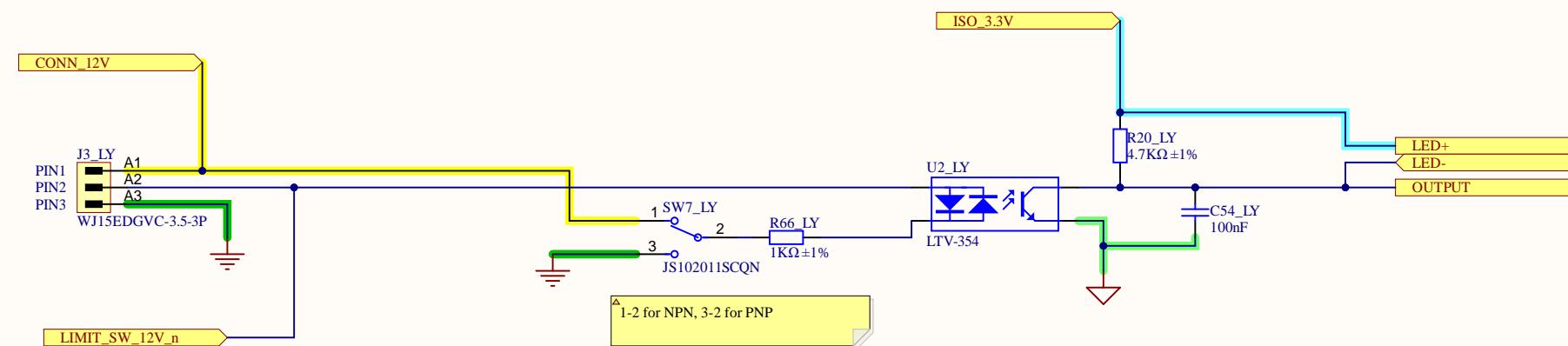
B

C

C

D

D



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SHEET Coupler 12V input		
PROJECT	REV	
FLEXI_HAL_PRO.PnjPCB	A3	
ENGR	DOCUMENT	
AM	Photocoupler12Vinput.SchDoc	
SIZE	MODIFIED	SHEET
B	2025-04-08 5:52:31 PM	14/ 14

A

A

B

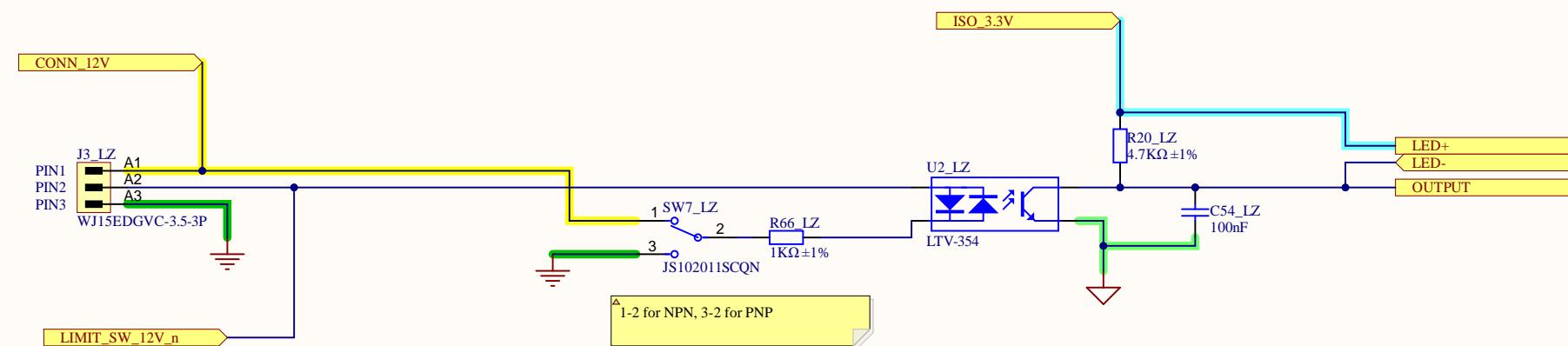
B

C

C

D

D



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SHEET Coupler 12V input		
PROJECT	REV	
FLEXI_HAL_PRO.PnjPCB	A3	
ENGR	DOCUMENT	
AM	Photocoupler12Vinput.SchDoc	
SIZE	MODIFIED	SHEET
B	2025-04-08 5:52:31 PM	14/ 14

A

A

B

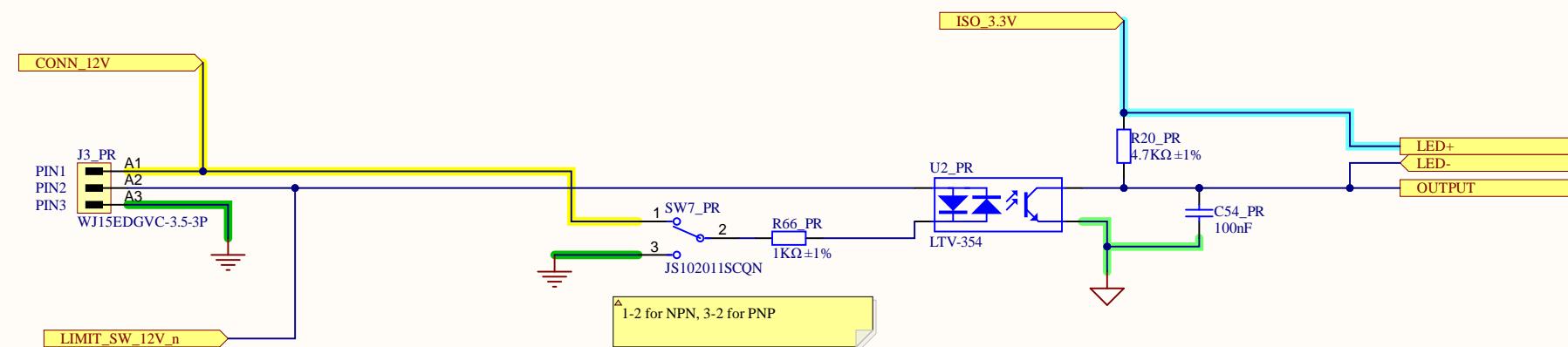
B

C

C

D

D

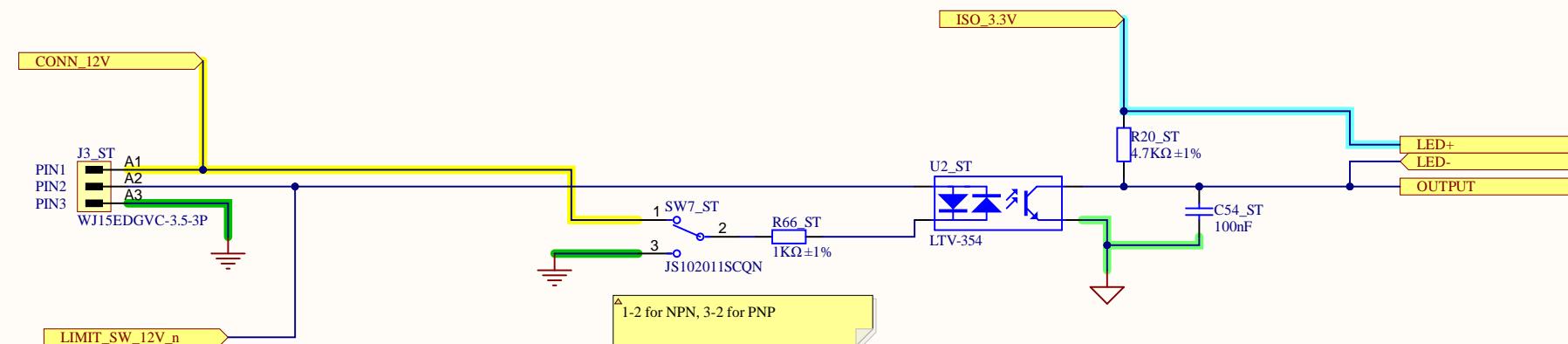


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SHEET Coupler 12V input		
PROJECT	REV	
FLEXI_HAL_PRO.PnjPCB	A3	
ENGR	DOCUMENT	
AM	Photocoupler12Vinput.SchDoc	
SIZE	MODIFIED	SHEET
B	2025-04-08 5:52:31 PM	14/ 14



A

A

B

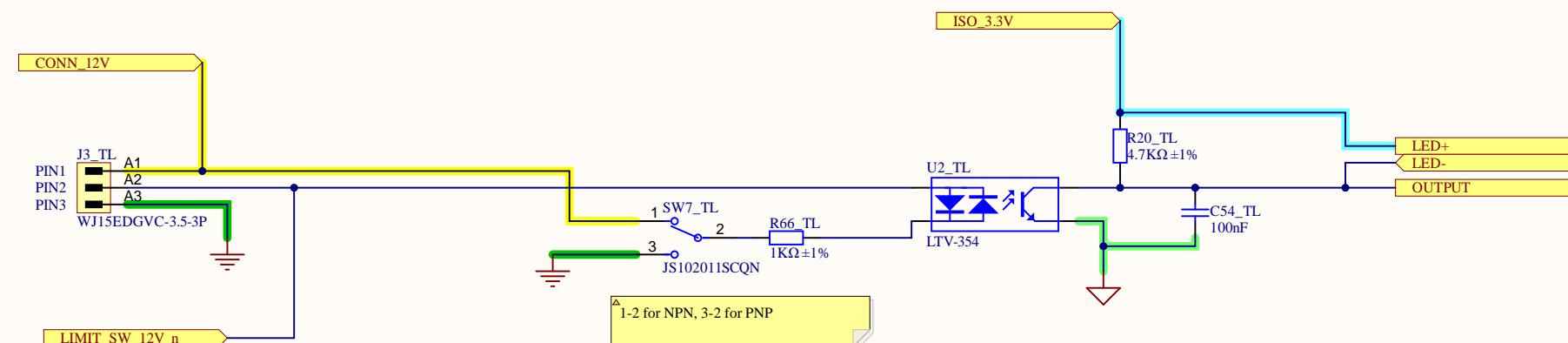
B

C

C

D

D



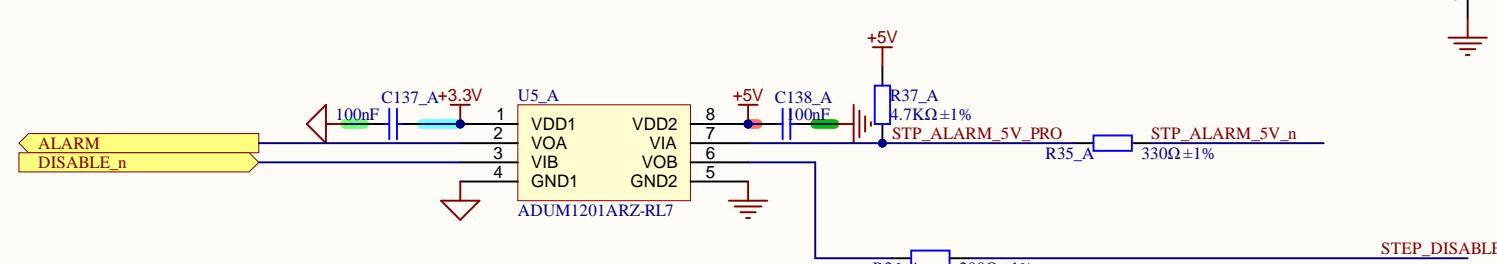
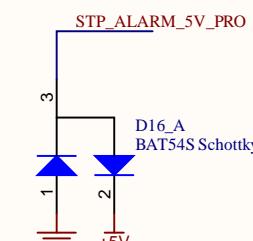
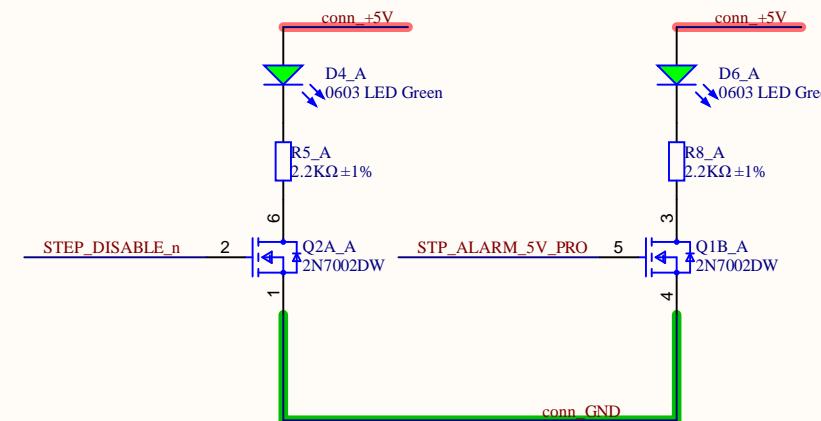
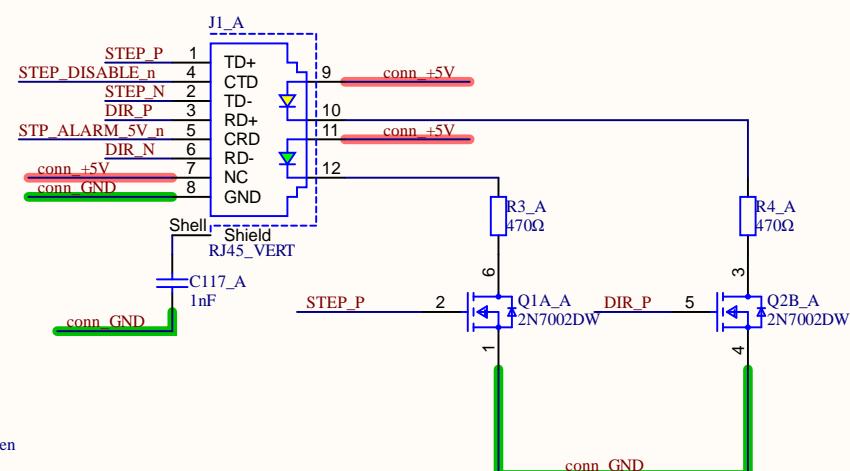
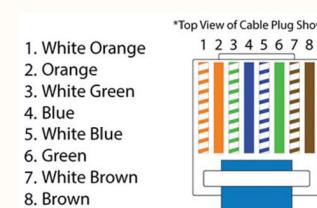
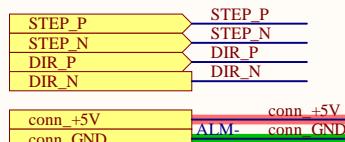
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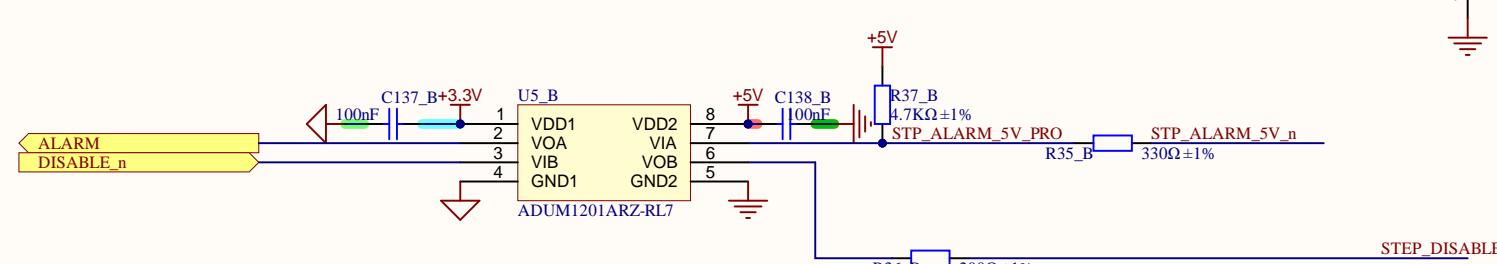
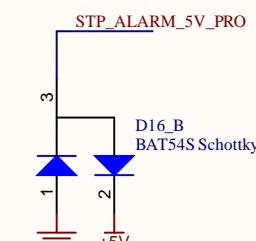
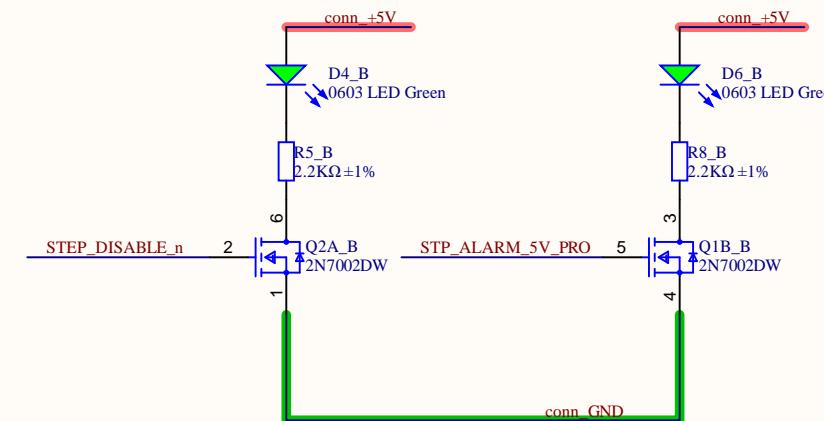
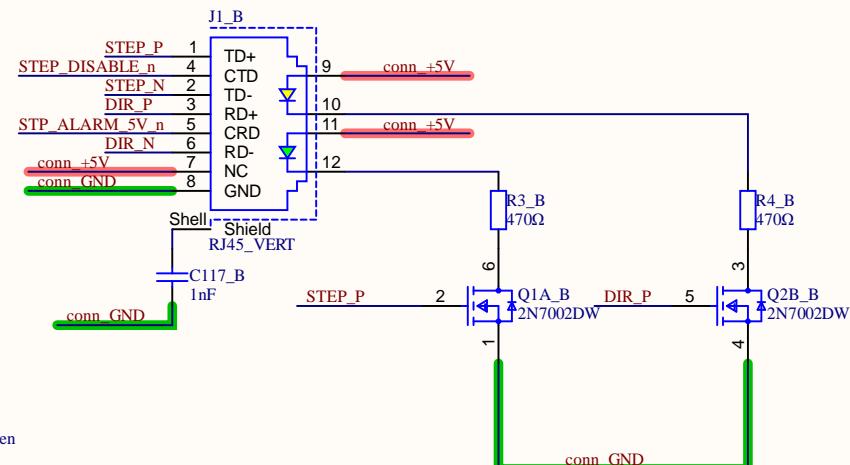
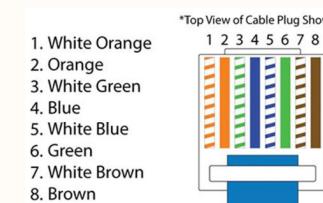
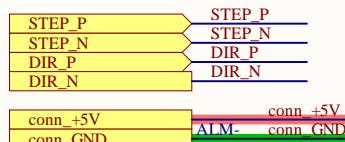
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SHEET Coupler 12V input		
PROJECT	REV	
FLEXI_HAL_PRO.PnjPCB	A3	
ENGR	DOCUMENT	
AM	Photocoupler12Vinput.SchDoc	
SIZE	MODIFIED	SHEET
B	2025-04-08 5:52:32 PM	14/ 14

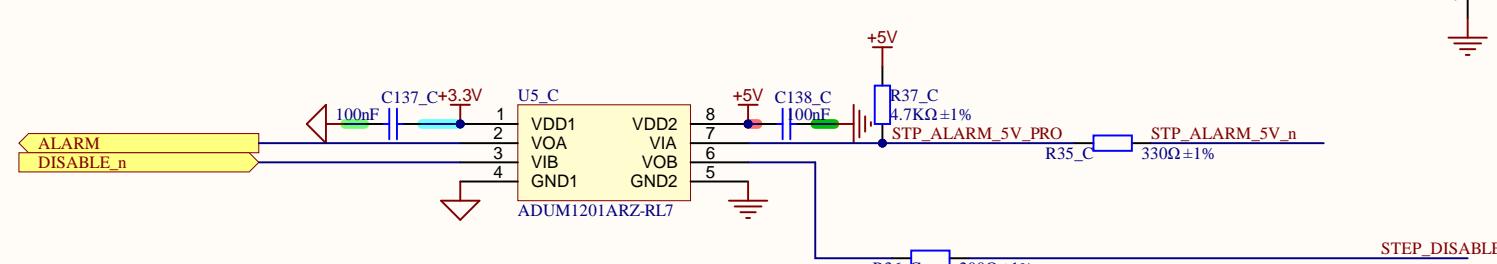
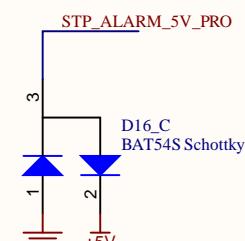
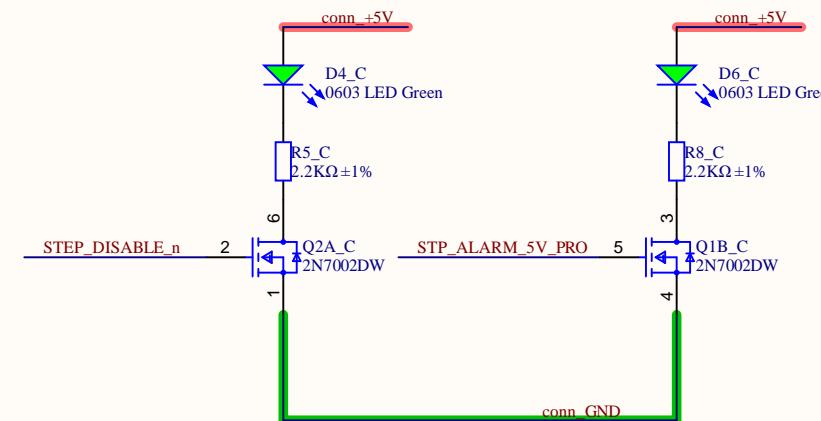
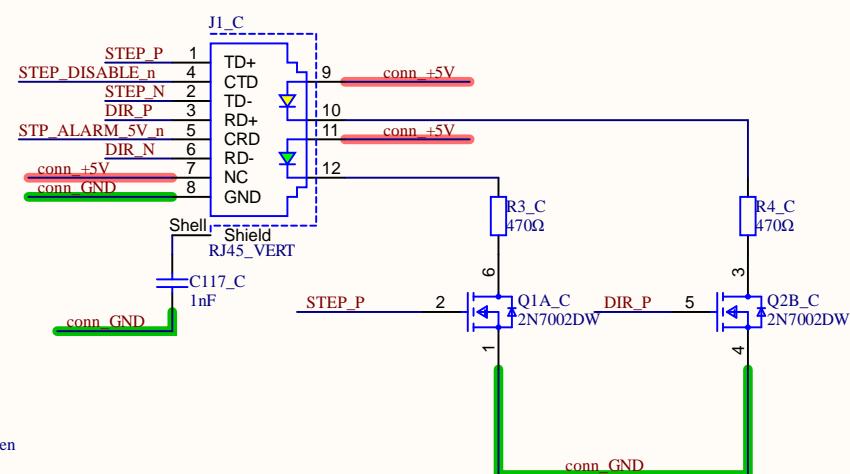
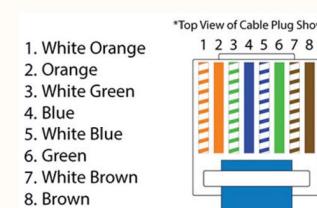
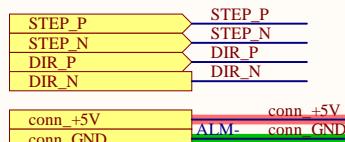
A



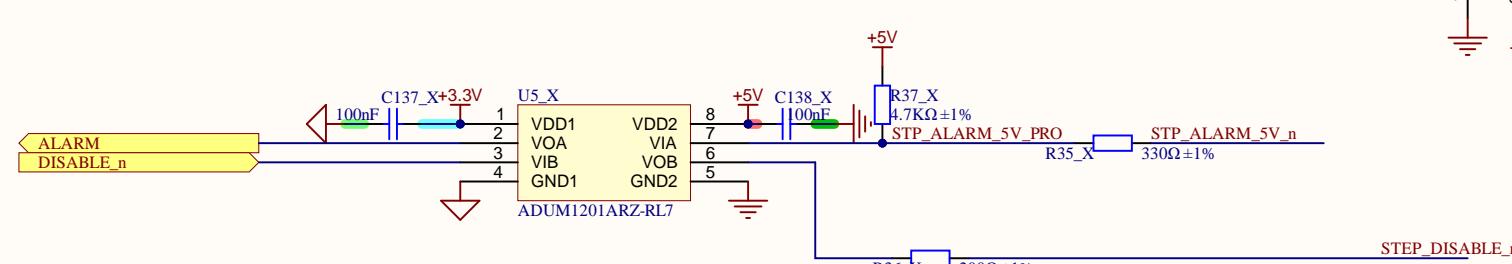
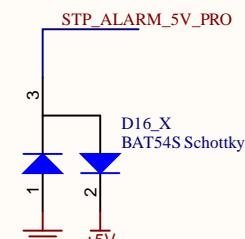
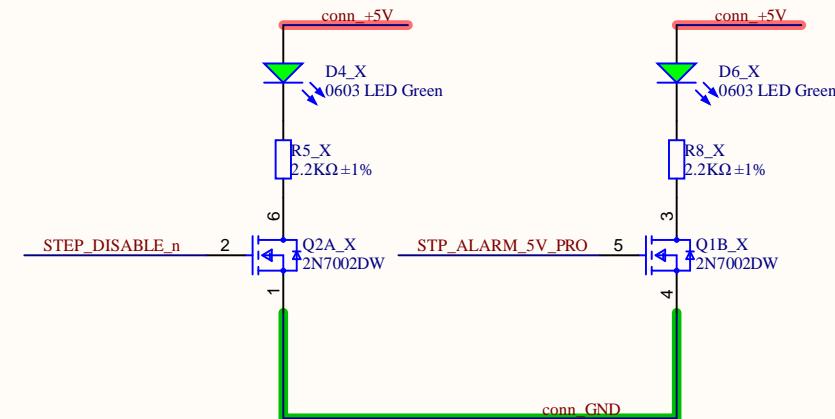
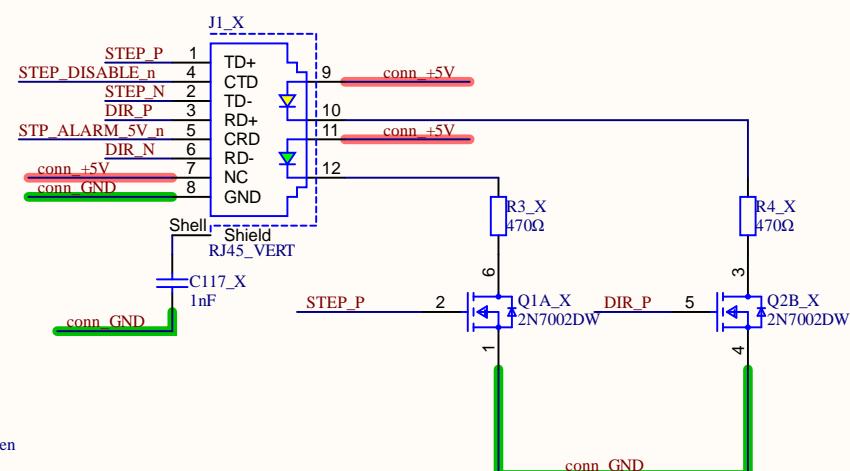
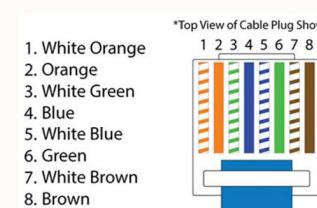
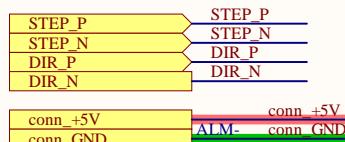
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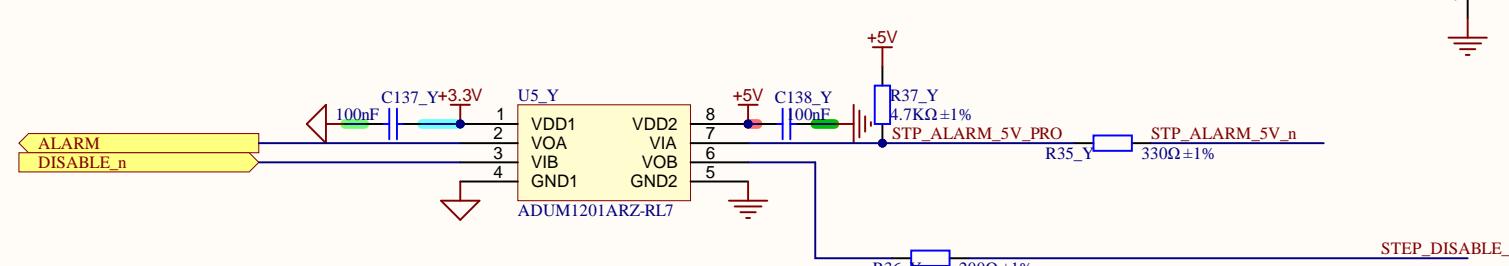
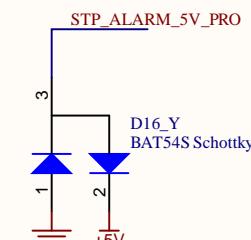
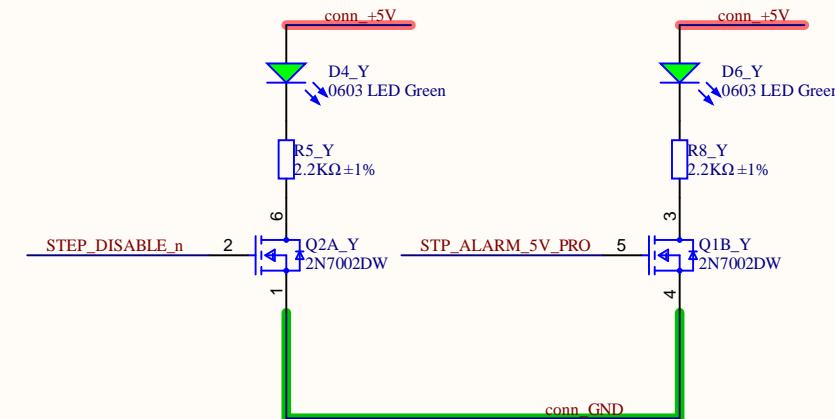
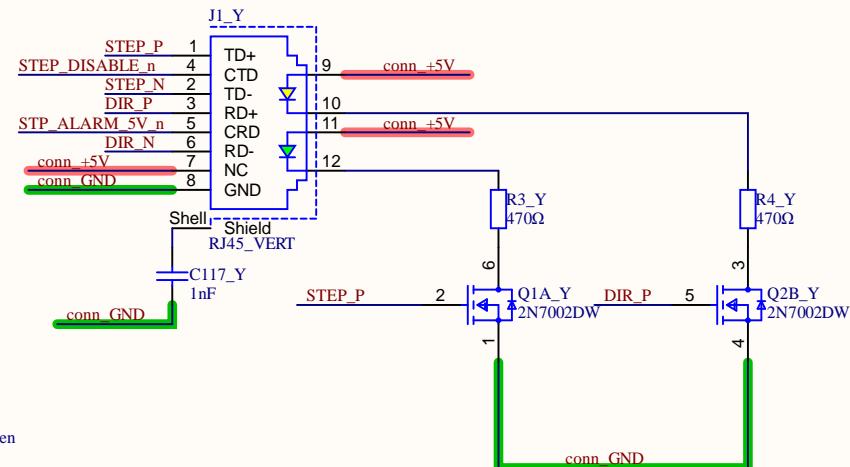
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